IEI Integration Corp.

MODEL:

IMB-ADL-H610

Micro ATX Motherboard Supports LGA1700 12th/13th Gen. Intel® Core™ i9/i7/i5/i3, Pentium® and Celeron® Processor, DDR4, Triple Independent Display, Dual GbE LAN, USB 3.2, SATA 6Gb/s, and RoHS

User Manual

Rev. 1.00 – June 21, 2023

RoHS Lead Free

Revision

Date	Version	Changes
June 21, 2023	1.00	Initial release

Copyright

Manual Conventions

WARNING

Warnings appear where overlooked details may cause damage to the equipment or result in personal injury. Warnings should be taken seriously.

CAUTION

Cautionary messages should be heeded to help reduce the chance of losing data or damaging the product.

NOTE

These messages inform the reader of essential but non-critical information. These messages should be read carefully as any directions or instructions contained therein can help avoid making mistakes.

Table Of Contents

List of Figures

List of Tables

Chapter

1

Introduction

1.1 Introduction

Figure 1-1: IMB-ADL-H610

The IMB-ADL-H610 is a micro ATX motherboard. It accepts a Socket LGA1700 Intel® 12/13th Generation Core™ i9/i7/i5/i3, Pentium® and Celeron® processor and supports two 288-pin 3200 MHz dual-channel DDR4 SDRAM DIMM modules up to 64 GB. The integrated Intel® H610 chipset supports four SATA 6Gb/s drives. Moreover, the IMB-ADL-H610 includes DP, HDMI and iDPM interfaces for triple independent display.

The IMB-ADL-H610 provides one 2.5GbE interfaces through the Intel® I225V controller and one 1GbE through Intel® i219 LM controller. Expansion include one PCIe x16 slot, one PCIe x4 slot, two PCIe x1 slots and one M.2 M-key 2280 supports NVMe for installing function cards like Graphic cards. It also includes two RS-232/422/485, two USB 3.2 Gen 1, two USB 3.2 Gen 2 and four USB 2.0 on the rear panel; two USB 2.0 and four RS-232 via internal pin headers; four SATA 6Gb/s for storage device connection.

1.2 Features

Some of the IMB-ADL-H610 motherboard features are listed below:

- Micro ATX form factor
- 12th/13th generation LGA1700 Intel® Core™ i9/i7/i5/i3, Pentium® and Celeron® processor supported
- Intel® H610/H610E chipset
- Two 288-pin 3200 MHz dual-channel unbuffered DDR4 SDRAM DIMM slots support up to 64 GB memory
- One Intel® I225V 2.5GbE controller and Intel® i219 LM controller
- Supports PCI Express Generation 5.0
- Triple independent display by DP, HDMI and iDPM interfaces
- Four SATA 6Gb/s connectors
- Two USB 3.2 Gen 2 ports and two USB 3.2 Gen 1 ports on the rear panel
- One M.2 M-key slot (PCIe x1) supports NVMe
- One PCIe x16 slot
- One PCIe x4 slot
- Two PCIe x1 slots
- Six serial ports (two on rear panel，the others via internal pin header)
- The optional expansion cards provide more choices to meet user's demand
- TPM 2.0 security function supported by PTT (Plate form Trust Technology) based on BIOS setting
- High Definition Audio
- RoHS compliant

1.3 Connectors

The connectors on the IMB-ADL-H610 are shown in the figure below.
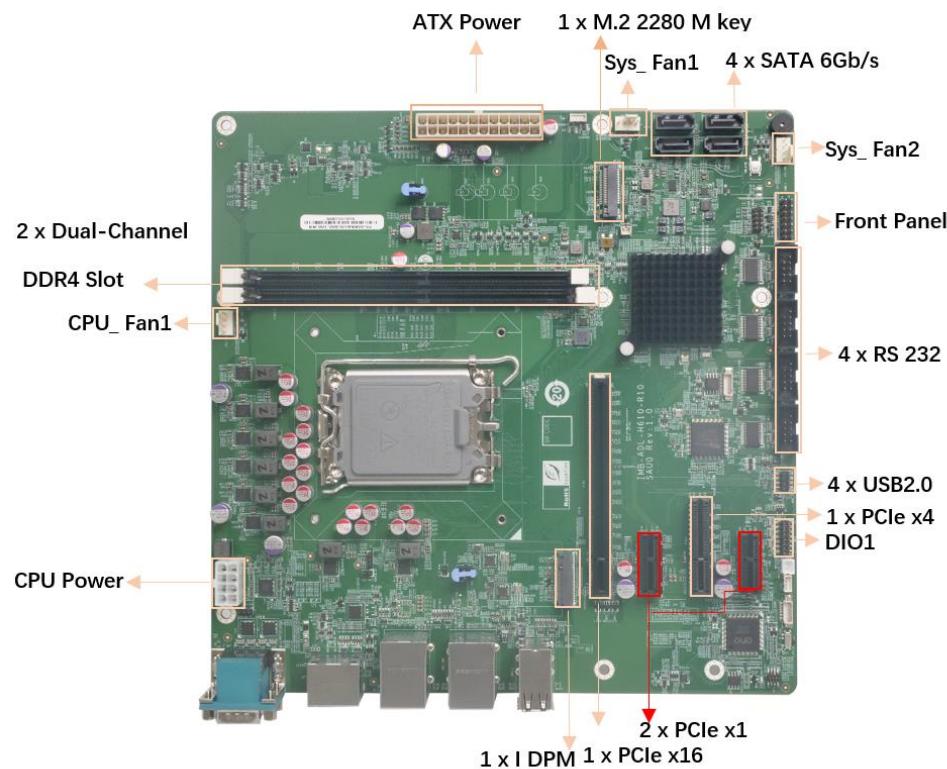
Figure 1-2: Connectors

1.4 Dimensions

The main dimensions of the IMB-ADL-H610 are shown in the diagram below.
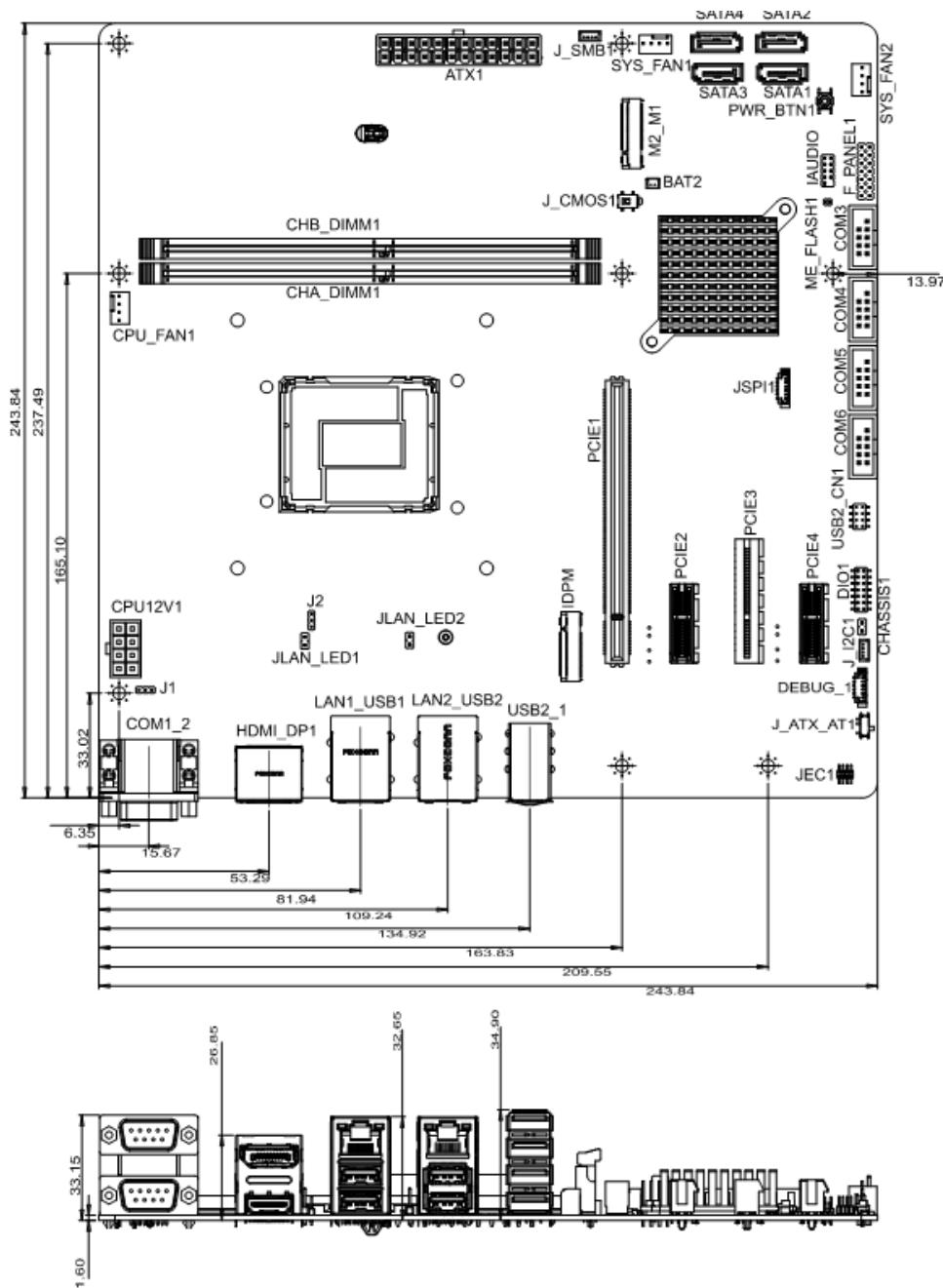
Figure 1-3: IMB-ADL-H610 Dimensions (mm)

1.5 Data Flow

Figure 1-4 shows the data flow between the system chipset, the CPU and other components installed on the motherboard.
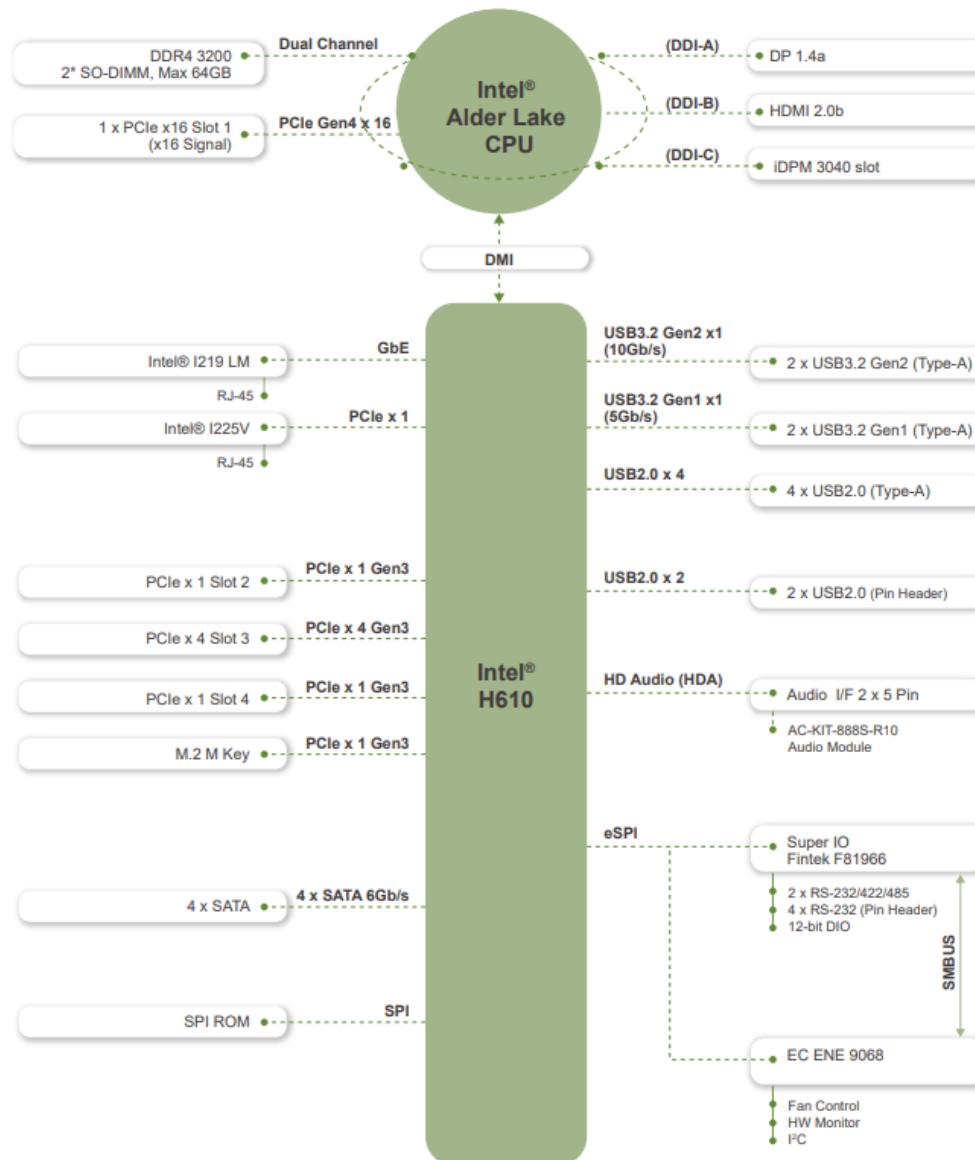
Figure 1-4: Data Flow Diagram

1.6 Technical Specifications

The IMB-ADL-H610 technical specifications are listed below.

Specification/Model	IMB-ADL-H610
Form Factor	Micro ATX
CPU Supported	12th/13th generation LGA1700 Intel® Core™ i9/i7/i5/i3, Pentium® and Celeron® CPU
Chipset	Intel® H610/H610E
Memory	Two 288-pin 3200 MHz dual-channel unbuffered DDR4 SDRAM DIMMs supported (system max. 64 GB)
Graphics Engine	Up to Intel® UHD Graphics 770;
Display Output	Triple independent display One DP1.4a (up to 4096x2304@60Hz) One HDMI2.0a (up to 4096x2304@30Hz) One IEI iDPM slot (only for IEI eDP/ LVDS/ VGA module)
Ethernet Controllers	**LAN1**: Intel® I219 LM controller **LAN2**: Intel® I225V 2.5GbE controller
Audio	Support 7.1 channel HD audio by IEI AC-KIT-888S kit module 1 x Analog audio (2x5 pin, p=2.0)
BIOS	AMI UEFI BIOS
Super I/O Controller	Fintek F81966
Watchdog Timer	Software programmable supports 1~255 sec. system reset
Expansions	1 x PCIe x16 slot 1 x PCIe x4 slot 2 x PCIe x1 slots 1 x M.2 M-key 2280 (PCIe x1, support NVMe SSD)
I/O Interface Connectors	
Audio Connectors	One internal front panel audio connector (10-pin header)
Chassis Intrusion	One 2-pin header

Digital I/O	One 12-bit digital I/O
Ethernet	Two RJ-45 GbE ports
Fan	One 4-pin CPU smart fan connector Two 4-pin system smart fan connectors
Front Panel	One 14-pin header (power LED, HDD LED, speaker, power button, reset button)
I²C	One 4-pin wafer connector
LAN LED	Two 2-pin header for LAN1 LED and LAN2 LED
Serial ATA	Four SATA 6Gb/s connectors
Serial Ports	Two RS-232/422/485 via DB-9 (RS-485 support AFC) 4 x RS-232 via internal box header
SMBus	One 4-pin wafer connector
USB Ports	Two USB 3.2 Gen 2(10Gb/s) ports (Type A) on rear panel Two USB 3.2 Gen 1(5Gb/s) ports (Type A) on rear panel Four USB 2.0 ports (Type A) on rear panel Two USB 2.0 ports via internal pin header
Environmental and Power Specifications	
Power Supply	AT/ATX power supply Support AT/ATX mode ErP/EuP compliant
Power Consumption	3.3V@0.35A, 5V@7.54A, 12V@5.75A, 5VSB@0.96A (Intel® Core™ i9-12900E CPU with 8 GB 3200 MHz DDR4 memory, EuP mode enabled)
Operating Temperature	-0ºC ~ 60ºC
Storage Temperature	-30ºC ~ 70ºC
Operating Humidity	5% ~ 95% (non-condensing)
Physical Specifications	

Dimensions	244 mm x 244 mm
Weight (GW/NW)	1200 g/700 g

Table 1-1: IMB-ADL-H610 Specifications

Chapter

2

Packing List

2.1 Anti-static Precautions

 WARNING!

Static electricity can destroy certain electronics. Make sure to follow the ESD precautions to prevent damage to the product, and injury to the user.

Make sure to adhere to the following guidelines:

- *Wear an anti-static wristband*: Wearing an anti-static wristband can prevent electrostatic discharge.
- *Self-grounding*: Touch a grounded conductor every few minutes to discharge any excess static buildup.
- *Use an anti-static pad*: When configuring any circuit board, place it on an anti-static mat.
- *Only handle the edges of the PCB*: Don't touch the surface of the motherboard. Hold the motherboard by the edges when handling.

2.2 Unpacking Precautions

When the IMB-ADL-H610 is unpacked, please do the following:

- Follow the anti-static guidelines above.
- Make sure the packing box is facing upwards when opening.
- Make sure all the packing list items are present.

2.3 Packing List

  NOTE:

If any of the components listed in the checklist below are missing, do not proceed with the installation. Contact the IEI reseller or vendor the IMB-ADL-H610 was purchased from or contact an IEI sales representative directly by sending an email to sales@ieiworld.com.

The IMB-ADL-H610 is shipped with the following components:

Quantity	Item and Part Number	Image
1	IMB-ADL-H610 single board computer	
2	SATA cable	
1	I/O shielding	
1	Quick installation guide	

Table 2-1: Packing List

2.4 Optional Items

The following are optional components which may be separately purchased:

Item and Part Number	Image
Dual port USB cable with bracket, 300mm, P=2.54 (**P/N**: 19800-003100-100-RS)	
Realtek ALC888S 7.1 Channel HD Audio peripheral board, RoHS (**P/N**: AC-KIT-888S-R10)	
SATA power cable, MOLEX 5264-4P to SATA15P (**P/N**: 32102-000100-200-RS)	
RS-232/422/485 cable, 230mm, P=2.54 (**P/N**: 32205-000702-200-RS)	
Low power LGA1700 Cooler kit for Intel® Core™ i9/i7/i5/i3 CPU, 72*70*25.5,65W, CCL; RoHS (**P/N**: 19100-000323-00-RS)	
eDP to eDP DisplayPort converter board (for IEI iDPM connector) (**P/N**: iDPM-eDP-R10)	
eDP to LVDS DisplayPort converter board (for IEI iDPM connector) (**P/N**: iDPM-LVDS-R10)	

Table 2-2: Optional Items

Chapter

3

Connectors

3.1 Peripheral Interface Connectors

This chapter details all the peripheral interface connectors.

3.1.1 IMB-ADL-H610 Layout

The figures below show all the peripheral interface connectors.
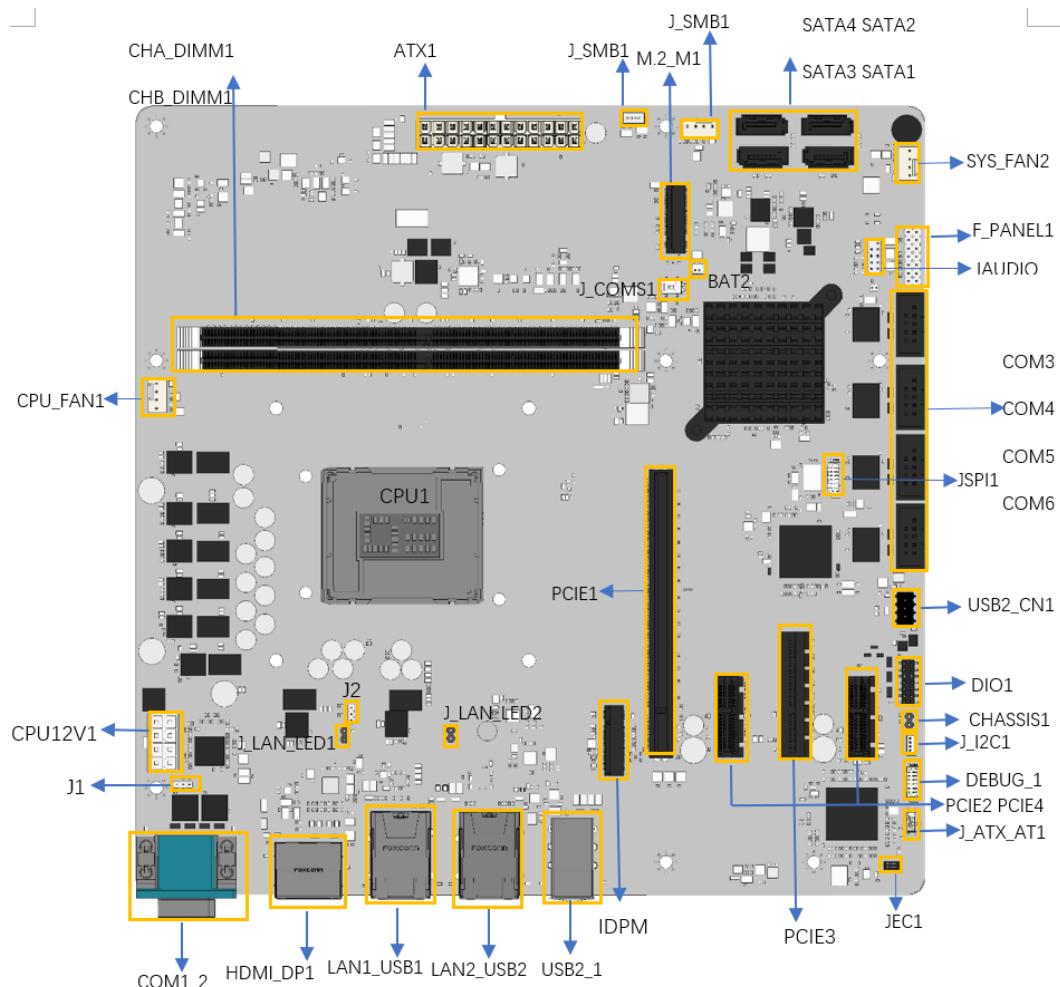
Figure 3-1: Peripheral Interface Connectors

3.1.2 Peripheral Interface Connectors

The table below lists all the connectors on the board.

Connector	Type	Label
AT/ATX power mode setting	3-pin Molex power connector	J_ATX_AT1
Clear CMOS jumper	4-pin Push button	J_CMOS1
Flash descriptor security override jumper	2-pin header	ME_FLASH1
Audio connector for IEI AC-KIT-888S kit	10-pin header	IAUDIO
ATX power connector	24-pin connector	ATX1
ATX CPU 12V power connector	8-pin connector	CPU12V1
RTC battery connector	2-pin connector	BAT2
Chassis intrusion connector	2-pin connector	CHASSIS1
DDR4 DIMM slots	288-pin DDR4 SO-DIMM socket	CHA_DIMM1, CHB_DIMM1
Digital I/O connector	14-pin connector	DIO1
EC debug connector	6-pin connector	DEBUG_1
Fan connectors	4-pin wafer	CPU_FAN1, SYS_FAN1, SYS_FAN2
Front panel connector	14-pin header	F_PANEL1
I2C connector	4-pin wafer	J_I2C1
LAN1 link LED connector	2-pin header	JLAN_LED1
LAN2 link LED connector	2-pin header	JLAN_LED2
RS-232 serial port connectors	10-pin header	COM3, COM4, COM5, COM6
RAA229134 FW flash connector	3-pin header	J1
LT86101X FW flash connector	3-pin header	J2

Connector	Type	Label
SATA 6Gb/s connectors	8-pin connector	SATA1, SATA2, SATA3, SATA4
SMBus connector	4-pin wafer	J_SMB1
Flash SPI ROM connector	6-pin wafer	JSPI1
Flash EC ROM connector	8-pin header	JEC1
Internal USB 2.0 connector	8-pin header	USB2_CN1
IEI iDPM slot	iDPM connector	IDPM
PCIe x1 slots	PCIe x1 slots	PCIE2, PCIE4
PCIe x4 slots	PCIe x4 slots	PCIE3
PCIe x16 slots	PCIe x16 slots	PCIE1
Onboard power button	4-pin Push button	PWR_BTN1

Table 3-1: Peripheral Interface Connectors

3.1.3 External Interface Panel Connectors

The table below lists the connectors on the external I/O panel.

Connector	Type	Label
External HDMI and DP combo connector	DisplayPort, HDMI	HDMI_DP1
External dual RS-232/422/485 connector	DB-9 male	COM1_2
External 1GbE RJ-45 and dual USB 3.2 Gen 1 combo connector	RJ-45 & USB 3.2 Gen 1 combo	LAN1_USB1
External 2.5GbE RJ-45 and dual USB 3.2 Gen 2 combo connector	RJ-45 & USB 3.2 Gen 2 combo	LAN2_USB2
External quad USB 2.0 connector	USB 2.0 combo	USB2_1

Table 3-2: Rear Panel Connectors

3.2 Internal Peripheral Connectors

The section describes all of the connectors on the IMB-ADL-H610.

3.2.1 CPU 12V Power Connector

CN Label: **CPU12V1**

CN Type: 8-pin Molex power connector, P=4.2mm

CN Location: See **Figure 3-2**

CN Pinouts: See **Table 3-3**

This connector provides power to the CPU.
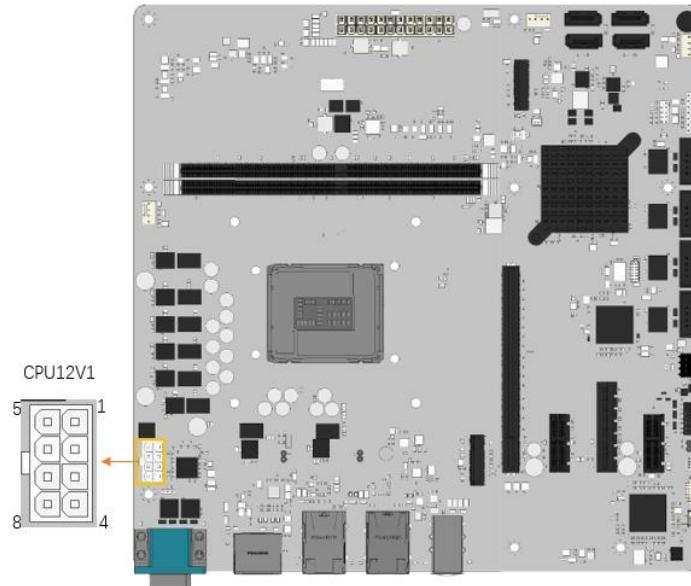
Figure 3-2: ATX CPU 12V Power Connector Location

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	GND	5	+12V
2	GND	6	+12V
3	GND	7	+12V
4	GND	8	+12V

Table 3-3: ATX CPU 12V Power Connector Pinouts

3.2.2 ATX Power Connector

CN Label: **ATX1**

CN Type: 24-pin connector, p=4.2 mm

CN Location: See **Figure 3-3**

CN Pinouts: See **Table 3-4**

The ATX power connector connects to an ATX power supply.
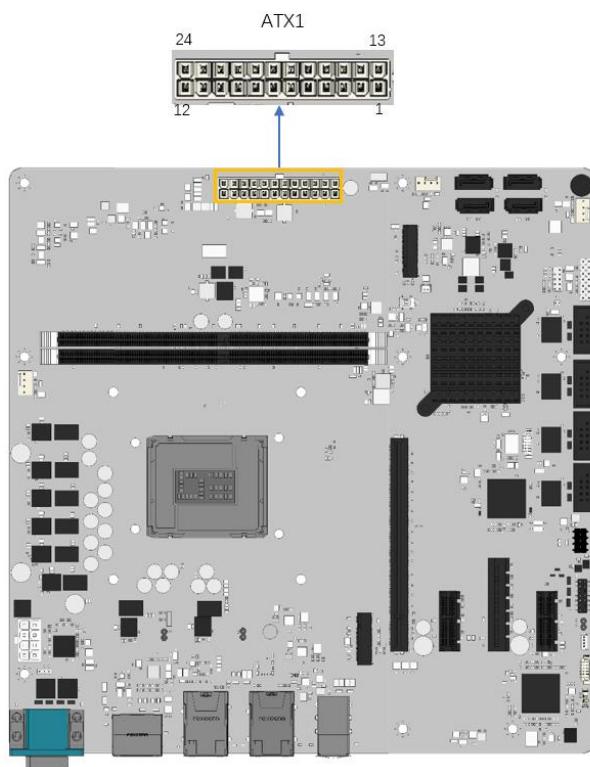
Figure 3-3: ATX Power Connector Location

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	+3.3V	13	+3.3V
2	+3.3V	14	-12V
3	GND	15	GND
4	+5V	16	PS_ON
5	GND	17	GND
6	+5V	18	GND

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
7	GND	19	GND
8	Power good	20	-5V
9	5VSB	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	+3.3V	24	GND

Table 3-4: ATX Power Connector Pinouts

3.2.3 Battery Connector

CAUTION:

Risk of explosion if battery is replaced by an incorrect type. Only certified engineers should replace the on-board battery.

Dispose of used batteries according to instructions and local regulations.

NOTE:

It is recommended to attach the RTC battery onto the system chassis in which the IMB-ADL-H610 is installed.

CN Label: **BAT2**

CN Type: 2-pin header

CN Location: See **Figure 3-4**

CN Pinouts: See **Table 3-5**

A system battery is placed in the battery holder. The battery provides power to the system clock to retain the time when power is turned off.
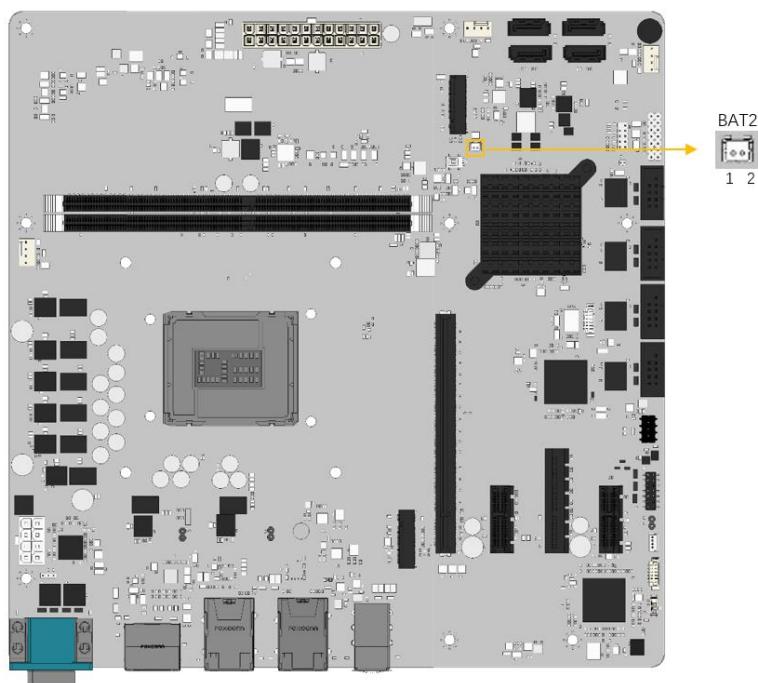
Figure 3-4: RTC Battery Connector Location

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	VBATT	2	GND

Table 3-5 : RTC Battery Connector Pinouts

3.2.4 Digital Input / Output Connector

CN Label: **DIO1**

CN Type: 14-pin header, p=2.0 mm

CN Location: See **Figure 3-5**

CN Pinouts: See **Table 3-6**

The Digital I/O connector provides programmable input and output for external devices.
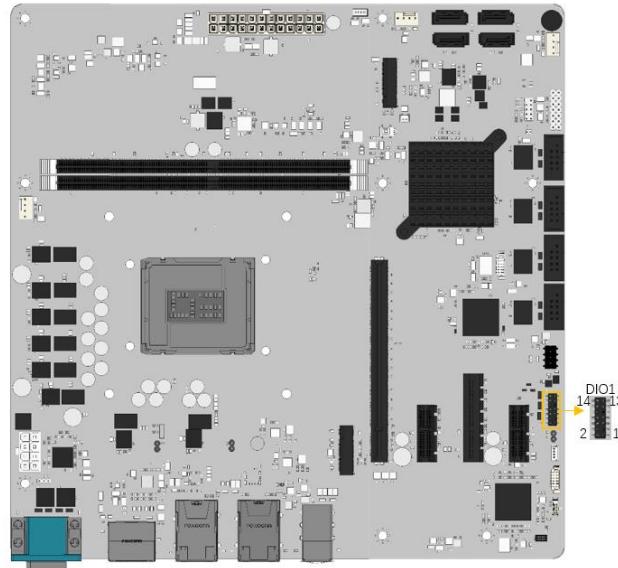
Figure 3-5: Digital Input / Output Connector Location

Pin	Description	Pin	Description
1	GND	2	VCC
3	Output 5	4	Output 4
5	Output 3	6	Output 2
7	Output 1	8	Output 0
9	Input 5	10	Input 4
11	Input 3	12	Input 2
13	Input 1	14	Input 0

Table 3-6: Digital Input / Output Connector Pinouts

3.2.5 EC Debug Connector

CN Label:	**DEBUG_1**
CN Type:	6-pin header, p=1.25 mm
CN Location:	See **Figure 3-6**
CN Pinouts:	See **Table 3-7**

The DEBUG_1 connector is used for EC debug (with SPI protocol).
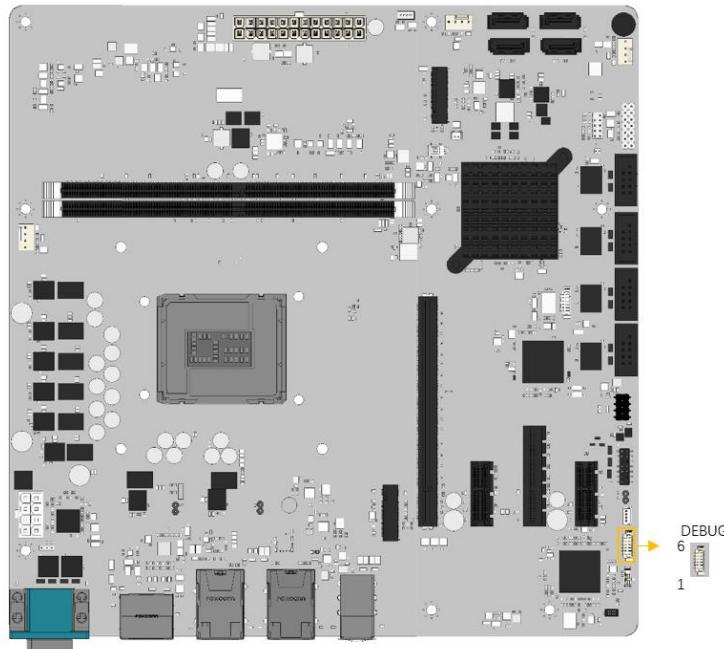
Figure 3-6: EC Debug Connector Location

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	NC	2	EDICS
3	EDIDO	4	EDICLK
5	EDIDI	6	GND

Table 3-7:EC Debug Connector Pinouts

3.2.6 Chassis Intrusion Connector

CN Label: **CHASSIS1**

CN Type: 2-pin header, p=2.54 mm

CN Location: See **Figure 3-7**

CN Pinouts: See **Table 3-8**

The CHASSIS1 (Chassis Intrusion Connector)
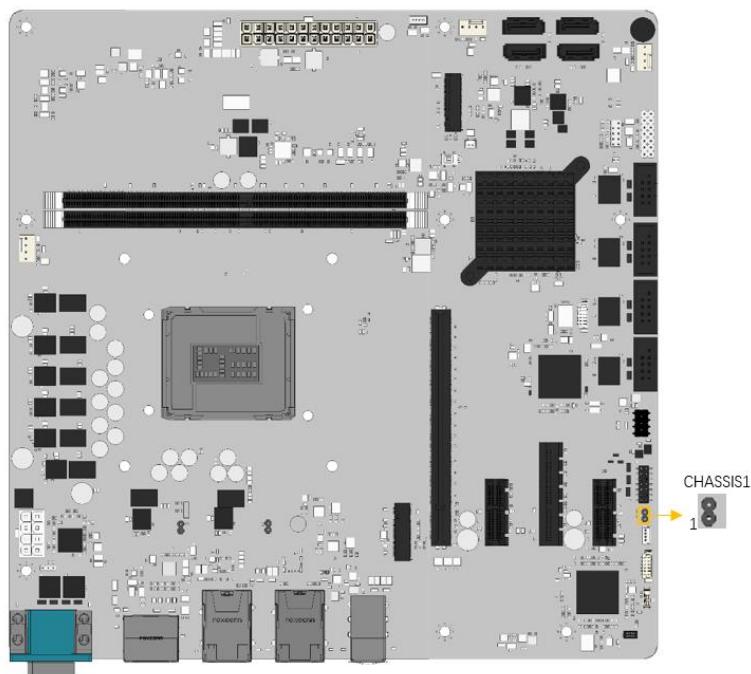
Figure 3-7: Chassis Intrusion Connector Location

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	CASEOPEN_N	2	GND

Table 3-8: Chassis Intrusion Connector Pinouts

3.2.7 RAA229134 FW Flash And LT86101X FW Flash Connector

CN Label: J1, J2

CN Type: 3-pin header, p=2.00 mm

CN Location: See **Figure 3-8**

CN Pinouts: See **Table 3-9**

The J1(RAA229134 FW Flash Connector) and J2(LT86101X FW Flash Connector).
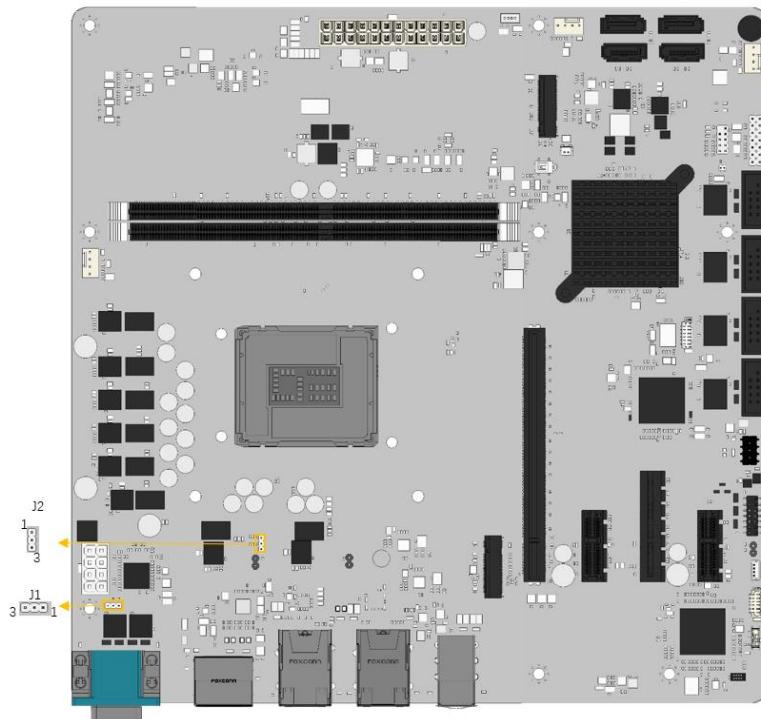
Figure 3-8: RAA229134 & LT86101X FW Flash Connector Location

PIN NO.	DESCRIPTION
1	SDA
2	SCL
3	GND

Table 3-9: RAA229134 & LT86101X FW Flash Connector Pinouts

3.2.8 CPU Fan Connector

CN Label: **CPU_FAN1**

CN Type: 4-pin wafer, p=2.54 mm

CN Location: See **Figure 3-9**

CN Pinouts: See **Table 3-10**

The fan connector attaches to a CPU cooling fan.
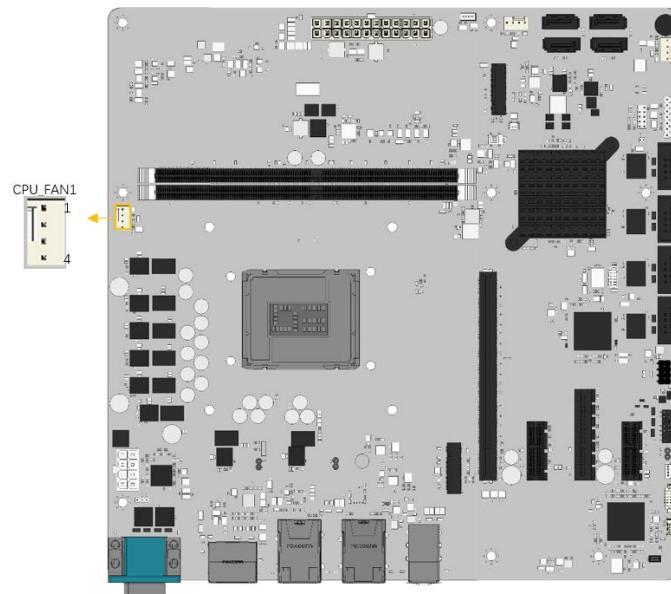
Figure 3-9: CPU Fan Connector Location

PIN NO.	DESCRIPTION
1	GND
2	+12V
3	FANIO
4	PWM (+5V)

Table 3-10: CPU Fan Connector Pinouts

3.2.9 System Fan Connectors

CN Label: **SYS_FAN1, SYS_FAN2**

CN Type: 4-pin wafer, p=2.54 mm

CN Location: See **Figure 3-10**

CN Pinouts: See **Table 3-11** and **Table 3-12**

Each fan connector attaches to a system cooling fan.
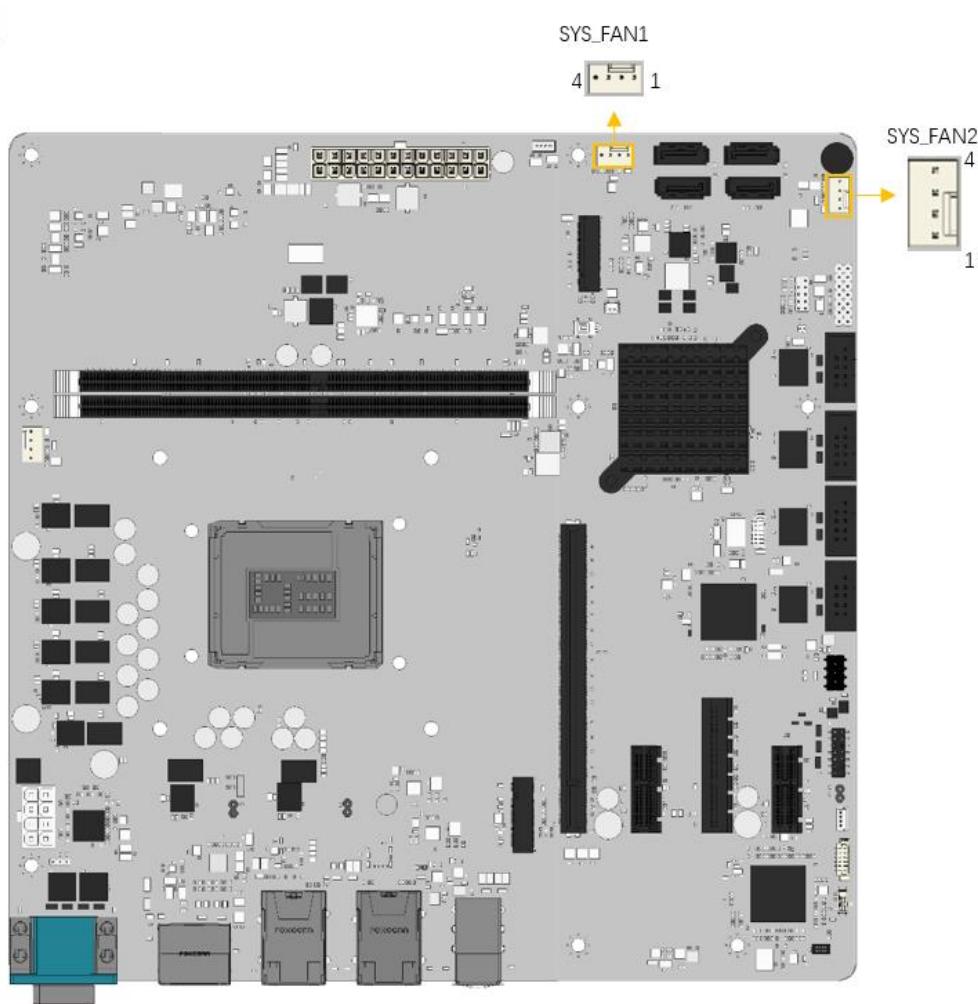
Figure 3-10: System Fan Connector Locations

PIN NO.	DESCRIPTION
1	GND
2	+12V
3	FANIO
4	PWM (+5V)

Table 3-11: System Fan (SYS_FAN1) Connector Pinouts

PIN NO.	DESCRIPTION
1	GND
2	+12V
3	FANIO
4	PWM (+5V)

Table 3-12: System Fan (SYS_FAN2) Connector Pinouts

3.2.10 Audio Connector for IEI AC-KIT-888S kit

CN Label: **IAUDIO**

CN Type: 10-pin header, p=2.00 mm

CN Location: See **Figure 3-11**

CN Pinouts: See **Table 3-13**

This connector connects to IEI AC-KIT-888S audio kit.
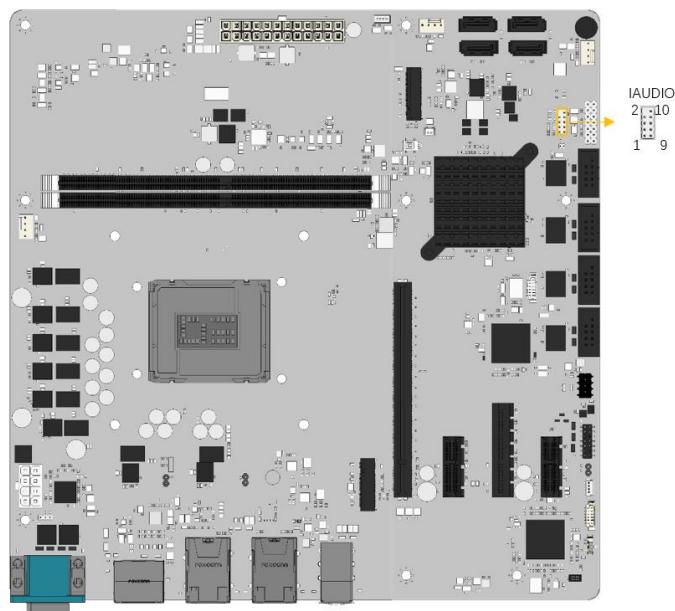
Figure 3-11: IAUDIO Location

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	HDA_SYNC	2	HDA_BIT_CLK
3	HDA_SDOUT	4	HDA_SPKR
5	HDA_SDIN	6	HDA_RST#
7	HAD_VCC	8	HDA_GND
9	HDA_+12V	10	HDA_GND

Table 3-13: IAUDIO Pinouts

3.2.11 Front Panel Connector

CN Label: **F_PANEL1**

CN Type: 14-pin header, p=2.54 mm

CN Location: See **Figure 3-12**

CN Pinouts: See **Table 3-14**

The front panel connector connects to the indicator LEDs, speaker and buttons on the computer's front panel.
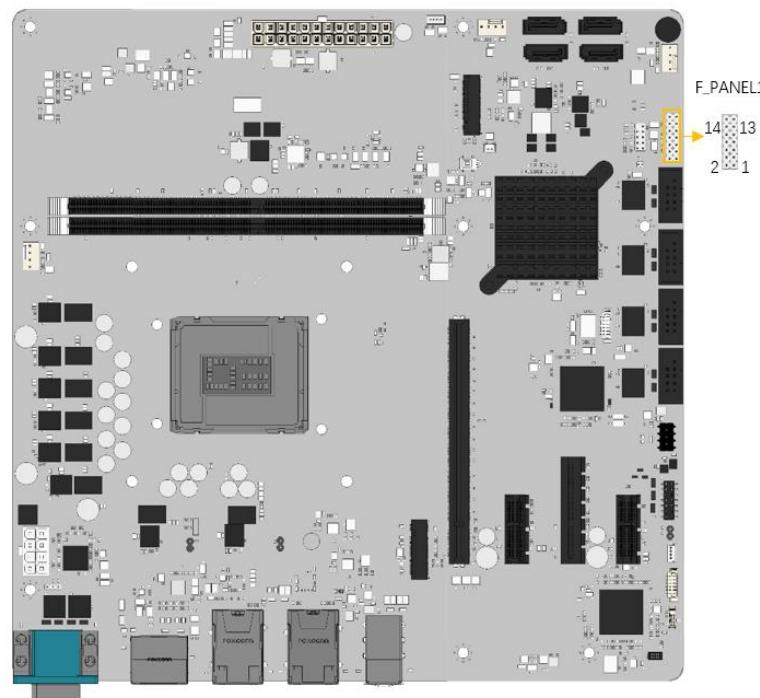
Figure 3-12: Front Panel Connector Location

	PIN	DESCRIPTION	PIN	DESCRIPTION	
PWR LED	1	PWR_LED+	2	SPKR+	SPKR
	3	NC	4	NC	
	5	PWR_LED-	6	NC	
PWR BTN	7	PWR_BTN+	8	SPKR-	
	9	PWR_BTN-	10	NC	
HDD LED	11	HDD_LED+	12	RESET+	RESET
	13	HDD_LED-	14	RESET-	

Table 3-14: Front Panel Connector Pinouts

3.2.12 I²C Connector

CN Label: J_I2C1

CN Type: 4-pin wafer, p=1.25 mm

CN Location: See **Figure 3-13**

CN Pinouts: See **Table 3-15**

The I²C connector is used to connect I²C-bus devices to the mainboard.
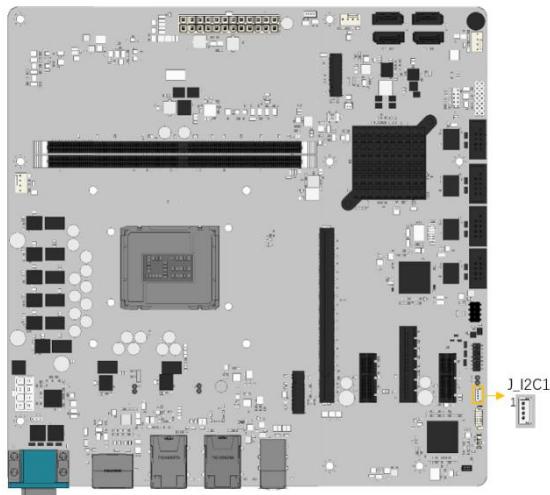
Figure 3-13: I²C Connector Location

PIN NO.	DESCRIPTION
1	GND
2	I2C_DATA

PIN NO.	DESCRIPTION
3	I2C_CLK
4	+5V

Table 3-15: I2C Connector Pinouts

3.2.13 SMBus Connector

CN Label: **J_SMB1**

CN Type: 4-pin wafer, p=1.25 mm

CN Location: See **Figure 3-14**

CN Pinouts: See **Table 3-16**

The SMBus (System Management Bus) connector provides low-speed system management communications.
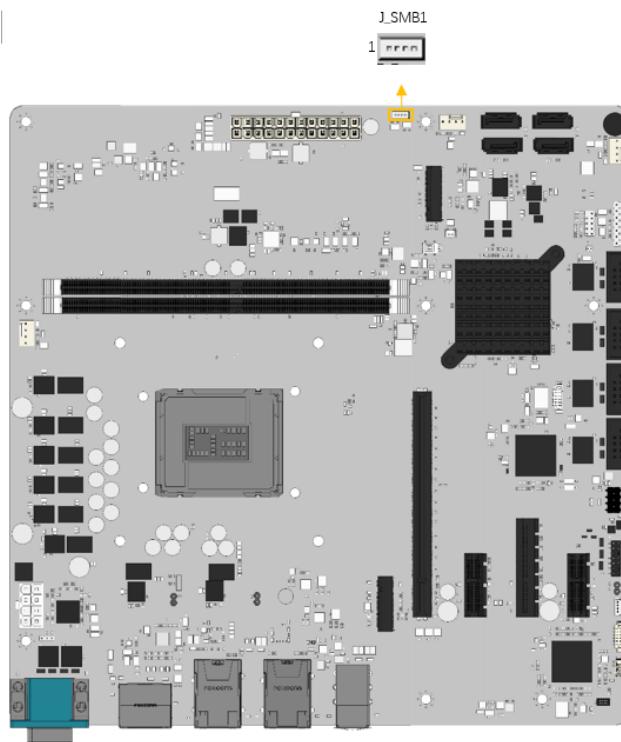
Figure 3-14: SMBus Connector Location

Pin	Description
1	GND
2	SMB_DATA
3	SMB_CLK
4	+5V

Table 3-16: SMBus Connector Pinouts

3.2.14 LAN link LED Connector

CN Label: JLAN_LED1, JLAN_LED2

CN Type: 2-pin header, p=2.54 mm

CN Location: See **Figure 3-15**

CN Pinouts: See **Table 3-17** and **Table 3-18**

The LAN LED connectors are used to connect to the LAN LED indicators on the chassis to indicate users the link activities of the two LAN ports.
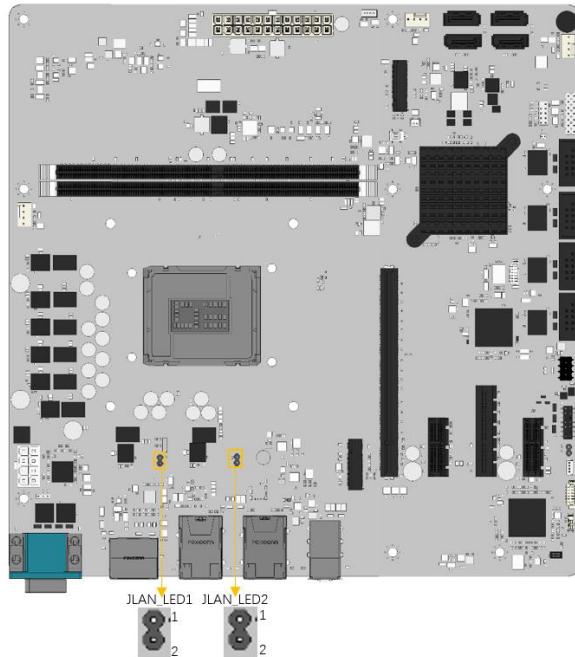
Figure 3-15: LAN LED Connector Locations

Pin	Description
1	+3.3V
2	LAN1_LED_LINK#_ACT

Table 3-17: LAN1 LED Connector (LED_LAN1) Pinouts

Pin	Description
1	+3.3V
2	LAN2_LED_LINK#_ACT

Table 3-18: LAN2 LED Connector (LED_LAN2) Pinouts

3.2.15 M.2 M-key (2280) slot

CN Label: M2_M1

CN Type: M-key slot

CN Location: See **Figure 3-16**

CN Pinouts: See **Table 3-19**

The M.2 M-key slot is keyed in the M position. The M.2 slot supports PCIe x1 interfaces.
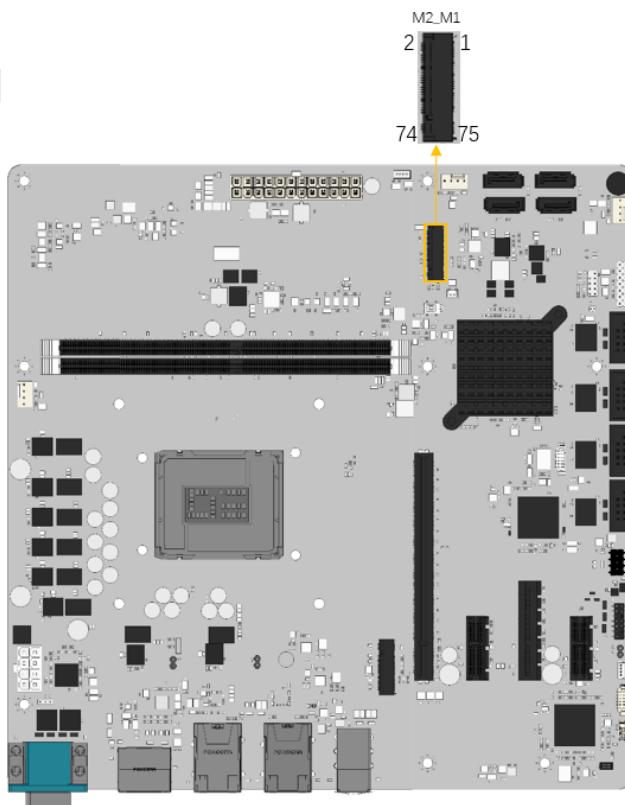
Figure 3-16: M.2 M-key slot Locations

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	GND	2	+3.3V
3	GND	4	+3.3V
5	PERN3	6	NC
7	PERP3	8	NC
9	GND	10	DAS/DSS#
11	PETN3	12	+3.3V
13	PETP3	14	+3.3V
15	GND	16	+3.3V
17	PERN2	18	+3.3V
19	PERP2	20	NC
21	GND	22	NC
23	PETN2	24	NC
25	PETP2	26	NC

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
27	GND	28	NC
29	PERN1	30	NC
31	PERP1	32	NC
33	GND	34	NC
35	PETN1	36	NC
37	PETP1	38	DEVSLP
39	GND	40	NC
41	PERN0/SATA-B+	42	NC
43	PERP0/SATA-B-	44	NC
45	GND	46	NC
47	PETN0/SATA-A-	48	NC
49	PETP0/SATA-A+	50	PERST#NC
51	GND	52	CLKREQ#NC
53	REFCLKN	54	PEWAKE/NC
55	REFCLKP	56	NC
57	GND	58	NC
59	Notch0	60	Notch1
61	Notch2	62	Notch3
63	Notch4	64	Notch5
65	Notch6	66	Notch7
67	NC	68	SUSCLK
69	PEDET	70	+3.3V
71	GND	72	+3.3V
73	GND	74	+3.3V
75	GND		

Table 3-19: M.2 M-Key slot Pinouts

3.2.16 PCIe x1 Slots

CN Label: **PCIE2, PCIE4**

CN Type: PCIe x1 slots

CN Location: See **Figure 3-17**

The PCIe slot enables a PCIe expansion module to be connected to the board.
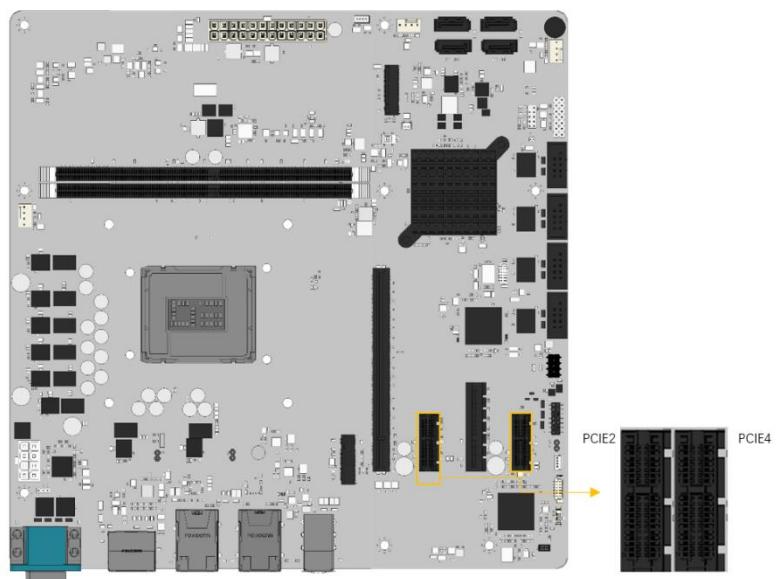
Figure 3-17: PCIe x1 Slot Locations

3.2.17 PCIe x4 Slot

CN Label: **PCIE3**

CN Type: PCIe x4 slot

CN Location: See **Figure 3-18**

The PCIe x4 expansion card slot are for PCIe x4 expansion cards.
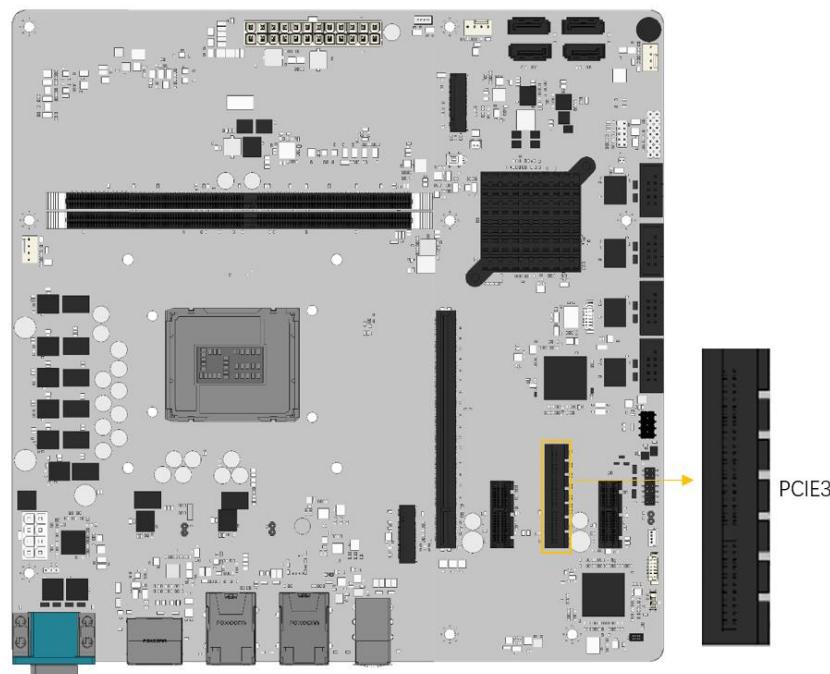
Figure 3-18: PCIe x4 Slot Location

3.2.18 PCIe x16 Slot

CN Label: **PCIE1**

CN Type: PCIe x16 slot

CN Location: See **Figure 3-19**

The PCIe x16 expansion card slot are for PCIe x16 expansion cards.

PCIE1
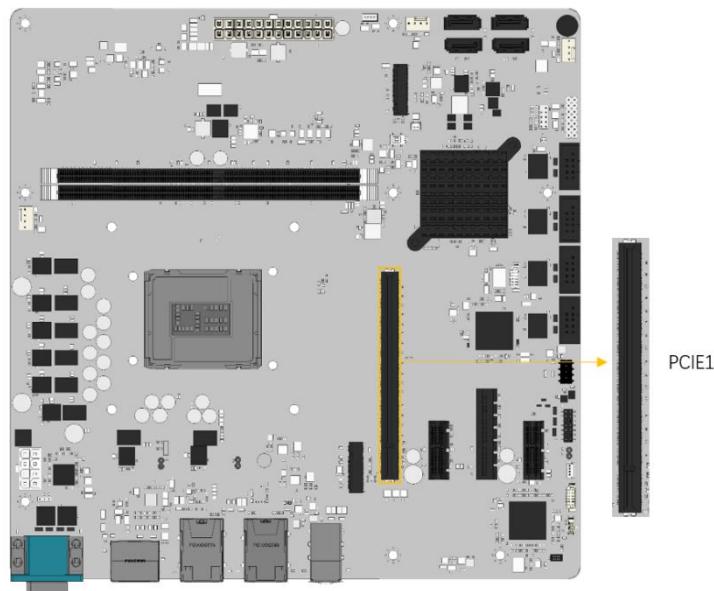
Figure 3-19: PCIe x16 Slot Location

3.2.19 DDR4 DIMM Sockets

CN Label: **CHA_DIMM1, CHB_DIMM1**

CN Type: 288-pin socket

CN Location: See **Figure 3-20**

The DIMM slots are for DDR4 DIMM memory modules
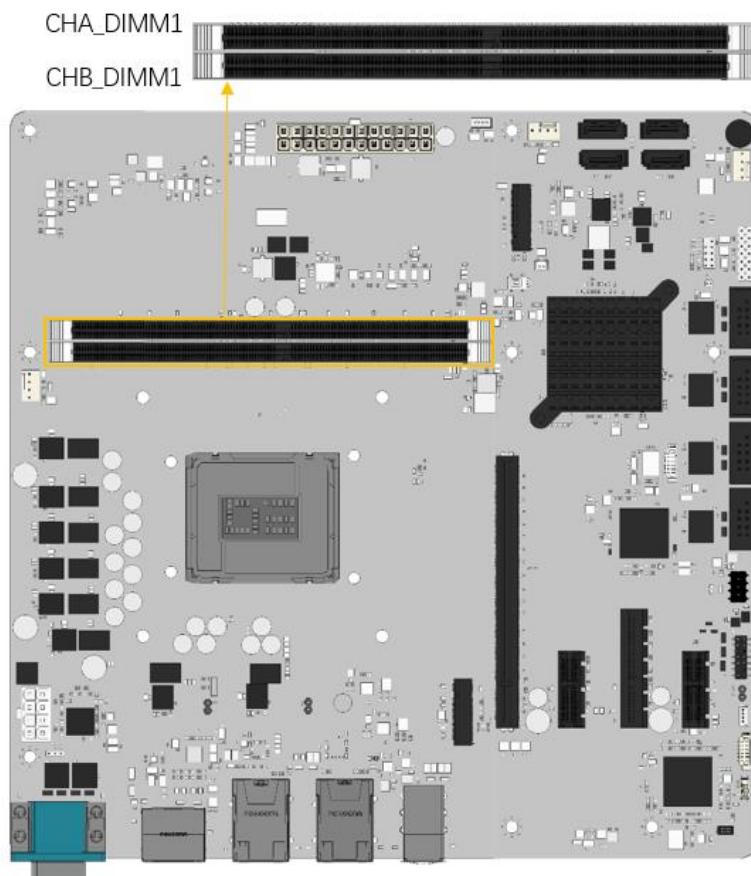
Figure 3-20: DDR4 DIMM Sockets Location

3.2.20 SATA 6Gb/s Connectors

CN Label: **SATA1, SATA2, SATA3, SATA4**

CN Type: 7-pin SATA connector，p=1.27mm

CN Location: See **Figure 3-21**

CN Pinouts: See **Table 3-20**

The SATA drive connectors can be connected to SATA drives and support up to 6Gb/s data transfer rate.
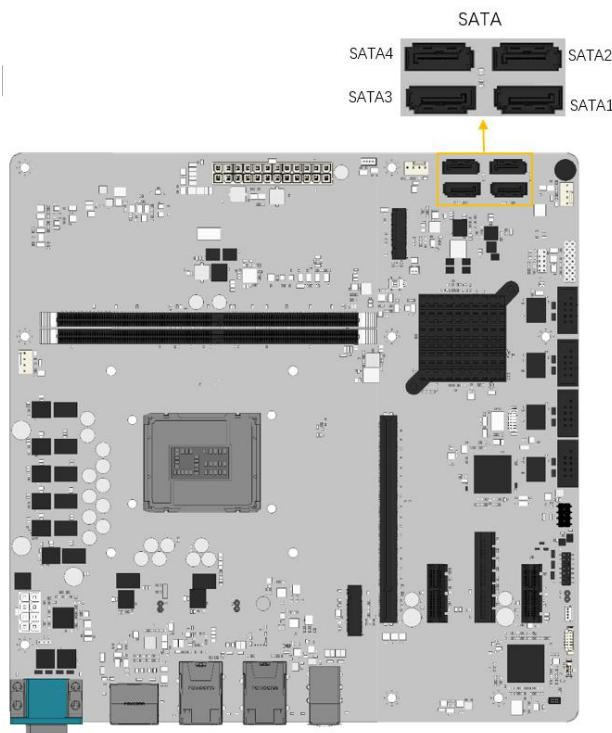
Figure 3-21: SATA 6Gb/s Connector Locations

PIN NO.	DESCRIPTION
1	GND
2	SATA_TX+
3	SATA_TX-
4	GND
5	SATA_RX-

PIN NO.	DESCRIPTION
6	SATA RX+
7	GND
8	N/C

Table 3-20: SATA 6Gb/s Connector Pinouts

3.2.21 RS-232 Serial Port Connectors

CN Label: **COM3, COM4, COM5, COM6**

CN Type: 10-pin box header, p=2.54 mm

CN Location: See **Figure 3-22**

CN Pinouts: See **Table 3-21**

Each of these connectors provides RS-232 connections.
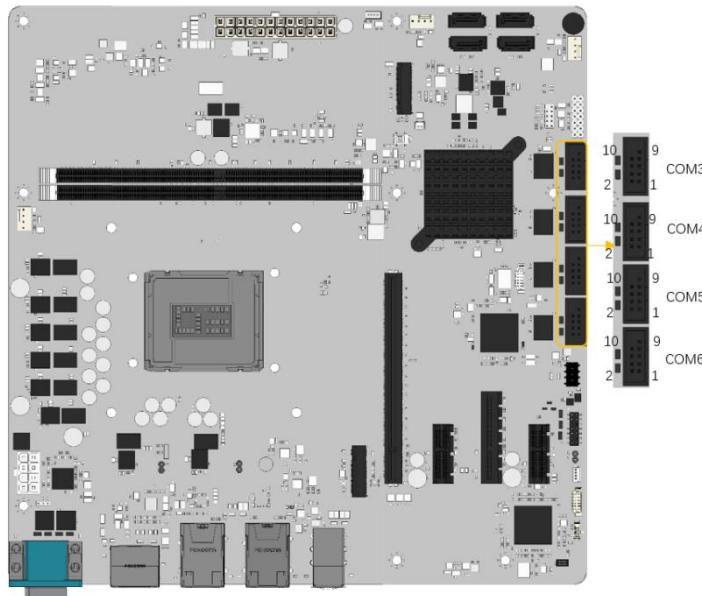
Figure 3-22: RS-232 Serial Port Connector Location

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	DCD	2	DSR
3	RXD	4	RTS
5	TXD	6	CTS

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
7	DTR	8	RI
9	GND	10	GND

Table 3-21: RS-232 Serial Port Connector Pinouts

3.2.22 Flash SPI ROM Connector

CN Label: JSPI1

CN Type: 6 pin header, p=1.25 mm

CN Location: See **Figure 3-23**

CN Pinouts: See **Table 3-22**

The Flash SPI ROM connector is used to flash the SPI ROM.
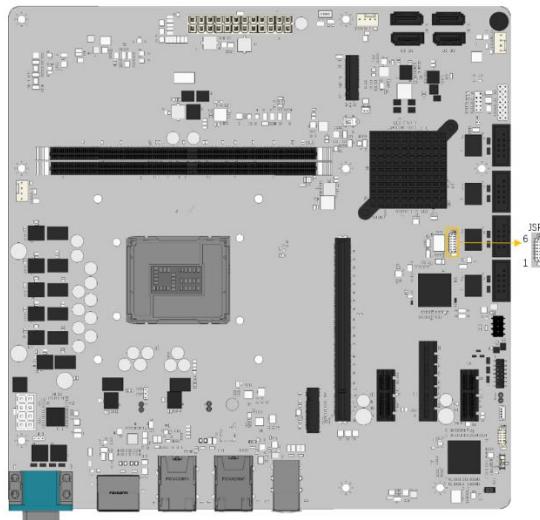
Figure 3-23: Flash SPI ROM Connector Location

Pin	Description	Pin	Description
1	+3.3V	4	SPI_CLK
2	SPI_CS#	5	SPI_SI
3	SPI_SO	6	GND

Table 3-22: Flash SPI ROM Connector Pinouts

3.2.23 Flash EC ROM Connector

CN Label: **JEC1**

CN Type: 8-pin header, p=1.27 mm

CN Location: See **Figure 3-24**

CN Pinouts: See **Table 3-23**

The Flash EC ROM connector is used to flash the EC ROM.
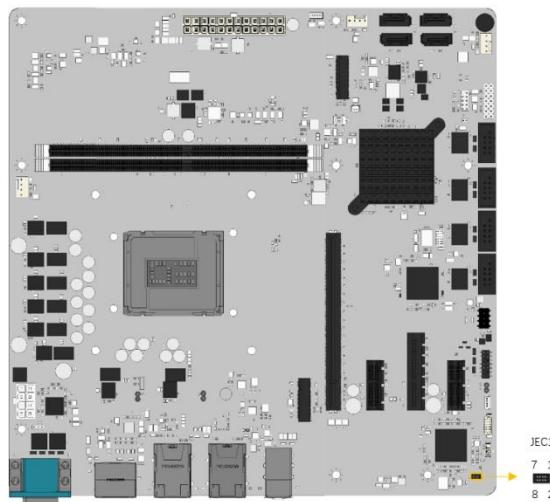
Figure 3-24: Flash EC ROM Connector Location

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	FSCE#	2	+3.3V
3	FMISO	4	NC
5	EC_DET_FLASH	6	FSCK
7	GND	8	FMOSI

Table 3-23: Flash EC ROM Connector Pinouts

3.2.24 Internal USB 2.0 Connector

CN Label: **USB2_CN1**

CN Type: 8pin header, p=2.54 mm

CN Location: See **Figure 3-25**

CN Pinouts: See **Table 3-24**

The Internal USB 2.0 connector connects to USB 2.0 devices.
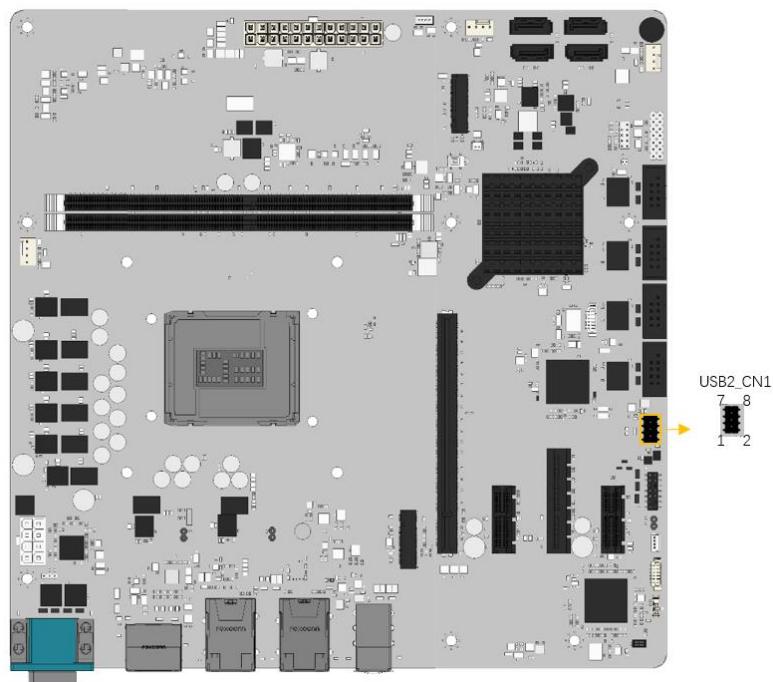
Figure 3-25: Internal USB 2.0 Connector Locations

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	VCC	3	GND
2	USB_DATA-	4	USB_DATA+
5	USB_DATA+	6	USB_DATA-
7	GND	8	VCC

Table 3-24: Internal USB 2.0 Connector Pinouts

3.3 External Peripheral Interface Connector Panel

The figure below shows the external peripheral interface connector (EPIC) panel. The EPIC panel consists of the following:
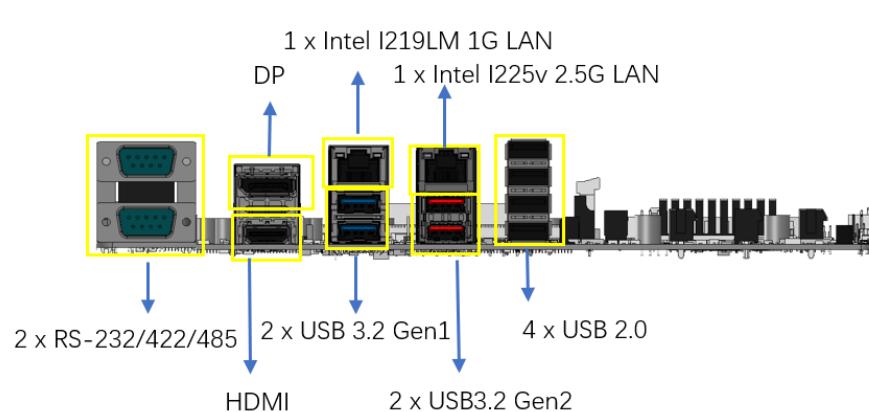
Figure 3-26: External Peripheral Interface Connector

3.3.1 External USB 2.0 Connector

CN Label:	**USB2_1**
CN Type:	USB 2.0
CN Location:	See **Figure 3-27**
CN Pinouts:	See **Table 3-25**

There are four external USB 2.0 connectors on the IMB-ADL-H610. The USB 2.0 connector can be connected to a USB 2.0/1.1 device
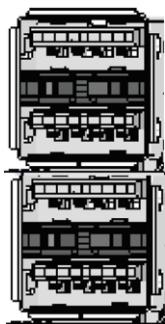
Figure 3-27: USB 2.0 Connector

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
11	USB_VCC	31	USB_VCC
12	D-	32	D-
13	D+	33	D+
14	GND	34	GND
21	USB_VCC	41	USB_VCC
22	D-	42	D-
23	D+	43	D+
24	GND	44	GND

Table 3-25: USB 2.0 Port Pinouts

3.3.2 External HDMI And DP Combo Connector

CN Label:	HDMI_DP1
CN Type:	HDMI, DisplayPort
CN Location:	See **Figure 3-28** and **Figure 3-29**
CN Pinouts:	See **Table 3-26** and **Table 3-27**

The HDMI connector can connect to an HDMI device.

Pin	Description	Pin	Description
21	HDMI_DATA2P	31	GND
22	GND	32	HDMI_CLKN
23	HDMI_DATA2N	33	N/C
24	HDMI_DATA1P	34	N/C

Pin	Description	Pin	Description
25	GND	35	HDMI_CLK
26	HDMI_DATA1N	36	HDMI_SDA
27	HDMI_DATA0P	37	GND
28	GND	38	+5V
29	HDMI_DATA0N	39	HDMI_HPD
30	HDMI_CLKP		

Table 3-26: HDMI Connector Pinouts
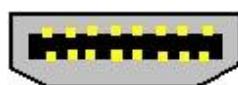
Figure 3-28: HDMI Connector

The DP connector connects to a display device with DisplayPort interface.

Pin	Description	Pin	Description
1	LANE0P	11	GND
2	GND	12	LANE3N
3	LANE0N	13	CONFIG_A_1
4	LANE1P	14	CONFIG_A_2
5	GND	15	AUXP
6	LANE1N	16	GND
7	LANE2P	17	AUXN
8	GND	18	HPD
9	LANE2N	19	GND
10	LANE3P	20	+5V

Table 3-27: DP Connector Pinouts
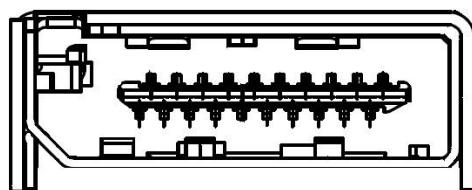
Figure 3-29: DP Connector

3.3.3 External RS-232/422/485 Combo Connector

CN Label:	**COM1, COM2**
CN Type:	Dual DB-9
CN Location:	See **Figure 3-30**
CN Pinouts:	See **Table 3-28**

The COM connector (COM1) and the COM connector (COM2) connects to a serial device that supports RS-232/422/485 communication.

	RS-232	**RS-422**	**RS-485**
1	DCD	TXD422-	TXD485-
2	RXD	TXD422+	TXD485+
3	TXD	RXD422+	--
4	DTR	RXD422-	--
5	GND	--	--
6	DSR	--	--
7	RTS	--	--
8	CTS	--	--
9	RI	--	--

Table 3-28: External RS-232 Connector Pinouts

COM1

COM2
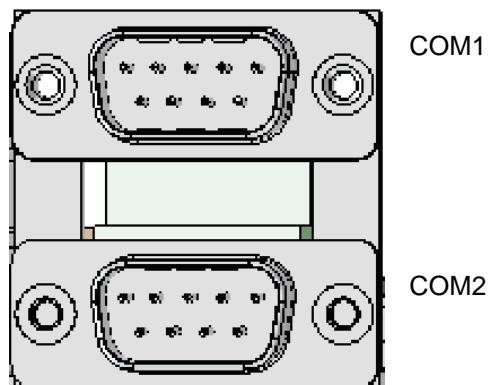
Figure 3-30: Dual DB-9

3.3.4 External 1GbE RJ-45 And External Dual USB 3.2 Gen1 Connector

CN Label: **LAN1_USB1**

CN Type: 12‒PIN 1GbE and 18‒PIN USB 3.2

CN Location: See **Figure 3-31**

CN Pinouts: See **Table 3-29** and **Table 3-30**

The LAN1_USB1 connector supports dual USB 3.2 Gen 1 (5Gb/s) and 1GbE LAN RJ-45.

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
20	LAN1_MD0+	26	LAN1_MD3+
21	LAN1_MD0-	27	LAN1_MD3-
22	LAN1_MD1+	29	VCC
23	LAN1_MD1-	30	ACT
24	LAN1_MD2+	31	100-
25	LAN1_MD2-	32	1000-

Table 3-29: RJ45 LAN Connector

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	VCC	10	VCC
2	USB_DATA-	11	USB_DATA-
3	USB_DATA+	12	USB_DATA+
4	GND	13	GND
5	USB3_RX-	14	USB3_RX-
6	USB3_RX+	15	USB3_ RX+
7	GND	16	GND
8	USB3_TX-	17	USB3_TX-
9	USB3_TX+	18	USB3_TX+

Table 3-30: External USB 3.2 Connector
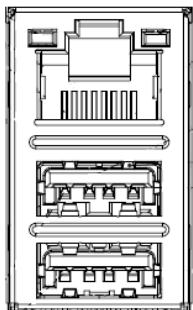
Figure 3-31: USB3.2 And RJ45 LAN Connector

3.3.5 External 2.5GbE RJ-45 And Dual USB 3.2 Gen 2 Connector

CN Label: **LAN2_USB2**

CN Type: 2.5GbE RJ-45 and USB 3.2 Gen 2 Type A

CN Location: See **Figure 3-32**

CN Pinouts: See **Table 3-31** and **Table 3-32**

The LAN2_USB2 connector supports dual USB 3.2 Gen 2(10Gb/s) and LAN 2.5GbE RJ-45. The red connector is USB 3.2 Gen 2.
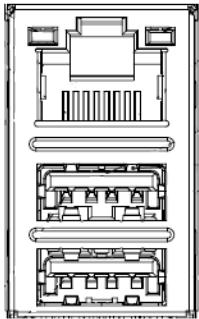
Figure 3-32: External LAN 2.5GbE Connector and USB 3.2 Gen 2 Type A

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
20	LAN2_MD0+	26	LAN2_MD3+
21	LAN2_MD0-	27	LAN2_MD3-
22	LAN2_MD1+	29	VCC
23	LAN2_MD1-	30	ACT
24	LAN2_MD2+	31	2500-
25	LAN2_MD2-	32	1000-

Table 3-31:External LAN 2.5GbE Connector Pinouts

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	VCC	10	VCC
2	USB_DATA-	11	USB_DATA-
3	USB_DATA+	12	USB_ DATA+
4	GND	13	GND
5	USB3_RX-	14	USB3_RX-
6	USB3_RX+	15	USB3_ RX+
7	GND	16	GND
8	USB3_TX-	17	USB3_TX-
9	USB3_TX+	18	USB3_TX+

Table 3-32: External Dual USB 3.2 Gen 2 Connector Pinouts

Chapter

4

Installation

4.1 Anti-Static Precautions

![warning icon] **WARNING:**

Failure to take ESD precautions during the installation of the IMB-ADL-H610 may result in permanent damage to the IMB-ADL-H610 and severe injury to the user.

Electrostatic discharge (ESD) can cause serious damage to electronic components, including the IMB-ADL-H610. Dry climates are especially susceptible to ESD. It is therefore critical that whenever the IMB-ADL-H610 or any other electrical component is handled, the following anti-static precautions are strictly adhered to.

- *Wear an anti-static wristband*: - Wearing a simple anti-static wristband can help to prevent ESD from damaging the board.
- *Self-grounding*: - Before handling the board touch any grounded conducting material. During the time the board is handled, frequently touch any conducting materials that are connected to the ground.
- *Use an anti-static pad*: When configuring the IMB-ADL-H610, place it on an anti-static pad. This reduces the possibility of ESD damaging the IMB-ADL-H610.
- *Only handle the edges of the PCB*: - When handling the PCB, hold the PCB by the edges.

4.2 Installation Considerations

![note icon] **NOTE:**

The following installation notices and installation considerations should be read and understood before installation. All installation notices must be strictly adhered to. Failing to adhere to these precautions may lead to severe damage and injury to the person performing the installation.

WARNING:

The installation instructions described in this manual should be carefully followed in order to prevent damage to the components and injury to the user.

Before and during the installation please **DO** the following:

- Read the user manual:
  - The user manual provides a complete description of the IMB-ADL-H610 installation instructions and configuration options.
- Wear an electrostatic discharge cuff (ESD):
  - Electronic components are easily damaged by ESD. Wearing an ESD cuff removes ESD from the body and helps prevent ESD damage.
- Place the IMB-ADL-H610 on an anti-static pad:
  - When installing or configuring the motherboard, place it on an anti-static pad. This helps to prevent potential ESD damage.
- Turn all power to the IMB-ADL-H610 off:
  - When working with the IMB-ADL-H610, make sure that it is disconnected from all power supplies and that no electricity is being fed into the system.

Before and during the installation of the IMB-ADL-H610, **DO NOT:**

- Remove any of the stickers on the PCB board. These stickers are required for warranty validation.
- Use the product before verifying all the cables and power connectors are properly connected.
- Allow screws to come in contact with the PCB circuit, connector pins, or its components.

4.3 Socket LGA1700 CPU Installation

![WARNING icon] **WARNING:**

CPUs are expensive and sensitive components. When installing the CPU please be careful not to damage it in anyway. Make sure the CPU is installed properly and ensure the correct cooling kit is properly installed.

DO NOT touch the pins at the bottom of the CPU. When handling the CPU, only hold it on the sides.

To install the CPU, follow the steps below.

Step 1: **Disengage the load lever** by pressing the lever down and slightly outward to clear the retention tab. Fully open the lever. See **Figure 4-1**.
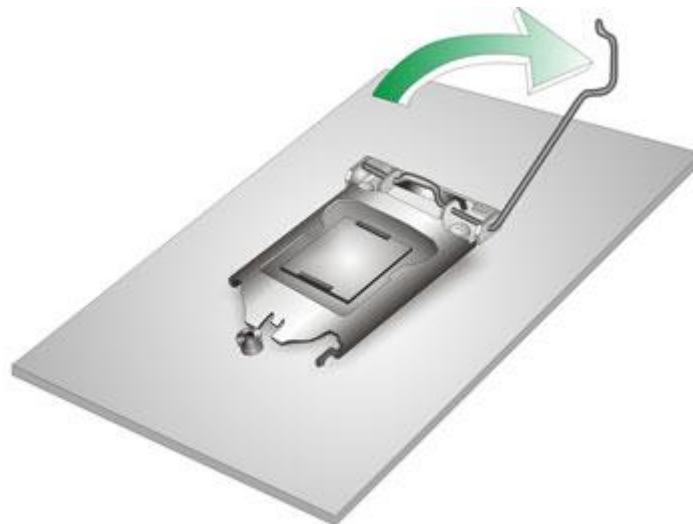
Figure 4-1: Disengage the CPU Socket Load Lever

Step 2: **Open the socket and remove the protective cover.** The black protective cover can be removed by pulling up on the tab labeled "Remove". See **Figure 4-2**.
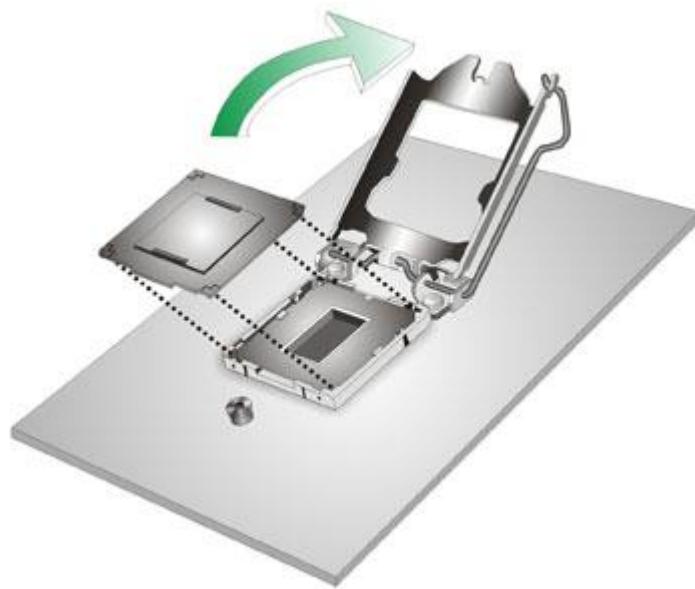
Figure 4-2: Remove Protective Cover

Step 3: **Inspect the CPU socket.** Make sure there are no bent pins and make sure the socket contacts are free of foreign material. If any debris is found, remove it with compressed air.

Step 4: **Orientate the CPU properly.** The contact array should be facing the CPU socket.

![warning] **WARNING:**

DO NOT touch the pins at the bottom of the CPU. When handling the CPU, only hold it on the sides.

Step 5: **Correctly position the CPU.** Match the Pin 1 mark with the cut edge on the CPU socket.

Step 6: **Align the CPU pins.** Locate pin 1 and the two orientation notches on the CPU. Carefully match the two orientation notches on the CPU with the socket alignment keys.

Step 7: **Insert the CPU.** Gently insert the CPU into the socket. If the CPU pins are properly aligned, the CPU should slide into the CPU socket smoothly. See **Figure 4-3**.
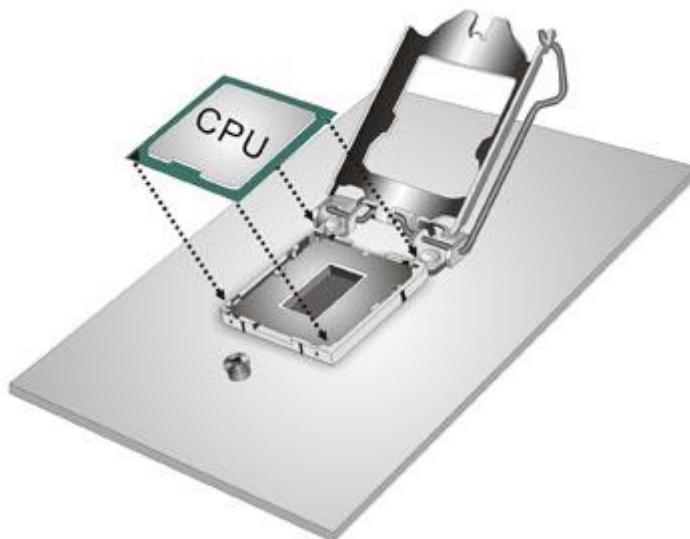
Figure 4-3: Insert the Socket LGA1700 CPU

Step 8: **Close the CPU socket.** Close the load plate and pull the load lever back a little to have the load plate be able to secure to the knob. Engage the load lever by pushing it back to its original position (**Figure 4-4**). There will be some resistance, but will not require extreme pressure.
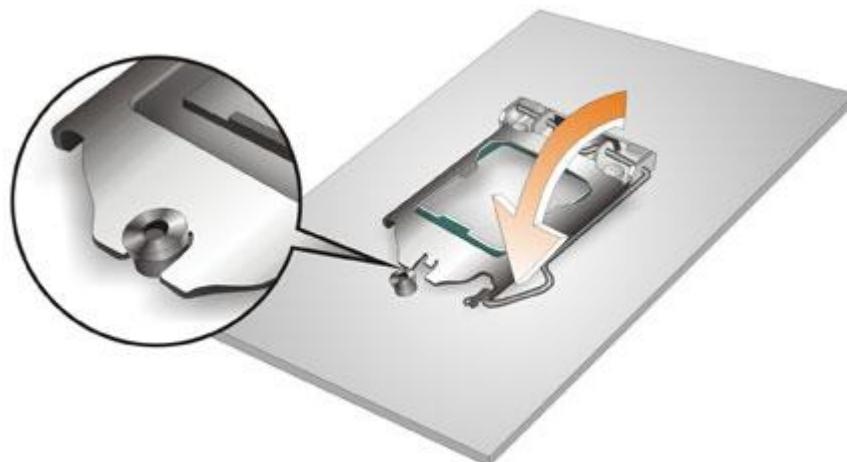
Figure 4-4: Close the Socket LGA1700

Step 9: **Connect the 12 V power to the board.** Connect the 12 V power from the power supply to the board.

4.4 Socket LGA1700 Cooling Kit Installation

WARNING:

DO NOT attempt to install a push-pin cooling fan.

The pre-installed support bracket prevents the board from bending and is ONLY compatible with captive screw type cooling fans.

The cooling kit can be bought from IEI. The cooling kit has a heat sink and fan.

WARNING:

Do not wipe off (accidentally or otherwise) the pre-sprayed layer of thermal paste on the bottom of the heat sink. The thermal paste between the CPU and the heat sink is important for optimum heat dissipation.

To install the cooling kit, follow the instructions below.

Step 1: A cooling kit bracket is pre-installed on the rear of the motherboard. See **Figure 4-5.**
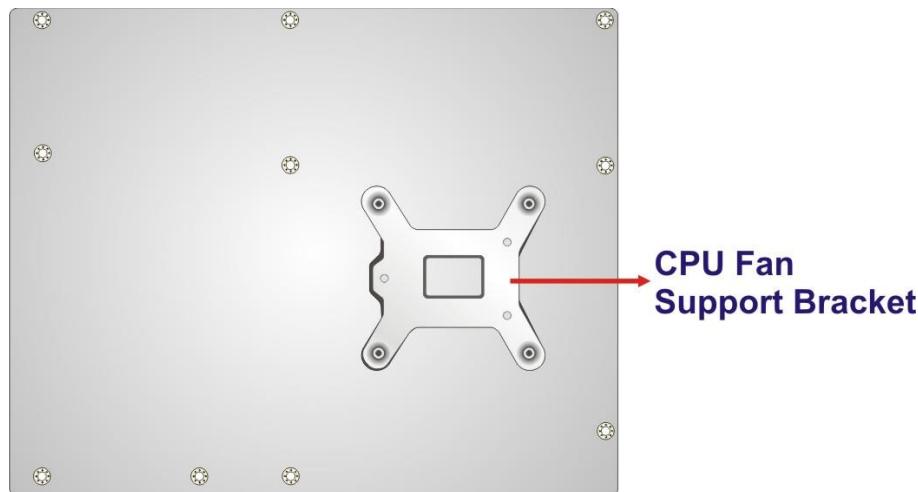
Figure 4-5: Cooling Kit Support Bracket

Step 2: **Place the cooling kit onto the socket LGA1700 CPU**. Make sure the CPU cable can be properly routed when the cooling kit is installed.

Step 3: **Mount the cooling kit**. Gently place the cooling kit on top of the CPU. Make sure the four threaded screws on the corners of the cooling kit properly pass through the holes of the cooling kit bracket.

Step 4: **Tighten the screws**. Use a screwdriver to tighten the four screws. In a diagonal pattern, tighten each screw a few turns then move to the next one, until they are all secured. Do not overtighten the screws.

Step 5: **Connect the fan cable**. Connect the cooling kit fan cable to the CPU fan connector on the IMB-ADL-H6101. Carefully route the cable and avoid heat generating chips and fan blades.

4.5 DIMM Installation

To install a DIMM, please follow the steps below and refer to **Figure 4-6**.
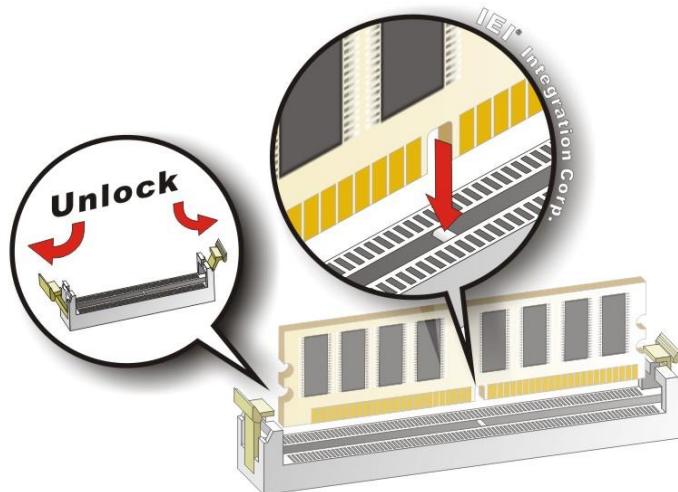
Figure 4-6: DIMM Installation

Step 1: **Open the DIMM socket handles**. Open the two handles outwards as far as they can. See **Figure 4-6**.

Step 2: **Align the DIMM with the socket**. Align the DIMM so the notch on the memory lines up with the notch on the memory socket. See **Figure 4-6**.

Step 3: **Insert the DIMM**. Once aligned, press down until the DIMM is properly seated. Clip the two handles into place. See **Figure 4-6**.

Step 4: **Removing a DIMM**. To remove a DIMM, push both handles outward. The memory module is ejected by a mechanism in the socket.

> ⚠️ **CAUTION:**
>
> For dual channel configuration, install two identical memory modules that feature the same capacity, timings, voltage, number of ranks and the same brand.

4.6 System Configuration

The system configuration is controlled by jumpers, buttons, switches and BIOS options. The system configuration must be performed before installation.

4.6.1 AT/ATX Power Mode Setting

The AT and ATX power mode selection is made through the AT/ATX power mode switch which is shown in **Figure 4-7**.
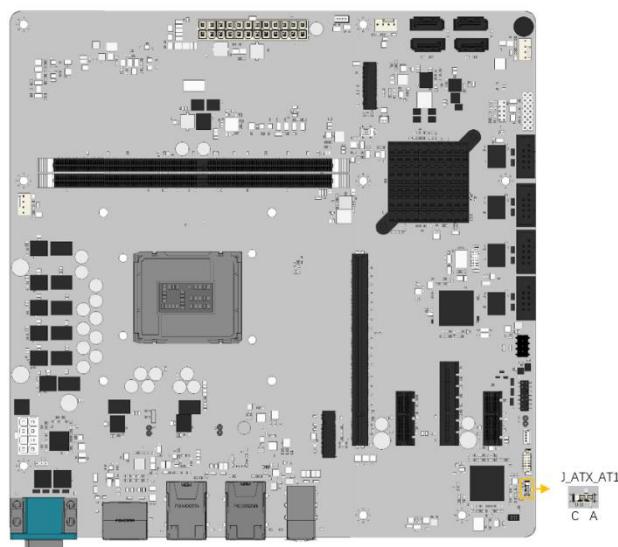
Figure 4-7: AT/ATX Power Mode Switch Location

PIN NO.	DESCRIPTION
Short A - B	ATX Power Mode (default)
Short B - C	AT Power Mode

Table 4-1: AT/ATX Power Mode Switch Settings

4.6.2 Clearing CMOS

To reset the BIOS, remove the on-board battery and short the **J_CMOS1** connector the clear CMOS connector is shown in **Figure 4-8**
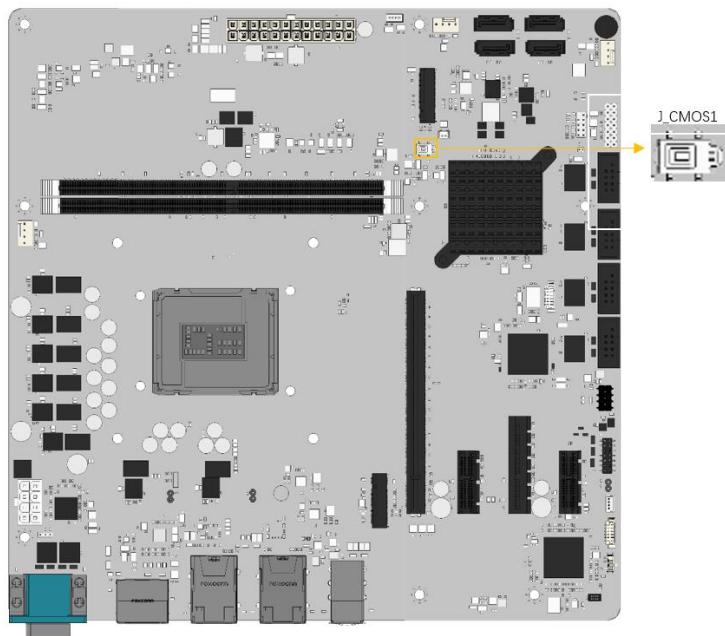
Figure 4-8: Clear CMOS Jumper Location

Status	DESCRIPTION
NC	Keep CMOS Setup (Normal Operation)
Press	Clear CMOS Setup

Table 4-2: Clear CMOS Jumper Pinouts

4.6.3 Flash Descriptor Security Override Jumper

The flash descriptor security override jumper (ME_FLASH1) allows to enable or disable the ME firmware update. Refer to **Table 4-3** and **Figure 4-9** for the jumper location and settings.

PIN NO.	DESCRIPTION
Open	Disabled (default)

PIN NO.	DESCRIPTION
Short	Enabled

Table 4-3: Flash Descriptor Security Override Jumper Pinouts
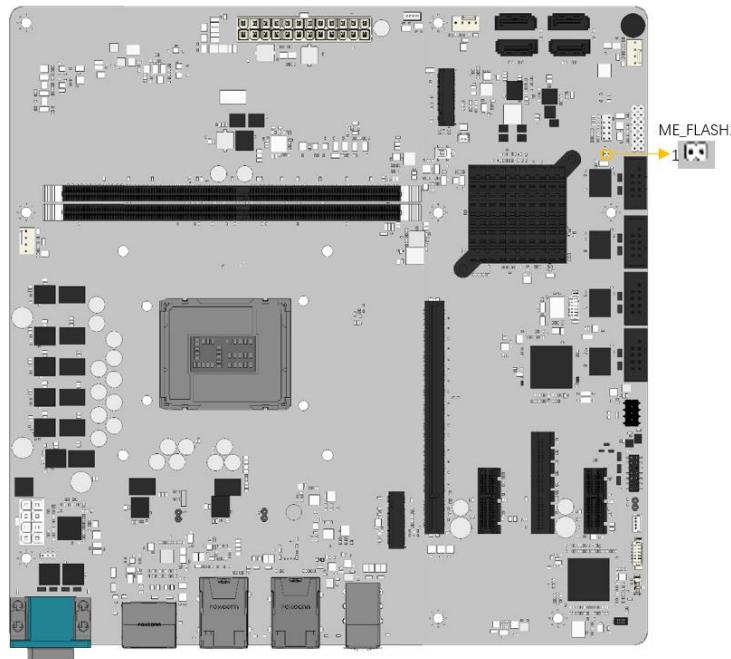
Figure 4-9: Flash Descriptor Security Override Jumper Location

To update the ME firmware, please follow the steps below.

Step 1: Before turning on the system power, short pin 2-3 of the flash descriptor security override jumper.

Step 2: Update the BIOS and ME firmware, and then turn off the system power.

Step 3: Remove the metal clip on the flash descriptor security override jumper or return to its default setting (short pin 1-2).

Step 4: Restart the system. The system will reboot 2 ~ 3 times to complete the ME firmware update.

4.7 Internal Peripheral Device Connections

This section outlines the installation of peripheral devices to the onboard connectors.

4.7.1 SATA Drive Connection

The IMB-ADL-H610 is shipped with two SATA drive cables. To connect the SATA drives to the connectors, please follow the steps below.

Step 1: **Locate the connectors**. The locations of the SATA drive connectors are shown in **Chapter 3**.

Step 2: **Insert the cable connector**. Insert the cable connector into the on-board SATA drive connector until it clips into place. See **Figure 4-10**.
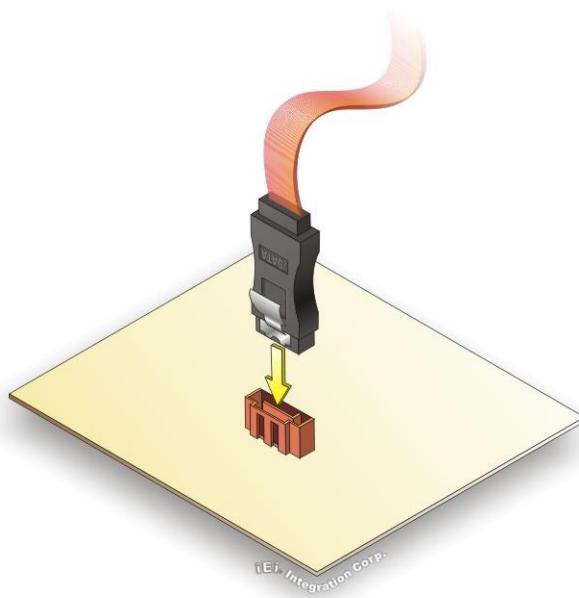
Figure 4-10: SATA Drive Cable Connection

Step 3: **Connect the cable to the SATA disk**. Connect the connector on the other end of the cable to the connector at the back of the SATA drive. See **Figure 4-11**.

Step 4: **Connect the SATA power cable**. Connect the SATA power connector to the back of the SATA drive. See **Figure 4-11**.
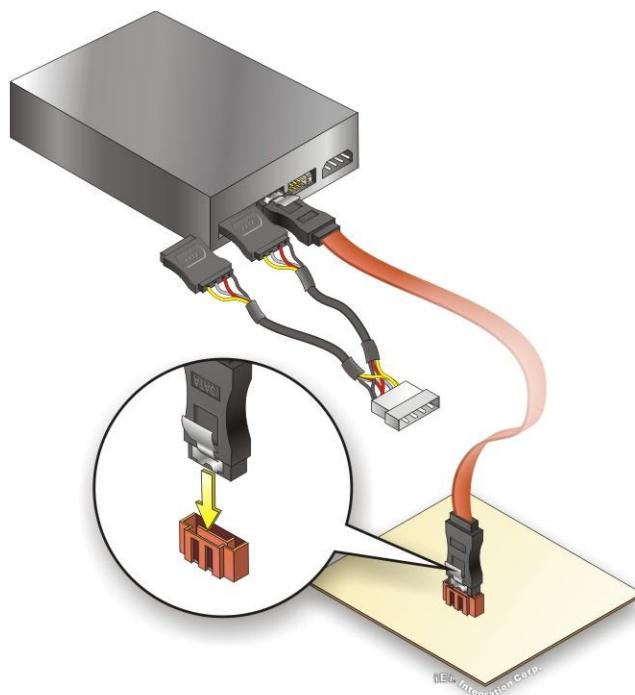
Figure 4-11: SATA Power Drive Connection

The SATA power cable can be bought from IEI. See Optional Items in Section 2.4.

4.8 Software Installation

All the drivers for the IMB-ADL-H610 are available on IEI Resource Download Center (https://download.ieiworld.com). Type IMB-ADL-H610 and press Enter to find all the relevant software, utilities, and documentation.
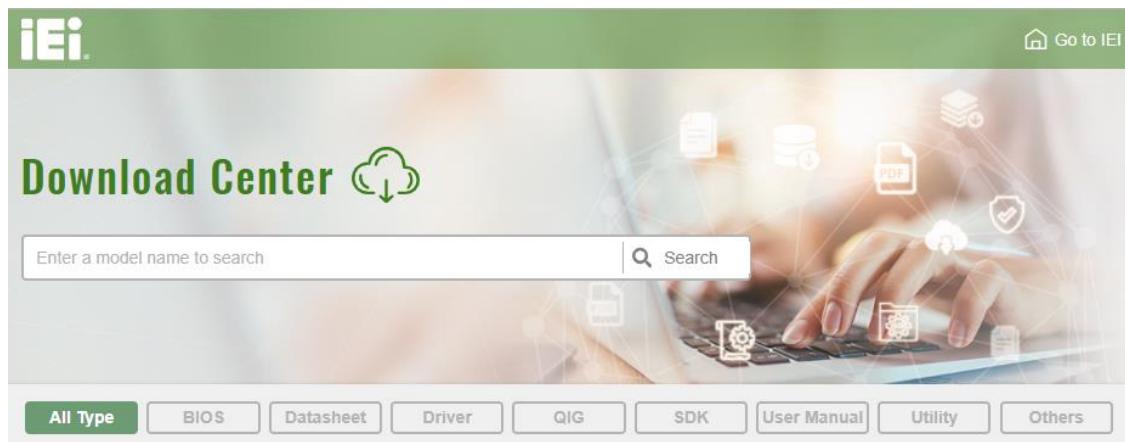
Figure 4-12: IEI Resource Download Center

4.9 Driver Download

To download drivers from IEI Resource Download Center, follow the steps below.

Step 1: Go to https://download.ieiworld.com. Type IMB-ADL-H610 and press Enter.
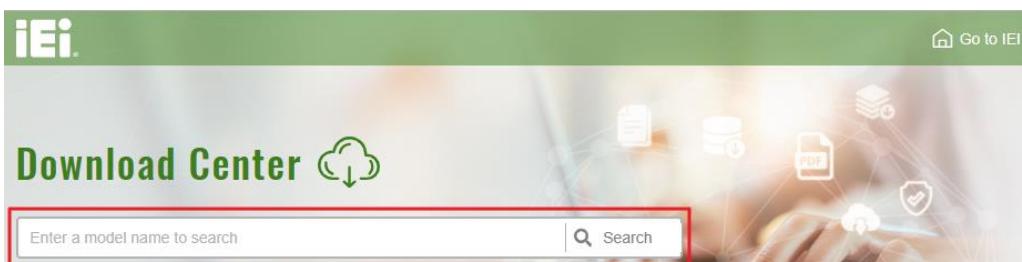
Step 2: All product-related software, utilities, and documentation will be listed. You can choose **Driver** to filter the result.

Step 3: Click the driver file name on the page and you will be prompted with the following window. You can download the entire ISO file (❶), or click the small arrow to find an individual driver and click the file name to download (❷).
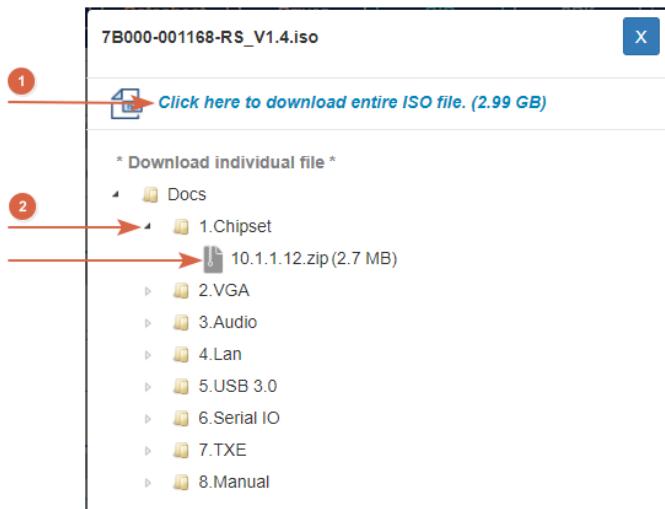
 NOTE:

To install software from the downloaded ISO image file in Windows 8, 8.1 or 10, double-click the ISO file to mount it as a virtual drive to view its content. On Windows 7 system, an additional tool (such as Virtual CD-ROM Control Panel from Microsoft) is needed to mount the file.

Chapter

5

BIOS

5.1 Introduction

The BIOS is programmed onto the BIOS chip. The BIOS setup program allows changes to certain system settings. This chapter outlines the options that can be changed.

![NOTE icon] **NOTE:**

> Some of the BIOS options may vary throughout the life cycle of the product and are subject to change without prior notice.

5.1.1 Starting Setup

The UEFI BIOS is activated when the computer is turned on. The setup program can be activated in one of two ways.

1. **Using keyboard**: Press the **DEL** or **F2** as soon as the system is turned on.
2. **Using touchscreen**: Press the **Setup** button on the upper right corner of the BIOS Starting Menu.

If the message disappears before the **DEL or F2** key is pressed, restart the computer and try again, then the BIOS Starting Menu will appear. Select "Setup" and press Enter to get into the BIOS Setup.
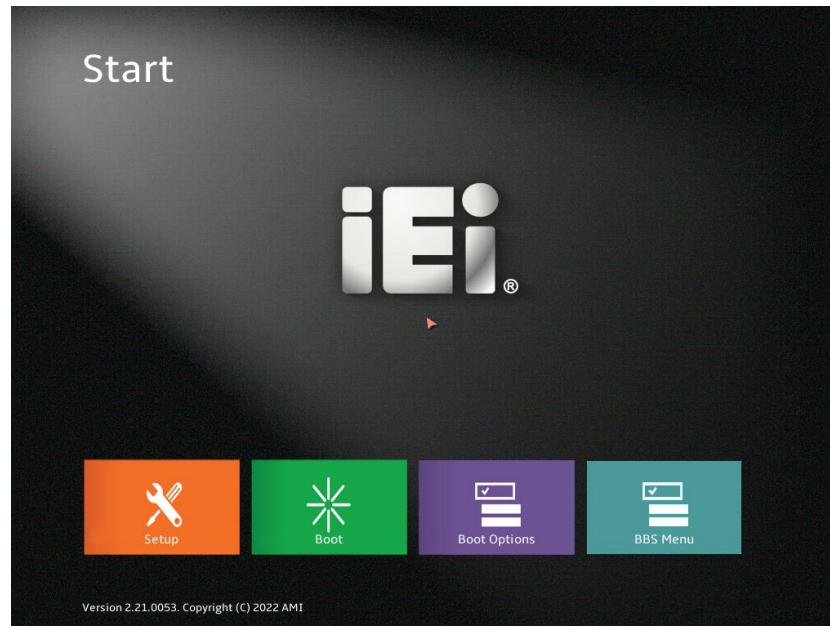
Figure 5-1: BIOS Starting Menu

5.1.2 Using Setup

The BIOS Setup menu can be navigated by using a keyboard or a touchscreen.

5.1.2.1 Keyboard Navigation

For keyboard navigation, use the navigation keys shown in **Table 5-1**.

Key	Function
Up arrow	Move to previous item
Down arrow	Move to next item
Left arrow	Move to the item on the left hand side
Right arrow	Move to the item on the right hand side
+	Increase the numeric value or make changes
-	Decrease the numeric value or make changes
Page Up	Move to the previous page
Page Dn	Move to the next page
Esc	Main Menu – Quit and not save changes into CMOS Status Page Setup Menu and Option Page Setup Menu -- Exit current page and return to Main Menu
F1	General help, only for Status Page Setup Menu and Option Page Setup Menu
F2	Load previous values
F3	Load optimized defaults
F4	Save changes and Exit BIOS
<K>	Scroll help area upwards
<M>	Scroll help area downwards

Table 5-1: BIOS Navigation Keys

5.1.2.2 Touch Navigation

For touchscreen navigation, use the on-screen navigation keys shown below.
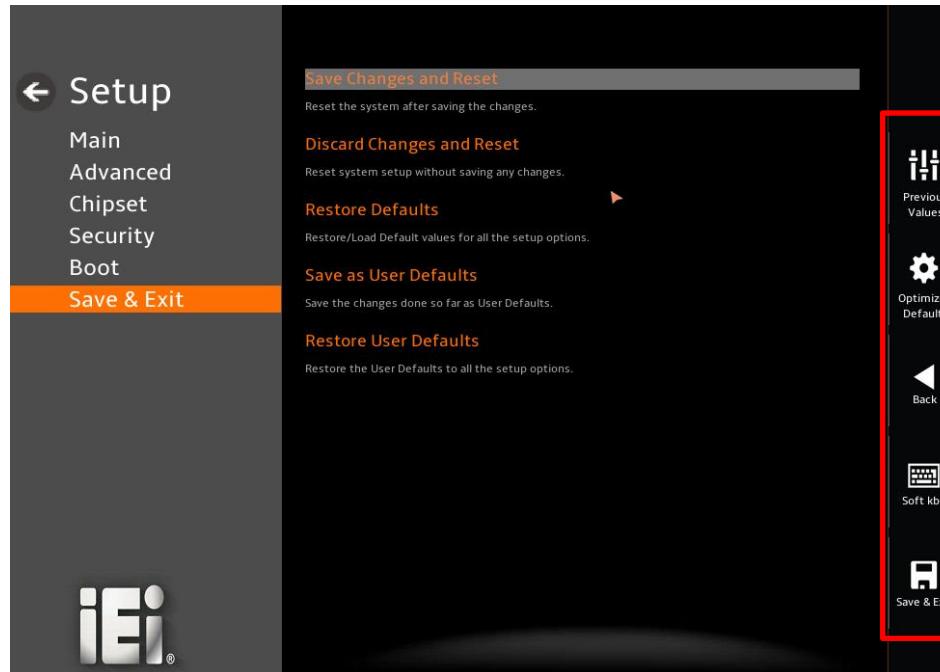
On-screen Button	Function
Previous Values	Load the last value you set.
Optimized Defaults	Load the factory default values in order to achieve the best performance.
Back	Return to the previous menu.
Soft kbd	Display the on-screen keyboard.
Save & Exit	Save the changes made to the BIOS options and reset the system.

Table 5-2: BIOS On-screen Navigation Keys

5.1.3 Getting Help

When **F1** is pressed a small help window describing the appropriate keys to use and the possible selections for the highlighted item appears. To exit the Help Window, press the **Esc** key.

5.1.4 Unable to Reboot after Configuration Changes

If the computer cannot boot after changes to the system configuration is made, CMOS defaults. Use the clear CMOS button described in **Chapter 4**.

5.1.5 BIOS Menu Bar

The **menu bar** on top of the BIOS screen has the following main items:

- Main – Changes the basic system configuration.
- Advanced – Changes the advanced system settings.
- Chipset – Changes the chipset settings.
- Security – Sets User and Supervisor Passwords.
- Boot – Changes the system boot configuration.
- Save & Exit – Selects exit options and loads default settings

The following sections completely describe the configuration options found in the menu items at the top of the BIOS screen and listed above.

5.2 Main

The **Main** BIOS menu appears when the **BIOS Setup** program is entered. The **Main** menu gives an overview of the basic system information.
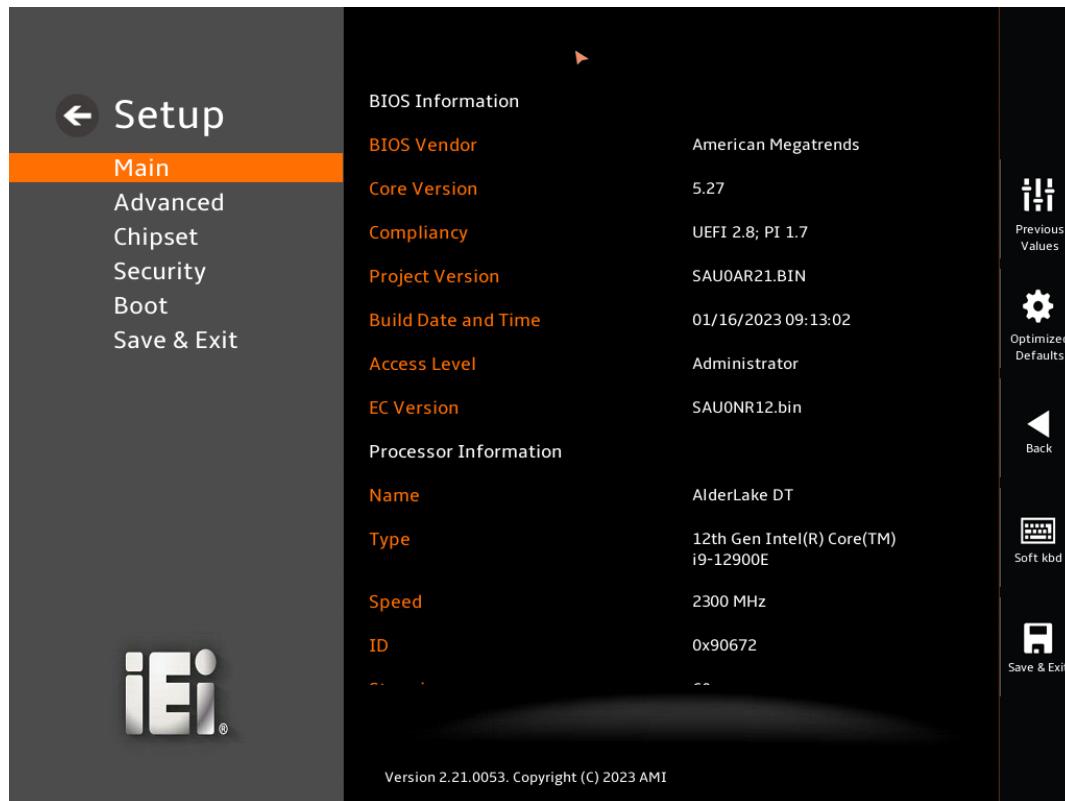
BIOS Menu 1: Main (1/3)
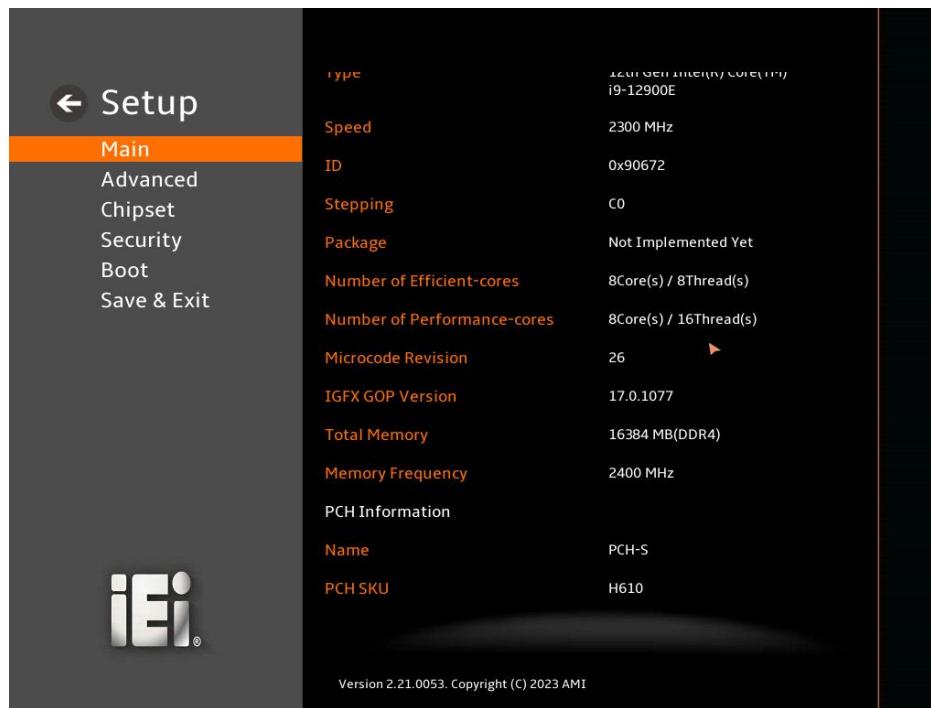
BIOS Menu 2: Main (2/3)
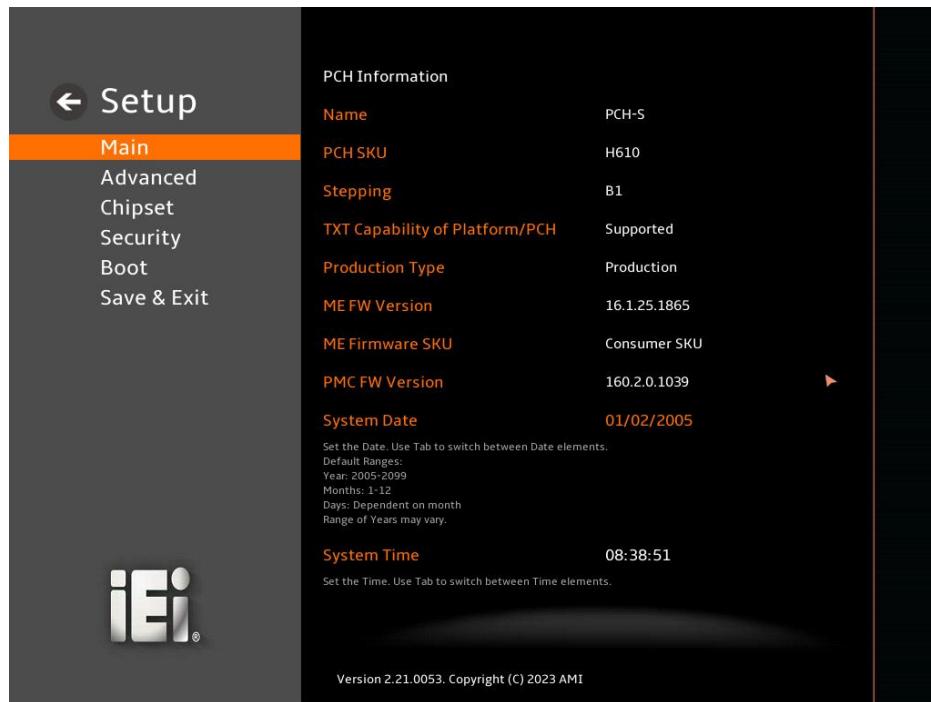
BIOS Menu 3: Main (3/3)

➔ **BIOS Information**

The **BIOS Information** lists a brief summary of the BIOS. The fields in **BIOS Information** cannot be changed. The items shown in the system overview include:

- **BIOS Vendor:** Installed BIOS vendor
- **Core Version:** Current BIOS version
- **Compliancy:** Current UEFI & PI version
- **Project Version:** the board version
- **Build Date and Time:** Date the current BIOS version was made
- **EC Version:** Current EC version

➔ **Processor Information**

The **Processor Information** lists a brief summary of the Processor. The fields in **Processor Information** cannot be changed. The items shown in the system overview include:

- **Name:** Displays the Processor Details
- **Type:** Displays the Processor Type
- **Speed:** Displays the Processor Speed
- **ID:** Displays the Processor ID

➔ **PCH Information**

The **PCH Information** lists a brief summary of the PCH. The fields in **PCH Information** cannot be changed. The items shown in the system overview include:

- **Name:** Displays the PCH Name
- **PCH SKU:** Displays the PCH SKU
- **Stepping:** Displays the PCH Stepping
- **TXT Capability of Platform/PCH:** Displays the TXT Capability
- **Production Type:** Displays the Production Type
- **ME FW Version:** Displays the ME Firmware Version
- **ME Firmware SKU:** Displays the ME Firmware SKU
- **PMC FW Version:** Displays the PMC Firmware Version

➔ **System Date [xx/xx/xx]**

Use the **System Date** option to set the system date. Manually enter the day, month and year.

➔ **System Time [xx:xx:xx]**

Use the **System Time** option to set the system time. Manually enter the hours, minutes and seconds.

5.3 Advanced

Use the **Advanced** menu (**BIOS Menu 4**) to configure the CPU and peripheral devices through the following sub-menus:

WARNING!

Setting the wrong values in the sections below may cause the system to malfunction. Make sure that the settings made are compatible with the hardware.
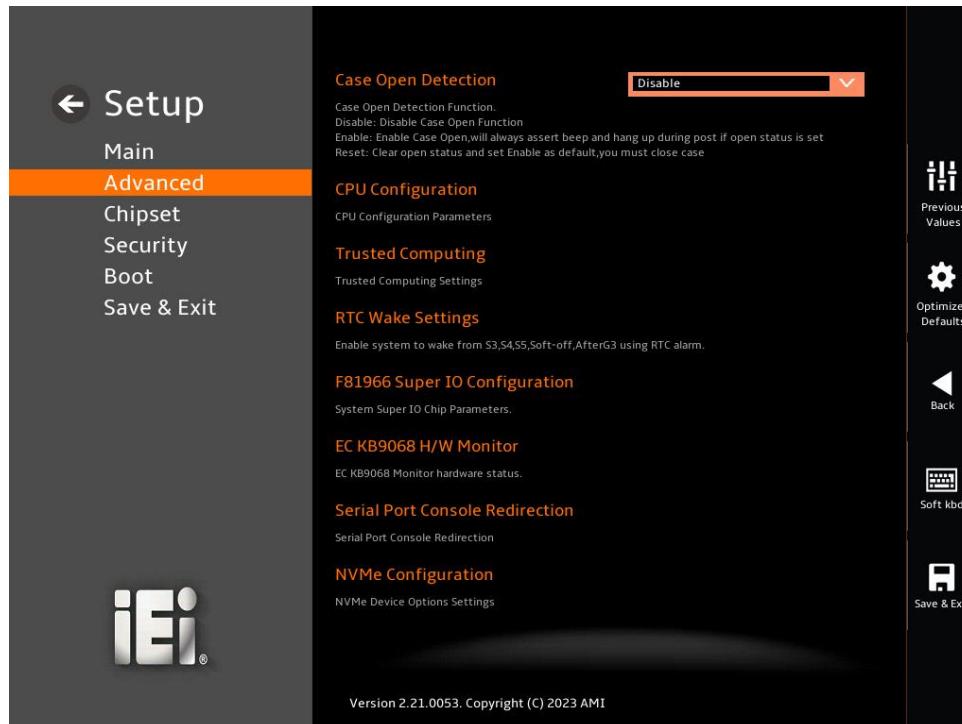
BIOS Menu 4: Advanced

5.3.1 Case Open Detection

➔ **Case Open Detection [Disabled]**

When the **Case Open Detection** is enabled, if anyone opens the computer's chassis, or case, Windows will notify the user with a pop-up message the next time he turns on his computer.

➔ **Disabled**   **DEFAULT**   Disables Case Open Detection.

➔ **Enabled**   Enables Case Open Detection.

5.3.2 CPU Configuration

Use the **CPU Configuration** menu (**BIOS Menu 5**) to view detailed CPU specifications or enable the Intel Virtualization Technology.
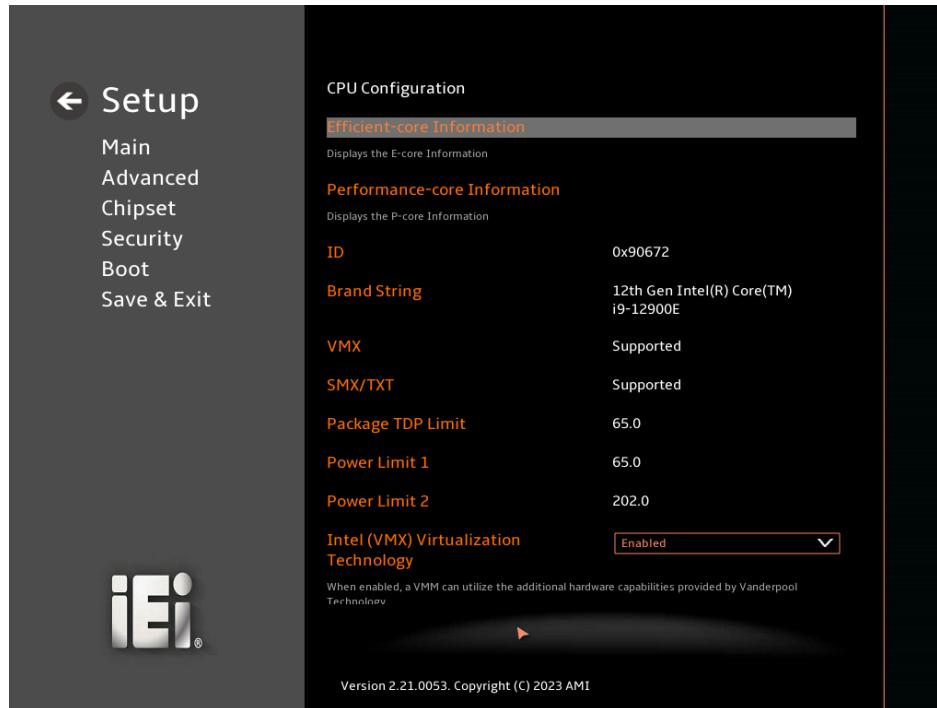
BIOS Menu 5: CPU Configuration (1/3)
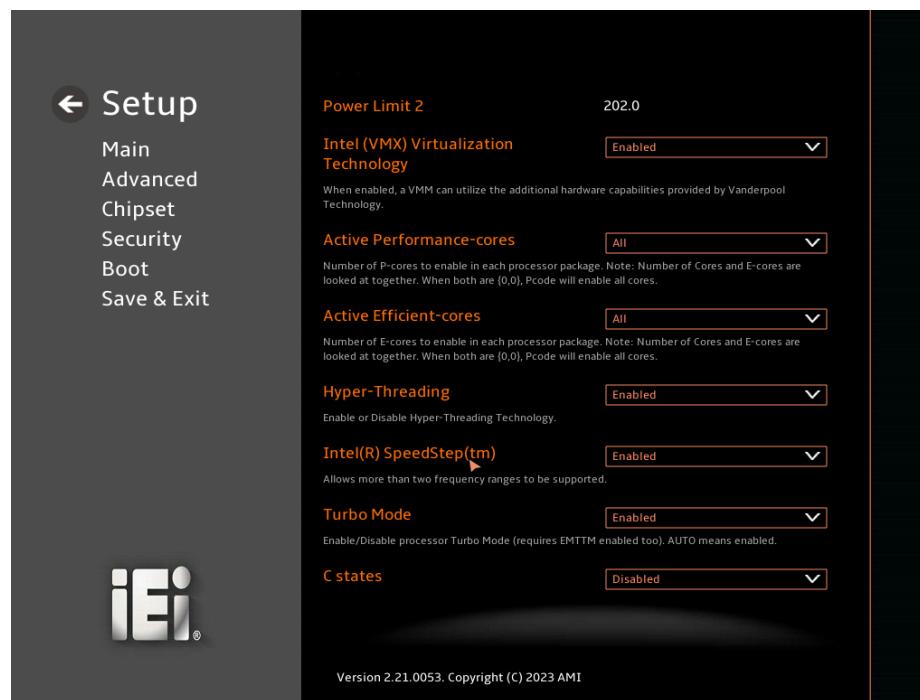
BIOS Menu 6: CPU Configuration (2/3)
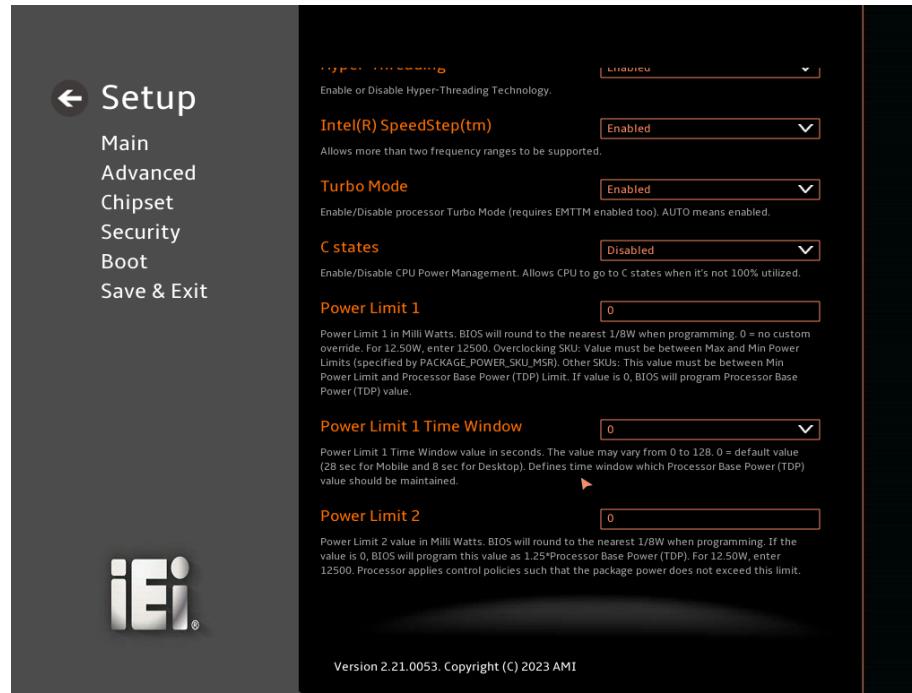
BIOS Menu 7: CPU Configuration (3/3)

➔ **Intel (VMX) Virtualization Technology [Enabled]**

Use the **Intel (VMX) Virtualization Technology** option to enable or disable virtualization on the system. When combined with third party software, Intel® Virtualization technology allows several OSs to run on the same system at the same time.

➔ **Disabled**   Disables Intel Virtualization Technology.

➔ **Enabled**   **DEFAULT**   Enables Intel Virtualization Technology.

➔ **Active Performance Cores [All]**

Use the **Active Performance Cores** BIOS option to enable numbers of P-cores in the processor package.

➔ **All**   **DEFAULT**   Enable all P-cores in the processor package.

➔ 1   Enable one P-core in the processor package.

➔ 2   Enable two P-core in the processor package.

➔ 3   Enable three P-core in the processor package

➔ 4   Enable four P-core in the processor package

➔ 5   Enable five P-core in the processor package

➔ 6   Enable six P-core in the processor package

➔ 7   Enable seven P-core in the processor package

➔ **Active Efficient Cores [All]**

Use the **Active Efficient Cores** BIOS option to enable numbers of E-cores in the processor package.

➔ **All**   **DEFAULT**   Enable all E-cores in the processor package.

➔ 0   Enable zero E-core in the processor package.

➔ 1   Enable one E-cores in the processor package.

➔ 2   Enable two E-cores in the processor package.

➔ 3   Enable three E-cores in the processor package.

➔ 4   Enable four E-cores in the processor package.

➔ 5   Enable five E-cores in the processor package.

➔ 6   Enable six E-cores in the processor package.

➔ 7   Enable seven E-cores in the processor package.

➔ **Hyper-Threading [Enabled]**

Use the **Hyper-Threading** option to enable or disable the **Hyper-Threading** Technology.

➔ **Disabled**   Disables Hyper-Threading Technology

➔ **Enabled**   **DEFAULT**   Enables Hyper-Threading Technology

➔ **Intel(R) SpeedStep(tm) [Enabled]**

Use the **Intel(R) SpeedStep(tm)** option to enable or disable the Intel® SpeedStep Technology which allows more than two frequency ranges to be supported.

➔ **Disabled**   Disables Intel® SpeedStep Technology

➔ **Enabled**   **DEFAULT**   Enables Intel® SpeedStep Technology

➔ **C states [Disabled]**

Use the **C states** option to enable or disable CPU power management which allows CPU to go to C states when it is not 100% utilized.

➔ **Disabled**   **DEFAULT**   Disables CPU power management

➔ **Enabled**   Enables CPU power management

➔ **Power Limit 1 [0]**

Use the + or – key to change the **Power Limit 1** value. BIOS will program the default values for Limit 1 and Power Limit 1 Time Window. For 12.50W, enter 12500.

➔   **Power Limit 1 Time Window [0]**

Use the **Power Limit 1 Time Window** option to select the PL1 time duration. The value may vary from 0 to 128. For 0 is the default value

➔   **Power Limit 2 [200000]**

Use the + or – key to change the **Power Limit 2** value. BIOS will round to the nearest 1/8W when programming. 0 = no custom override. For 12.50W, enter 12500.

5.3.3 Trusted Computing

Use the **Trusted Computing** menu (**BIOS Menu 8**) to configure settings related to the Trusted Computing Group (TCG) Trusted Platform Module (TPM).

BIOS Menu 8: Trusted Computing Configuration

➔ **TPM Support [Enable]**

Use the **TPM Support** option to enable or disable BIOS support for security device.

➔ **Disable**　　　　　　TPM support is disabled.

➔ **Enable**　　**DEFAULT**　　TPM support is enabled.

➔ **Pending Operation [None]**

Use the **Pending Operation** option to schedule an operation for the security device.

➔ **None**　　**DEFAULT**　　TPM information is previous.S

➔ **TPM Clear**　　　　　　TPM information is cleared

5.3.4 RTC Wake Settings

The **RTC Wake Settings** menu (**BIOS Menu 9**) configures RTC wake event.
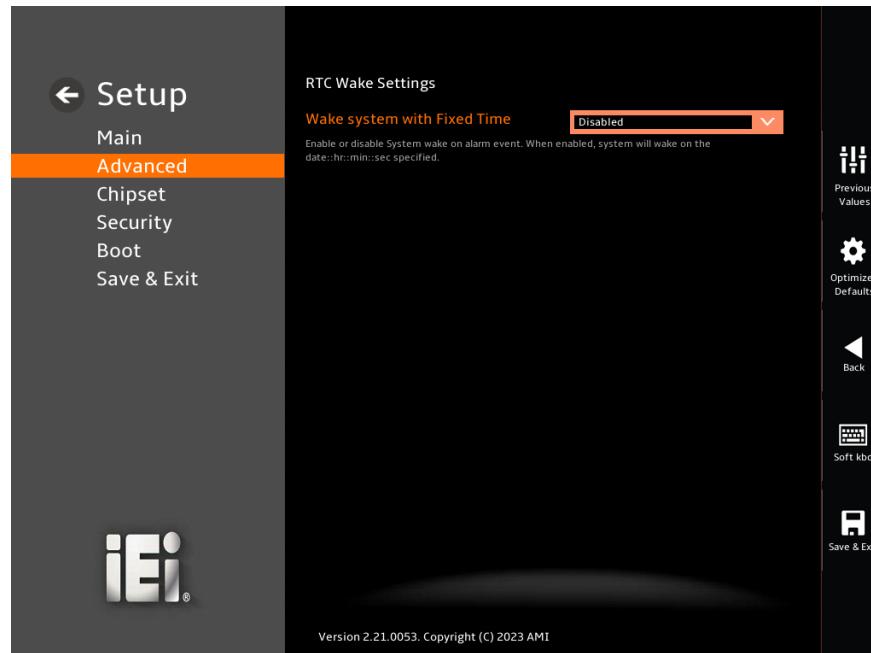
BIOS Menu 9: RTC Wake Settings (1/2)
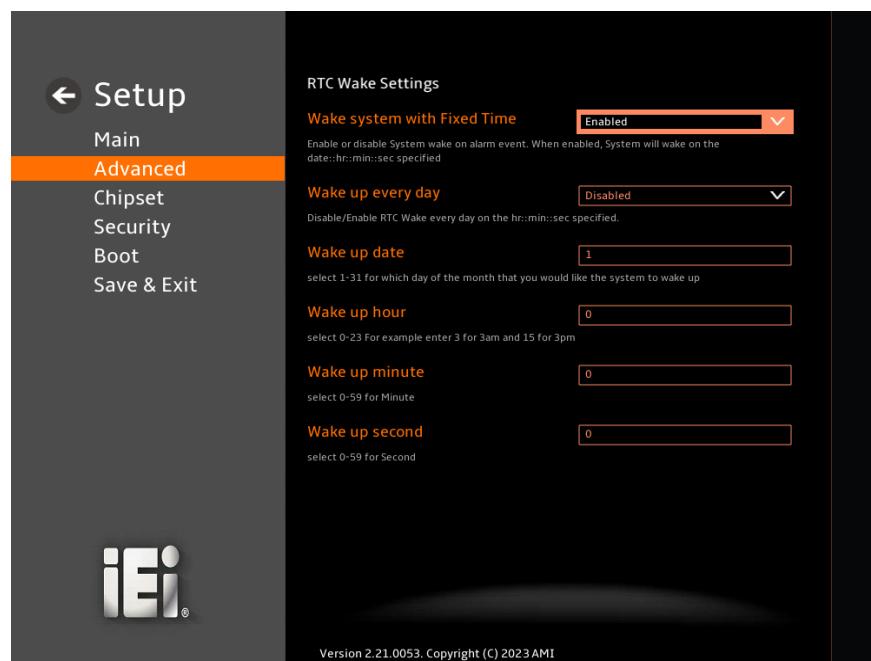
BIOS Menu 10: RTC Wake Settings (2/2)

➔ **Wake system with Fixed Time [Enabled]**

Use the **Wake system with Fixed Time** option to enable or disable the system wake on alarm event.

➔ **Disabled**　　　　　The real time clock (RTC) cannot generate a wake event

➔ **Enabled**　　DEFAULT　If selected, the **Wake up every day** option appears allowing you to enable to disable the system to wake every day at the specified time. Besides, the following options appear with values that can be selected:

　　　　Wake up date

　　　　Wake up hour

　　　　Wake up minute

　　　　Wake up second

After setting the alarm, the computer turns itself on from a suspend state when the alarm goes off.

5.3.5 F81966 Super IO Configuration

Use the **F81966 Super IO Configuration** menu (**BIOS Menu 11**) to set or change the configurations for serial ports.
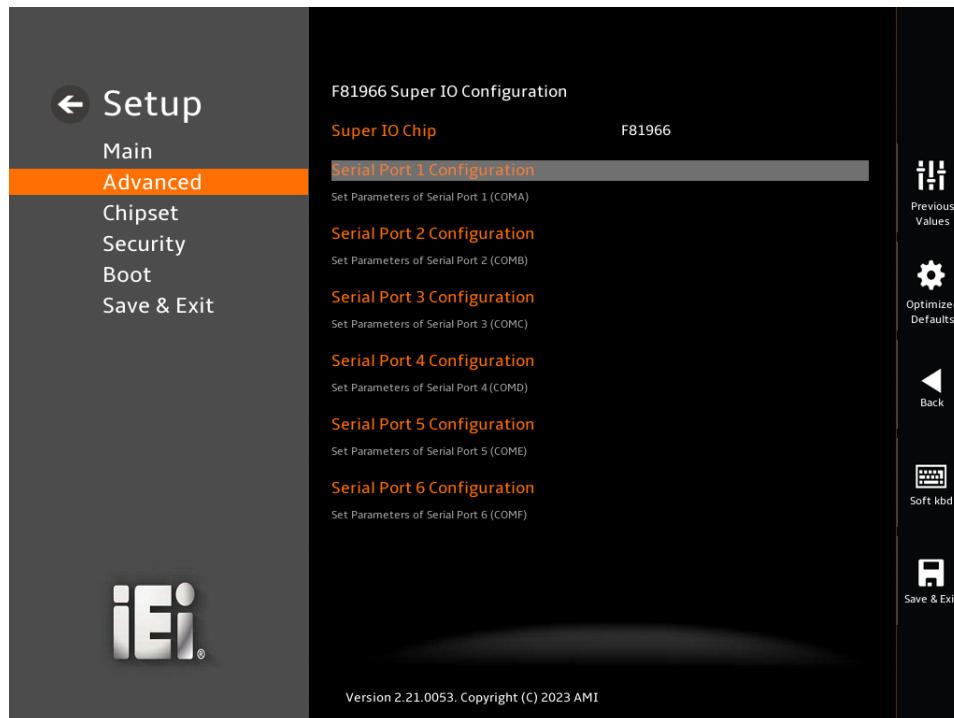
BIOS Menu 11: F81866 Super IO Configuration

5.3.5.1 Serial Port 1 Configuration

Use the **Serial Port 1 Configuration** menu (**BIOS Menu 12**) to configure the serial port.
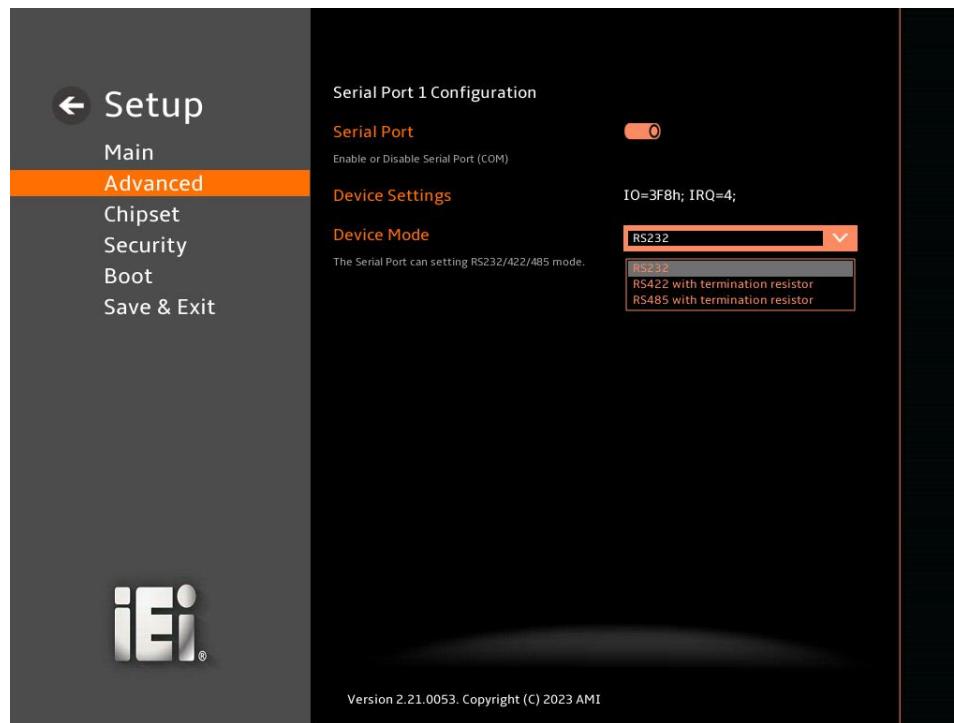
BIOS Menu 12: Serial Port 1 Configuration Menu

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

  ➔ **Disabled**   Disable the serial port

  ➔ **Enabled**   **DEFAULT**   Enable the serial port

➔ **Device Settings**

The **Device Settings** option shows the serial port IO port address and interrupt address.

  ➔ **IO=3F8h;**   Serial Port I/O port address is 3F8h and the interrupt
    IRQ=4   address is IRQ4

➔ **Device Mode [RS232]**

Use the **Device Mode** option to change the serial port mode.

➔ **RS232** DEFAULT The serial port mode is RS-232

RS422 with termination resistor The serial port mode is RS-422

RS485 with termination resistor The serial port mode is RS-485

5.3.5.2 Serial Port 2 Configuration

Use the **Serial Port 2 Configuration** menu (**BIOS Menu 13**) to configure the serial port.

BIOS Menu 13: Serial Port 2 Configuration Menu

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

> ➔ **Disabled** Disable the serial port

> ➔ **Enabled** **DEFAULT** Enable the serial port

➔ **Device Settings**

The **Device Settings** option shows the serial port IO port address and interrupt address.

> ➔ **IO=2F8h;** Serial Port I/O port address is 2F8h and the interrupt
>   **IRQ=3** address is IRQ3

➔ **Device Mode [RS232]**

Use the **Device Mode** option to change the serial port mode.

➔	**RS232**	DEFAULT	The serial port mode is RS-232
	RS422 with termination resistor		The serial port mode is RS-422
	RS485 with termination resistor		The serial port mode is RS-485

5.3.5.3 Serial Port 3 Configuration

Use the **Serial Port 3 Configuration** menu (**BIOS Menu 14**) to configure the serial port.
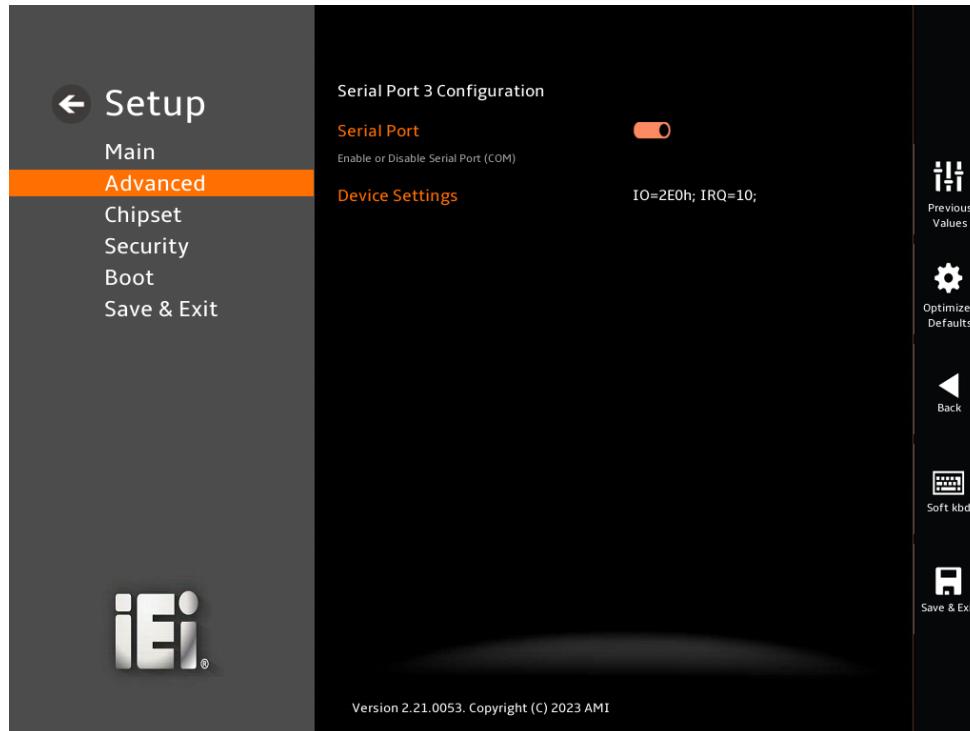
BIOS Menu 14: Serial Port 3 Configuration Menu

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

➔ **Disabled**   Disable the serial port

➔ **Enabled**   **DEFAULT**   Enable the serial port

➔ **Device Settings**

The **Device Settings** option shows the serial port IO port address and interrupt address.

➔ **IO=2E0h;**   Serial Port I/O port address is 2E0h and the interrupt
   IRQ=10   address is IRQ10

5.3.5.4 Serial Port 4 Configuration

Use the **Serial Port 4 Configuration** menu (**BIOS Menu 15**) to configure the serial port.
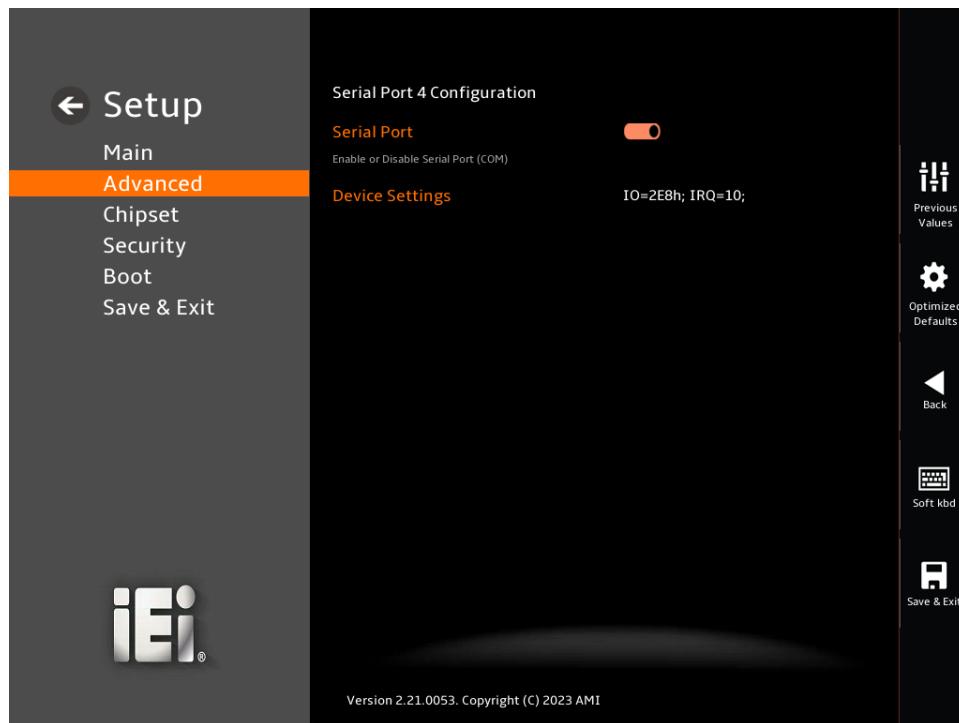
BIOS Menu 15: Serial Port 4 Configuration Menu

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

    ➔ **Disabled**   Disable the serial port

    ➔ **Enabled**   DEFAULT   Enable the serial port

➔ **Device Settings**

The **Device Settings** option shows the serial port IO port address and interrupt address.

    ➔ **IO=2E8h;**   Serial Port I/O port address is 2E8h and the interrupt
       IRQ=10   address is IRQ10

5.3.5.5 Serial Port 5 Configuration

Use the **Serial Port 5 Configuration** menu (**BIOS Menu 16**) to configure the serial port.

Serial Port 5 Configuration

Serial Port

Enable or Disable Serial Port (COM)

Device Settings   IO=2C0h; IRQ=10;

Version 2.21.0053. Copyright (C) 2023 AMI
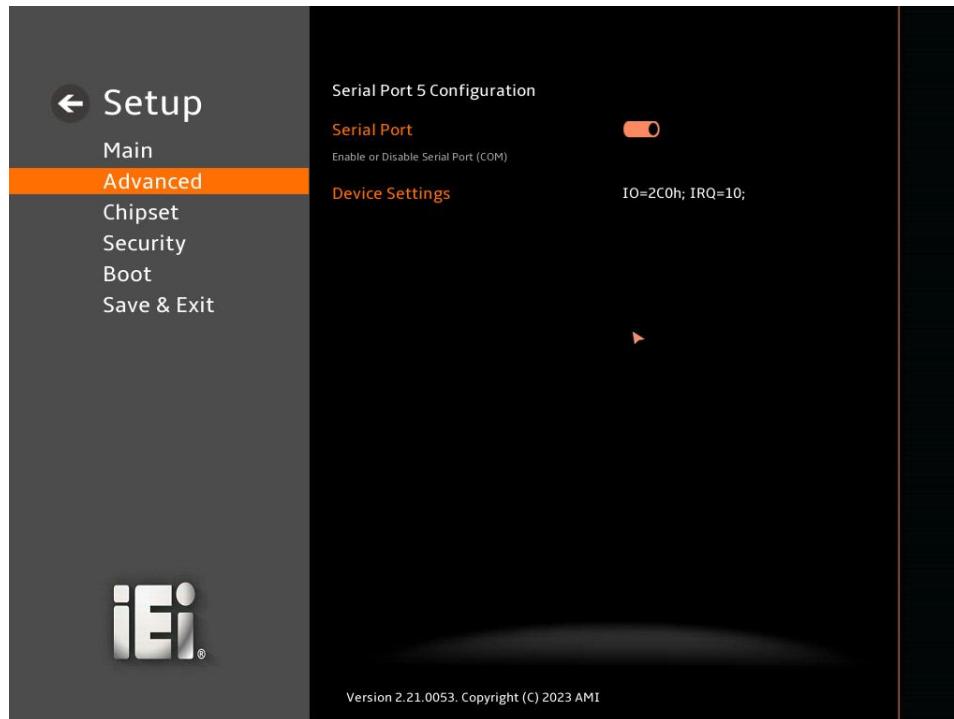
BIOS Menu 16: Serial Port 5 Configuration Menu

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

  ➔ **Disabled**   Disable the serial port

  ➔ **Enabled**   DEFAULT   Enable the serial port

➔ **Device Settings**

The **Device Settings** option shows the serial port IO port address and interrupt address.

  ➔ **IO=2C0h;**   Serial Port I/O port address is 2C0h and the interrupt
    IRQ=10   address is IRQ10

5.3.5.6 Serial Port 6 Configuration

Use the **Serial Port 6 Configuration** menu (**BIOS Menu 17**) to configure the serial port.
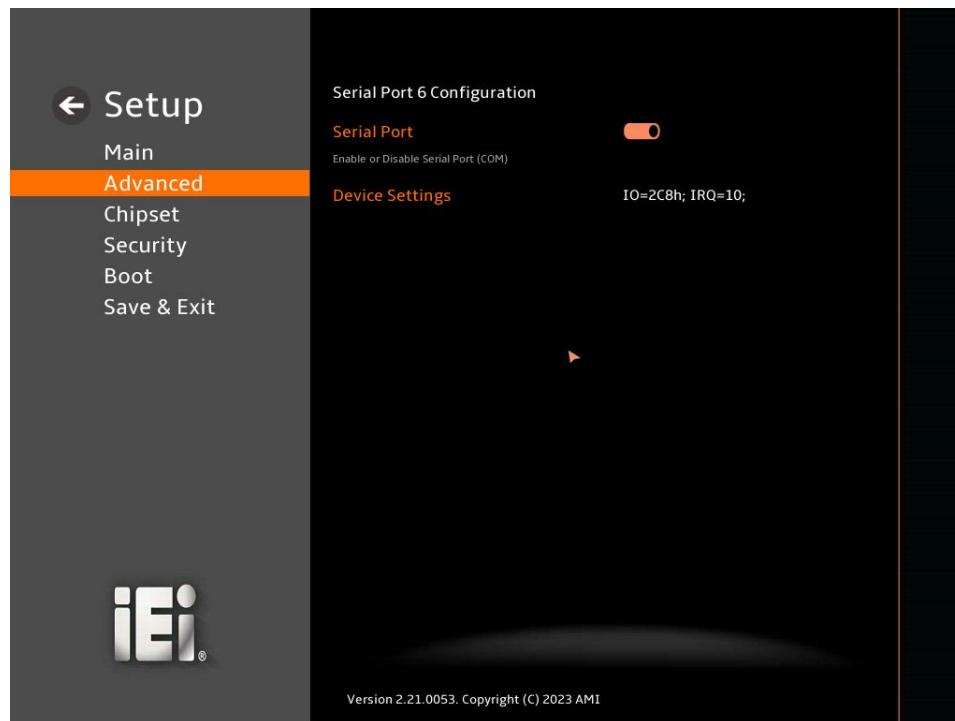
BIOS Menu 17: Serial Port 6 Configuration Menu

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

➔ **Disabled**   Disable the serial port

➔ **Enabled** DEFAULT Enable the serial port

➔ **Device Settings**

The **Device Settings** option shows the serial port IO port address and interrupt address.

➔ **IO=2C8h;**   Serial Port I/O port address is 2C8h and the interrupt
IRQ=10   address is IRQ10

5.3.6 EC KB9068 H/W Monitor

The **EC KB9068 H/W Monitor** menu (**BIOS Menu 18**) contains the smart fan mode configuration submenu and shows the state of H/W real-time operating temperature, fan speeds and system voltages.
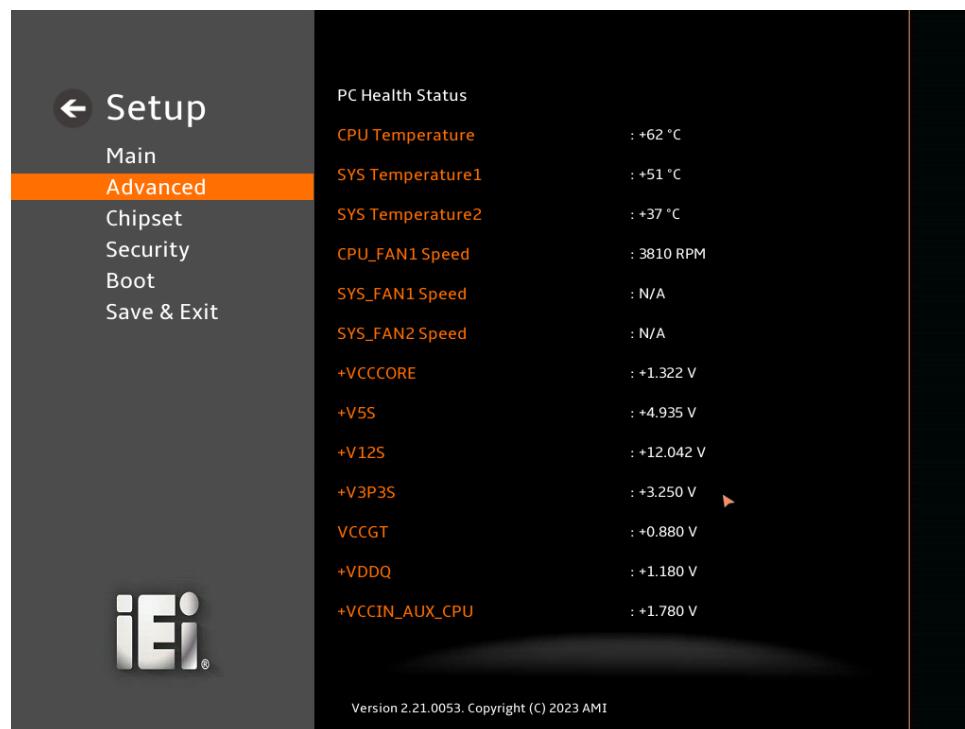
BIOS Menu 18: EC KB9068 H/W Monitor (1/2)
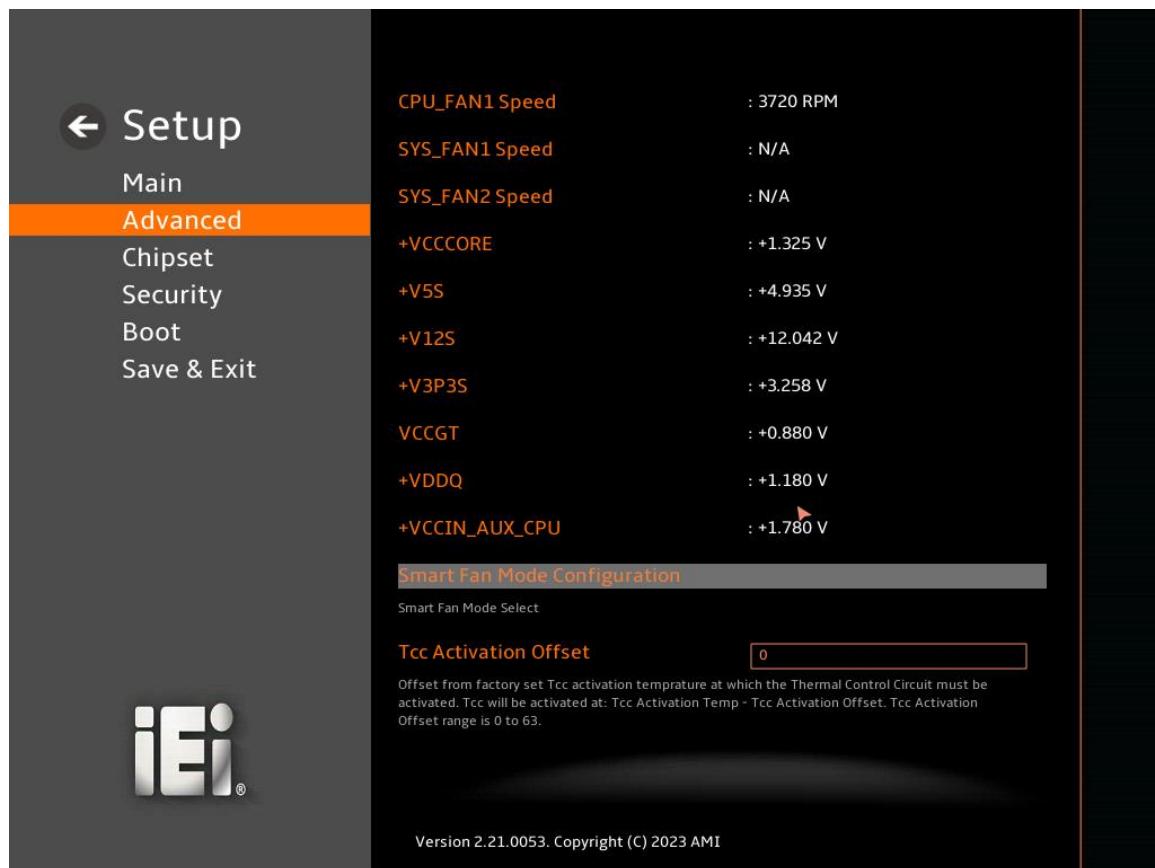
BIOS Menu 19: EC KB9068 H/W Monitor (2/2)

➔ **PC Health Status**

The following system parameters and values are shown. The system parameters that are monitored are:

- System Temperatures:
  - ○ CPU Temperature
  - ○ System Temperature1
  - ○ System Temperature2

- Fan Speeds:
  - ○ CPU_Fan1 Speed
  - ○ SYS_Fan1 Speed
  - ○ SYS_Fan2 Speed

- Voltages:
  - ○ +VCCCORE
  - ○ +5VS
  - ○ +12S
  - ○ +3VP3S
  - ○ VCCGT
  - ○ +VDDQ
  - ○ VCCIN_AUX_CPU

➔ **Tcc Activation Offset [0]**

Offset from factoryset Tcc activation temprature at which the Thermal Control Circuit must be activated. Tcc will be activated at: Tcc Activation Temp-Tcc Activation Offset.Tcc Activation Offset range is 0 to 63.

5.3.6.1 Smart Fan Mode Configuration

Use the **Smart Fan Mode Configuration** submenu (**BIOS Menu 20**) to configure the CPU/system fan start/off temperature and control mode.
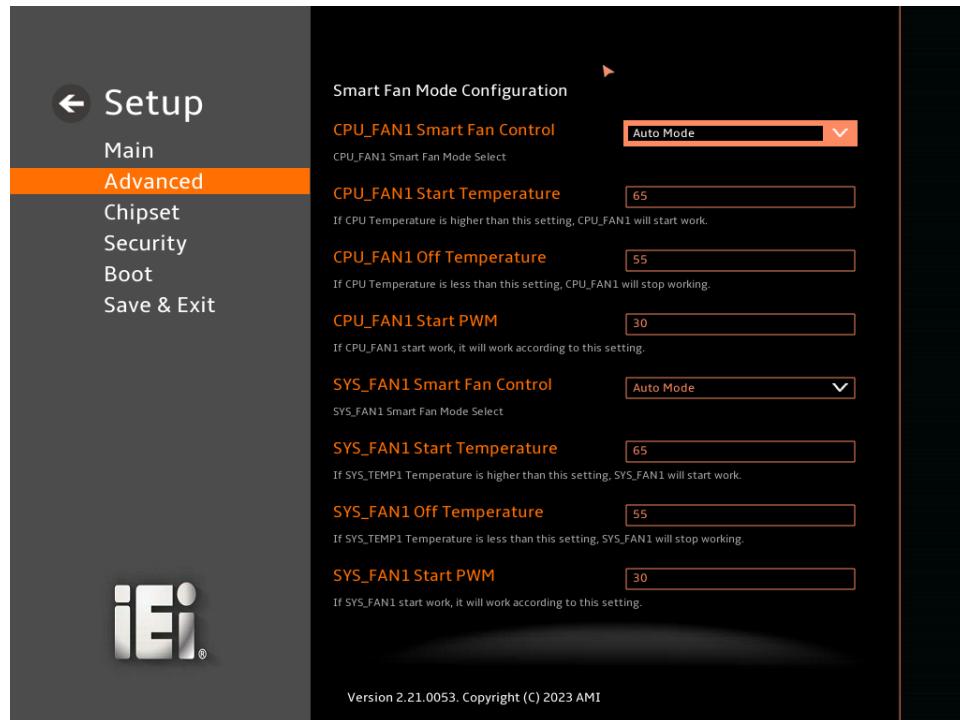
BIOS Menu 20: Smart Fan Mode Configuration (1/2)
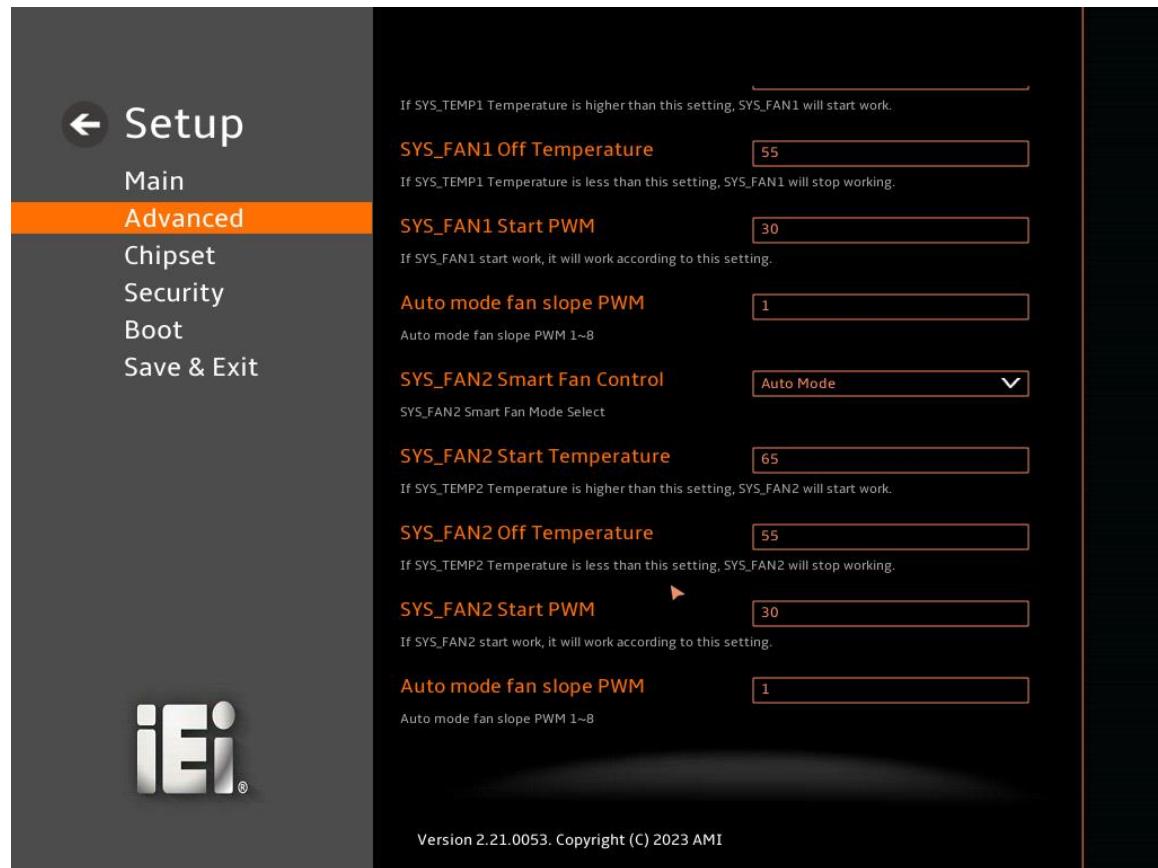
BIOS Menu 21: Smart Fan Mode Configuration (2/2)

➔ **CPU_FAN1 Smart Fan Control [Auto Mode]**

Use the **CPU_FAN1 Smart Fan Control** option to configure the CPU Smart Fan.

➔ **Manual Mode**		The fan spins at the speed set in Manual Mode settings.
➔ **Auto Mode**	DEFAULT	The fan adjusts its speed using Auto Mode settings.

➔ **CPU_FAN1 Start Temperature**

If the CPU temperature is between **fan off** and **fan start**, the fan speed change to **fan start PWM**. To set a value, Use the + or – key to change the value or enter a decimal number between 1 and 100.

➔ **CPU_FAN1 Off Temperature**

If the CPU temperature is lower than the value set this option, the fan speed change to be lowest. To set a value, Use the + or – key to change the value or enter a decimal number between 1 and 100.

➔ **CPU_FAN1 Start PWM**

Use the **CPU_FAN Start PWM** option to set the PWM start value. Use the + or – key to change the value or enter a decimal number between 1 and 100.

➔ **SYS_FAN1 Smart Fan Control [Auto Mode]**

Use the **SYS_FAN1 Smart Fan Control** option to configure the System Smart Fan.

➔ **Manual Mode**   The fan spins at the speed set in Manual Mode settings.

➔ **Auto Mode**   DEFAULT   The fan adjusts its speed using Auto Mode settings.

➔ **SYS_FAN1 Start Temperature**

If the System temperature is between **fan off** and **fan start**, the fan speed change to **fan start PWM**. To set a value, Use the + or – key to change the value or enter a decimal number between 1 and 100.

➔ **SYS_FAN1 Off Temperature**

If the System temperature is lower than the value set this option, the fan speed change to be lowest. To set a value, Use the + or – key to change the value or enter a decimal number between 1 and 100.

➔ **SYS_FAN1 Start PWM**

Use the **SYS_Fan1 Start PWM** option to set the PWM start value. Use the + or – key to change the value or enter a decimal number between 1 and 100.

➔ **Auto mode fan slope PWM**

The PWM slope refers to the ratio relationship between the fan speed and the temperature. When the temperature rises or falls by 1°C, the fan speed increases or decreases accordingly. Changing the slope requires entering a number between 1 and 8.

➔ **CPU_FAN2 Smart Fan Control [Auto Mode]**

Use the **CPU_FAN2 Smart Fan Control** option to configure the CPU Smart Fan.

➔ **Manual Mode** The fan spins at the speed set in Manual Mode settings.

➔ **Auto Mode** **DEFAULT** The fan adjusts its speed using Auto Mode settings.

➔ **SYS_FAN2 Start Temperature**

If the System temperature is between **fan off** and **fan start**, the fan speed change to **fan start PWM**. To set a value, Use the + or – key to change the value or enter a decimal number between 1 and 100.

➔ **SYS_FAN2 Off Temperature**

If the System temperature is lower than the value set this option, the fan speed change to be lowest. To set a value, Use the + or – key to change the value or enter a decimal number between 1 and 100.

➔ **SYS_FAN2 Start PWM**

Use the **SYS_Fan2 Start PWM** option to set the PWM start value. Use the + or – key to change the value or enter a decimal number between 1 and 100.

➔ **Auto mode fan slope PWM**

The PWM slope refers to the ratio relationship between the fan speed and the temperature. When the temperature rises or falls by 1°C, the fan speed increases or decreases accordingly. Changing the slope requires entering a number between 1 and 8.

5.3.7 Serial Port Console Redirection

The **Serial Port Console Redirection** menu (**BIOS Menu 22**) allows the console redirection options to be configured. Console Redirection allows users to maintain a system remotely by re-directing keyboard input and text output through the serial port.
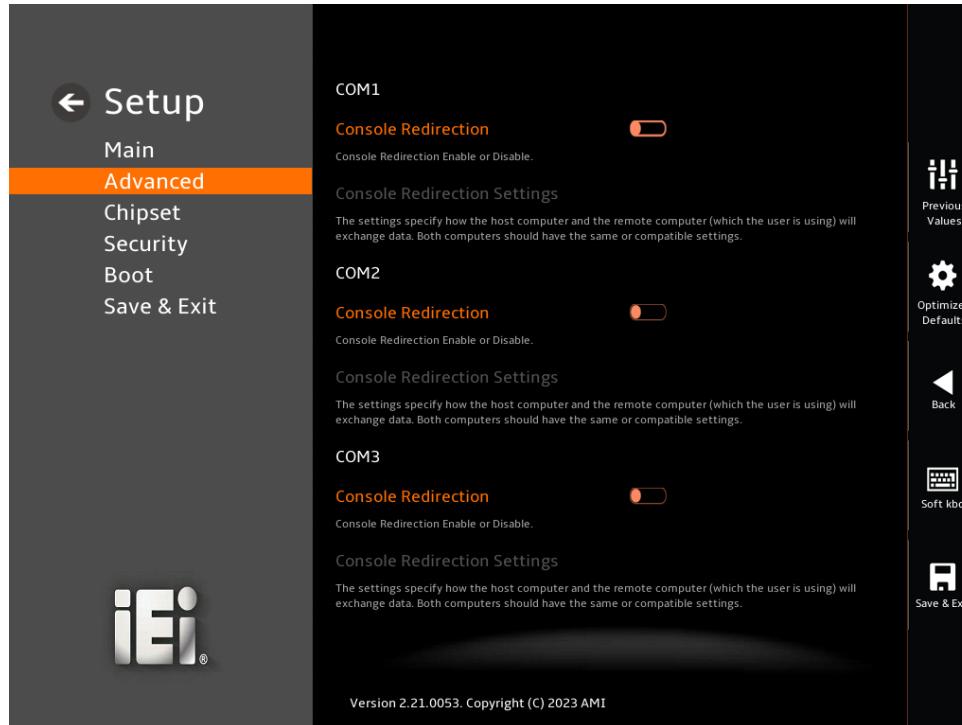
BIOS Menu 22: Serial Port Console Redirection (1/2)
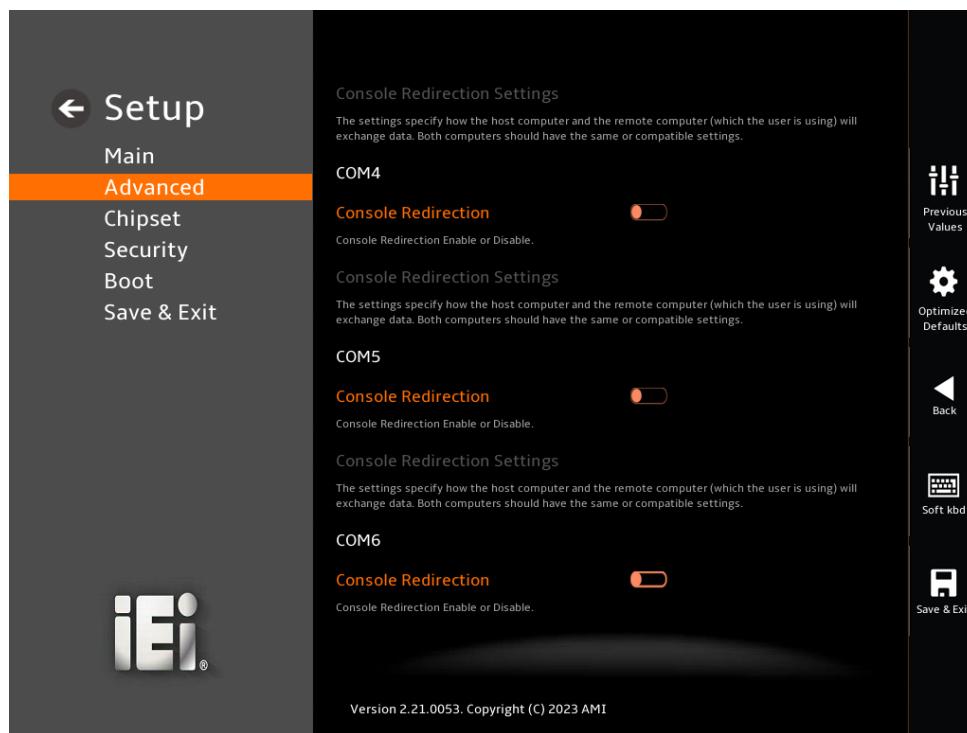
BIOS Menu 23: Serial Port Console Redirection (2/2)

➔ **Console Redirection [Disabled]**

Use **Console Redirection** option to enable or disable the console redirection function.

➔ **Disabled**   DEFAULT   Disabled the console redirection function

➔ **Enabled**   Enabled the console redirection function

The **Console Redirection Settings** submenu will be available when the **Console Redirection** option is enabled.

5.3.7.1 Console Redirection Settings

The following options are available in the **Console Redirection Settings** submenu **(BIOS Menu 24)** when the **COM Console Redirection** (for COM1 to COM6) option is enabled.
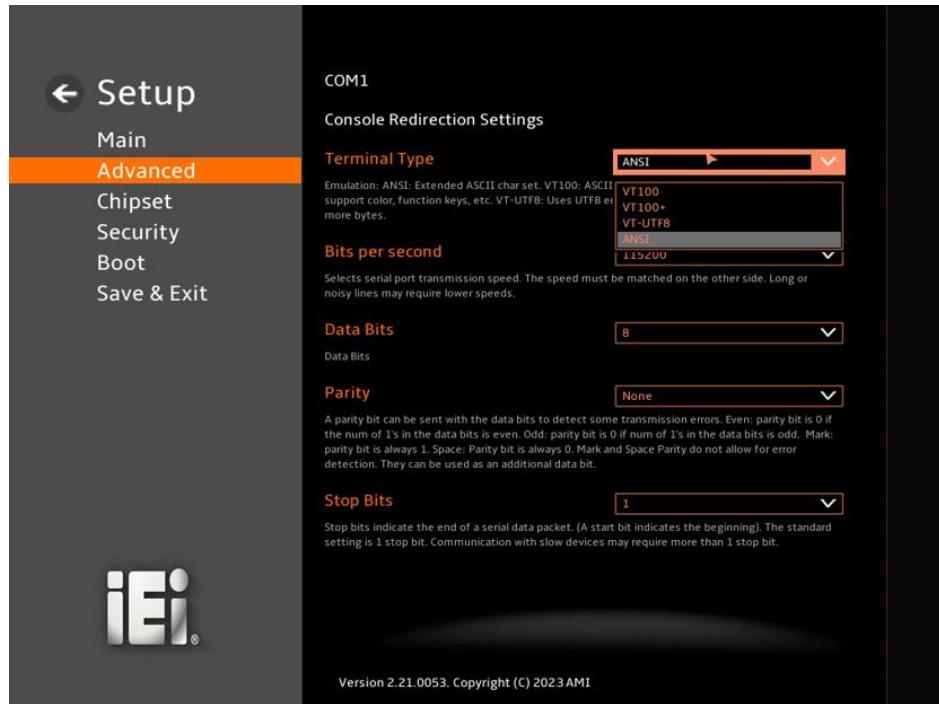
BIOS Menu 24: COM Console Redirection Settings

➔ **Terminal Type [ANSI]**

Use the **Terminal Type** option to specify the remote terminal type.

➔ **VT100**   The target terminal type is VT100

➔ **VT100+**   The target terminal type is VT100+

➔ **VT-UTF8**   The target terminal type is VT-UTF8

➔ **ANSI**   DEFAULT   The target terminal type is ANSI

➔ **Bits per second [115200]**

Use the **Bits per second** option to specify the serial port transmission speed. The speed must match on the other side. Long or noisy lines may require lower speeds.

➔ **9600**　　　　　　　　Sets the serial port transmission speed at 9600.

➔ **19200**　　　　　　　Sets the serial port transmission speed at 19200.

➔ **38400**　　　　　　　Sets the serial port transmission speed at 38400.

➔ **57600**　　　　　　　Sets the serial port transmission speed at 57600.

➔ **115200**　　DEFAULT　Sets the serial port transmission speed at 115200.

➔ **Data Bits [8]**

Use the **Data Bits** option to specify the number of data bits.

➔ **7**　　　　　　　　Sets the data bits at 7.

➔ **8**　　DEFAULT　　Sets the data bits at 8.

➔ **Parity [None]**

Use the **Parity** option to specify the parity bit that can be sent with the data bits for detecting the transmission errors.

➔ **None**　　DEFAULT　No parity bit is sent with the data bits.

➔ **Even**　　　　　　The parity bit is 0 if the number of ones in the data bits is even.

➔ **Odd**　　　　　　The parity bit is 0 if the number of ones in the data bits is odd.

➔ **Mark**　　　　　　The parity bit is always 1. This option does not allow for error detection.

➔ **Space**　　　　　The parity bit is always 0. T This option does not allow for error detection.

➔ **Stop Bits [1]**

Use the **Stop Bits** option to specify the number of stop bits used to indicate the end of a serial data packet. Communication with slow devices may require more than 1 stop bit.

➔ **1**　　DEFAULT　　Sets the number of stop bits at 1.

è   **2**   Sets the number of stop bits at 2.

5.3.8 NVMe Configuration

Use the **NVMe Configuration** (**BIOS Menu 25**) menu to display the NVMe controller and device information.
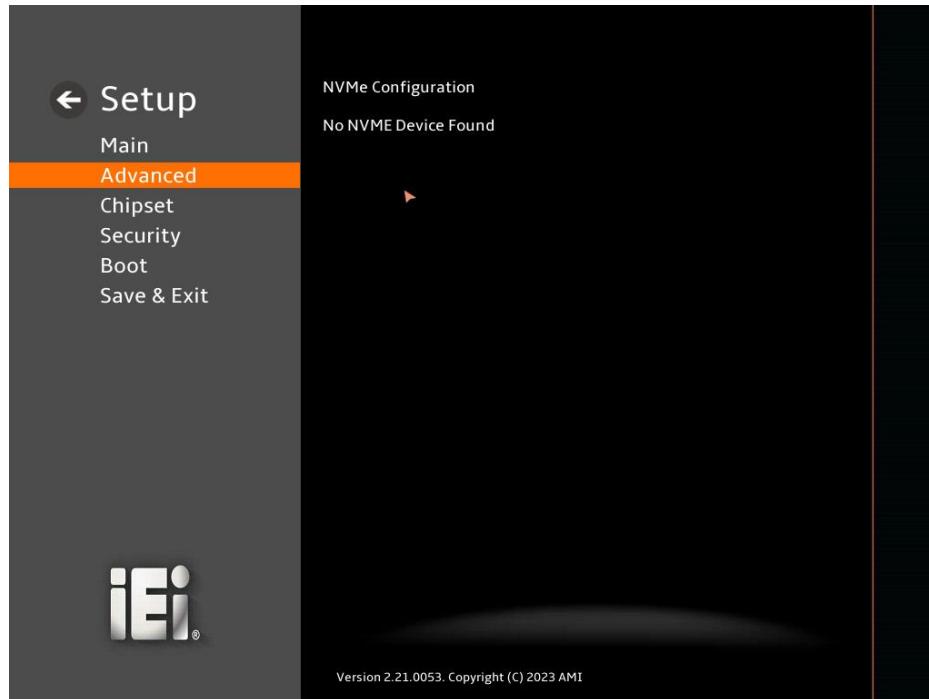
BIOS Menu 25: NVMe Configuration

5.4 Chipset

Use the **Chipset** menu (**BIOS Menu 26**) to access the PCH IO and System Agent (SA) configuration menus.

WARNING!

Setting the wrong values for the Chipset BIOS selections in the Chipset BIOS menu may cause the system to malfunction.

BIOS Menu 26: Chipset

5.4.1 System Agent (SA) Configuration

Use the **System Agent (SA) Configuration** menu (**BIOS Menu 27**) to configure the System Agent (SA) parameters.
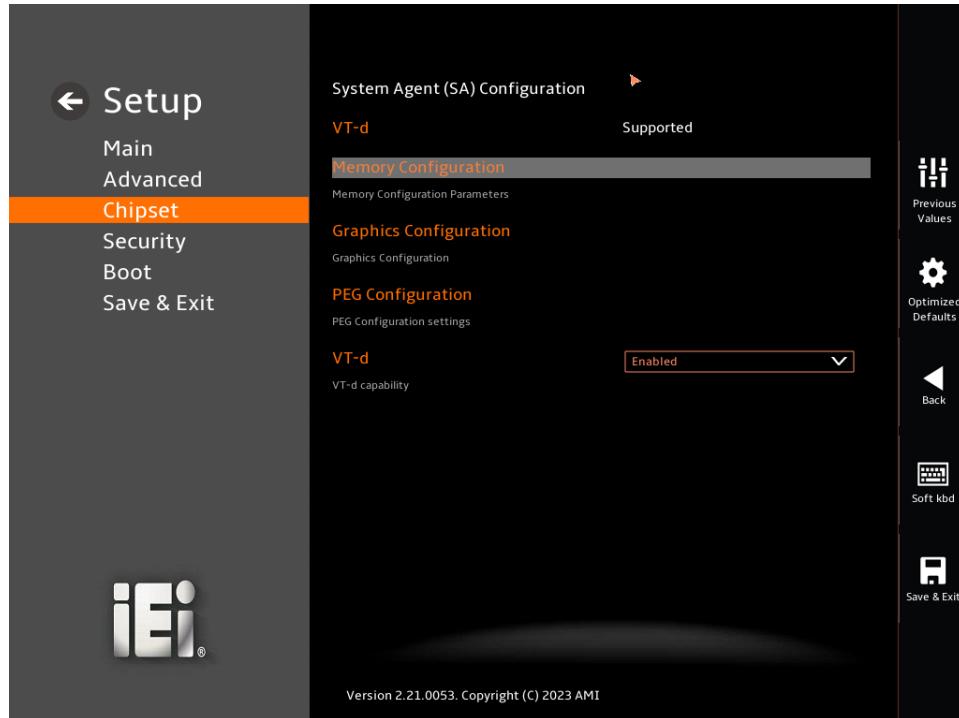
BIOS Menu 27: System Agent (SA) Configuration

➔ **VT-d [Enabled]**

Use the **VT-d** option to enable or disable the VT-d capability.

➔ **Disabled**   Disable the VT-d capability

➔ **Enabled**   DEFAULT   Enable the VT-d capability

5.4.1.1 Memory Configuration

Use the **Memory Configuration** submenu (**BIOS Menu 28**) to view memory information.

BIOS Menu 28: Memory Configuration

5.4.1.2 Graphics Configuration

Use the **Graphics Configuration** (**BIOS Menu 29**) menu to configure the video device connected to the system.
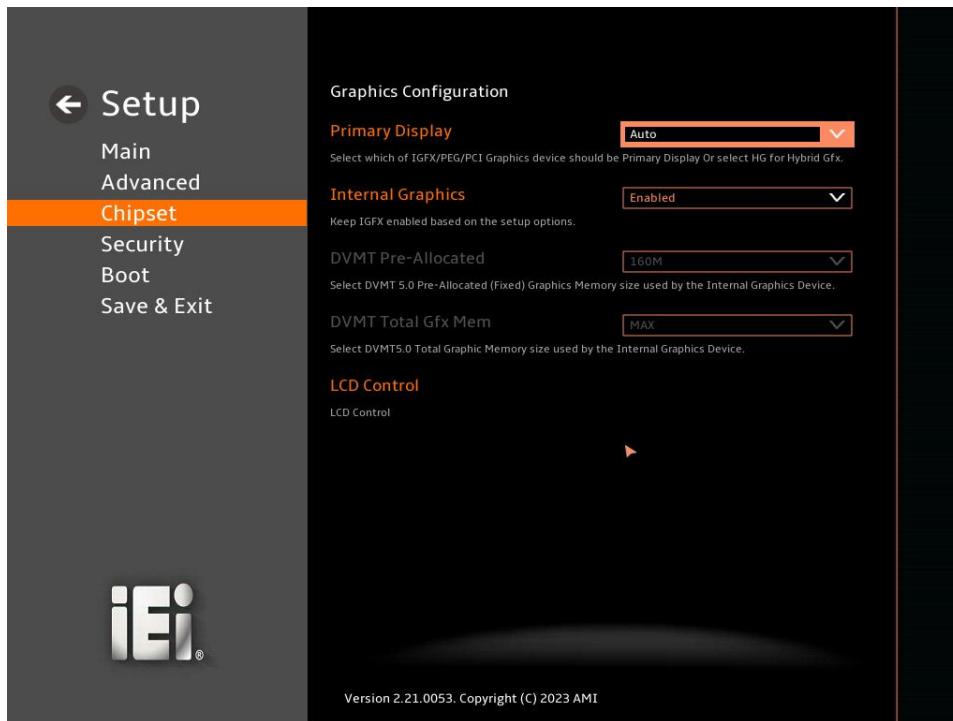
BIOS Menu 29: Graphics Configuration

➔ **Primary Display [Auto]**

Use the **Primary Display** option to select the primary graphics controller the system uses. The following options are available:

- Auto   **Default**
- IGFX
- PEG
- PCI
- SG

➔ **Internal Graphics [Enabled]**

Use the Internal Graphics option to configure whether to keep IGFX enabled. If user wants to support dual display by internal graphics and external graphics, this Internal Graphics

option should be set to Enabled and the above Primary Display option should be set to IGFX.

➔	**Auto**		Auto mode
➔	**Disabled**		Disables IGFX.
➔	**Enabled**	**Default**	Enables IGFX.

➔ **DVMT Pre-Allocated [160M]**

Use the **DVMT Pre-Allocated** option to set the amount of system memory allocated to the integrated graphics processor when the system boots. The system memory allocated can then only be used as graphics memory, and is no longer available to applications or the operating system. Configuration options are listed below:

▪ 160M **Default**

➔ **DVMT Total Gfx Mem [MAX]**

Use the **DVMT Total Gfx Mem** option to select DVMT5.0 total graphic memory size used by the internal graphic device. The following options are available:

▪ MAX **Default**

➔ **LCD Control**

LCD controller is used to control LVDS backlight PWM mode and iDPM Slot Status (No Module Installed). The following options are available:

▪ Invert **Default**
▪ Normal

5.4.1.3 PEG Port Configuration
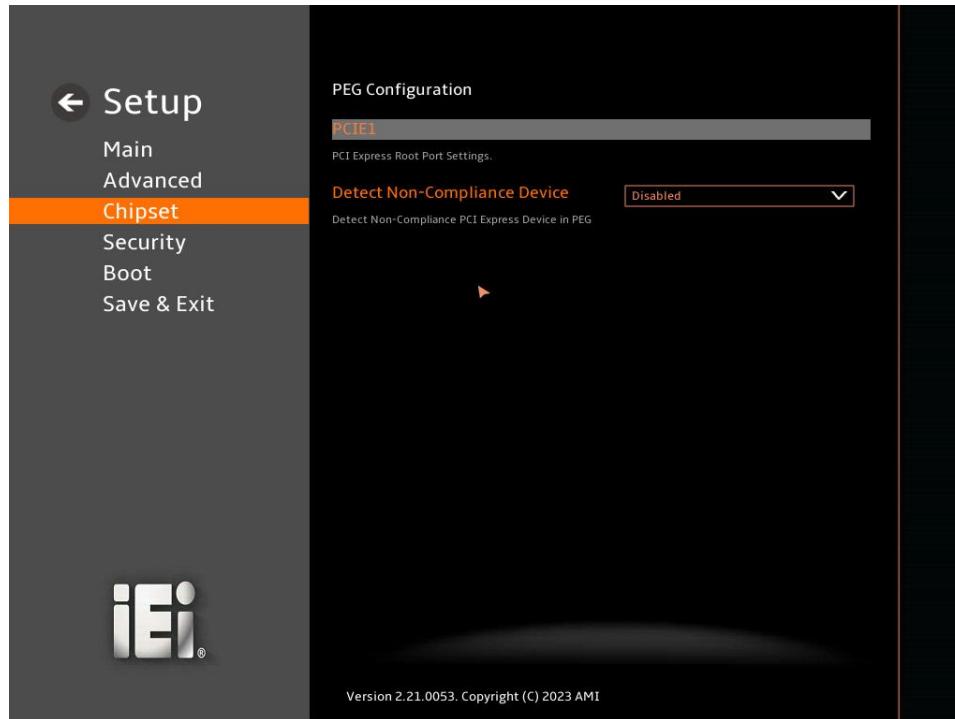
BIOS Menu 30: PEG Port Configuration

➔ **Detect Non-Compliance Device [Disabled]**

Use the **Detect Non-Compliance Device** option to detect non-compliance PCIe device in PEG.

➔ **Disabled** DEFAULT Do not detect non-compliance PCIe device in PEG

➔ **Enabled** Detect non-compliance PCIe device in PEG

5.4.1.3.1 PCIE1

BIOS Menu 31: PCIE1

➔ **PCIE1 [Enabled]**

Use **PCIE1** to control the PCI Express root port**.**

 ➔ **Enabled**   DEFAULT

 ➔ **Disabled**

➔ **PCIe Speed [Auto]**

Use the **PCIe Speed** option to specify the PCI Express port speed. Configuration options are listed below.

 ➔ **Auto**   DEFAULT   Auto mode.

 ➔ **Gen1**   Configure PCIe Speed to Gen1.

 ➔ **Gen2**   Configure PCIe Speed to Gen2.

 ➔ **Gen3**   Configure PCIe Speed to Gen3.

➔   **Gen4**   Configure PCIe Speed to Gen4.

➔   **Gen5**   Configure PCIe Speed to Gen5.

5.4.2 PCH-IO Configuration

Use the **PCH-IO Configuration** menu (**BIOS Menu 32**) to configure the PCH parameters.

BIOS Menu 32: PCH-IO Configuration (1/2)

BIOS Menu 33: PCH-IO Configuration (2/2)

➔ **Auto Power Button Function [Disabled (AT)]**

Use the **Auto Power Button Function** BIOS option to show the power mode state. Use the **J_ATX_AT1** to switch the AT/ATX power mode.

➔ **Enabled (AT)** **DEFAULT** The system power mode is AT.

➔ **Power Saving Function (EUP) [Disabled]**

Use the **Power Saving Function (EUP)** BIOS option to enable or disable the power saving function.

➔ **Disabled** **DEFAULT** Power saving function is disabled.

➔ **Enabled** Power saving function is enabled. It will reduce power consumption when the system is off.

➔ **USB Power SW1(LAN2_USB2) [S0/S3/S4/S5 ON]**

Use the **USB Power SW1** to enable or disable the USB Power.

➔ **S0/S3/S4/S5 ON** DEFAULT
➔ **S0/S3/S4/S5 OFF**

➔ **USB Power SW2(LAN1_USB1) [S0/S3/S4/S5 ON]**

Use the **USB Power SW2** to enable or disable the USB Power.

➔ **S0/S3/S4/S5 ON** DEFAULT
➔ **S0/S3/S4/S5 OFF**

➔ **USB Power SW3(USB2_1_C2) [S0/S3/S4/S5 ON]**

Use the **USB Power SW3** to enable or disable the USB Power.

➔ **S0/S3/S4/S5 ON** DEFAULT
➔ **S0/S3/S4/S5 OFF**

➔ **USB Power SW4(USB2_1_C3) [S0/S3/S4/S5 ON]**

Use the **USB Power SW4** to enable or disable the USB Power.

➔ **S0/S3/S4/S5 ON** DEFAULT
➔ **S0/S3/S4/S5 OFF**

➔ **USB Power SW5(USB2_CN1_C4) [S0/S3/S4/S5 ON]**

Use the **USB Power SW5** to enable or disable the USB Power.

➔ **S0/S3/S4/S5 ON** DEFAULT
➔ **S0/S3/S4/S5 OFF**

➔ **USB Power SW7(USB2_CN1_C6) [S0/S3/S4/S5 ON]**

Use the **USB Power SW7** to enable or disable the USB Power.

➔ **S0/S3/S4/S5 ON** DEFAULT

➔ **S0/S3/S4/S5 OFF**

5.4.2.1 PCI Express Configuration

Use the **PCI Express Configuration** submenu (**BIOS Menu 34**) to configure the PCI Express slots.
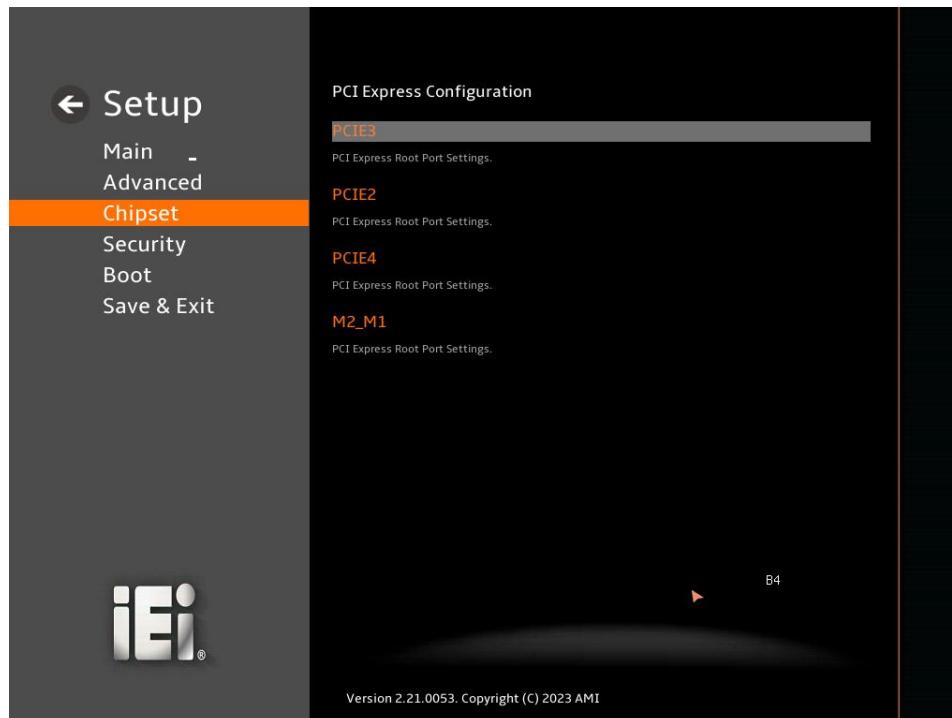
BIOS Menu 34: PCI Express Configuration

5.4.2.2 PCIE3, PCIE2, PCIE4, M2_M1

Use the **PCIE3，PCIE2，PCIE4，M2_M1** submenu (**BIOS Menu 35**) to configure the PCI Root Port Setting.
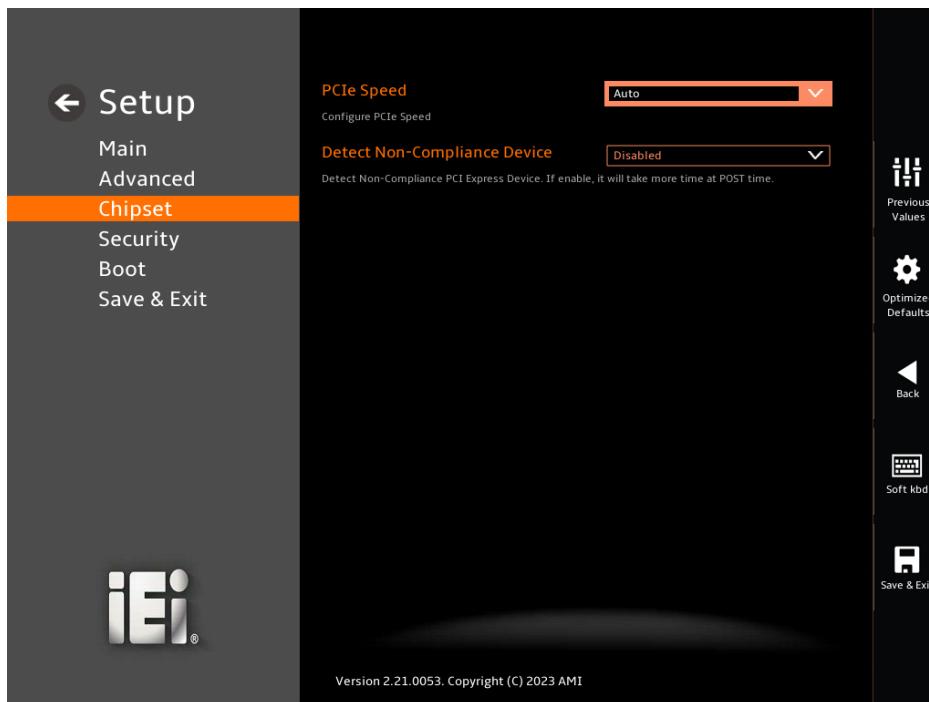
BIOS Menu 35: PCIe Slot Configuration Submenu

➔ **PCIe Speed [Auto]**

Use the **PCIe Speed** option to specify the PCI Express port speed. Configuration options are listed below.

➔ **Auto**   DEFAULT   Auto mode.

➔ **Gen1**   Configure PCIe Speed to Gen1.

➔ **Gen2**   Configure PCIe Speed to Gen2.

➔ **Gen3**   Configure PCIe Speed to Gen3.

➔ **Gen4**   Configure PCIe Speed to Gen4.

➔ **Detect Non-Compliance Device [Disabled]**

Use the **Detect Non-Compliance Device** option to configure whether to detect if a non-compliance PCI Express device is connected to the PCI Express port.

➔ **Disabled** DEFAULT Do not detect if a non-compliance PCI Express device is connected to the PCI Express port.

➔ **Enabled** Detect if a non-compliance PCI Express device is connected to the PCI Express port.

5.4.2.3 SATA Configuration

Use the **SATA Configuration** menu (**BIOS Menu 36**) to change and/or set the configuration of the SATA devices installed in the system.
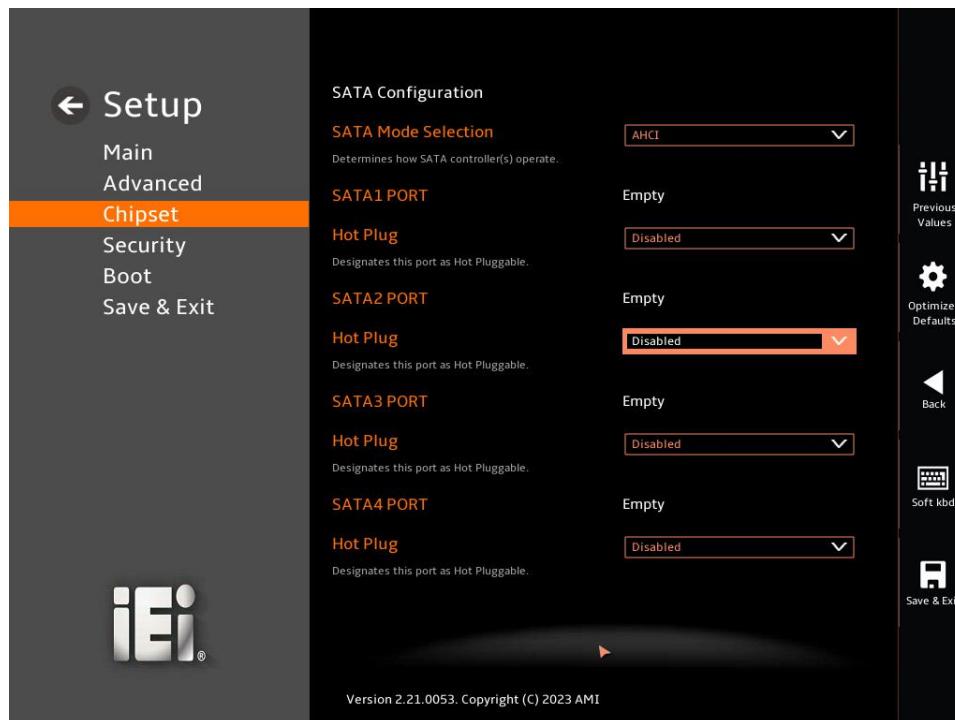
BIOS Menu 36: SATA Configuration

➔ **SATA Mode Selection [AHCI]**

Use the **SATA Mode Selection** option to determine how the SATA devices operate.

➔	**AHCI**	**DEFAULT**	Configures SATA devices as AHCI device.
➔	**Intel RST Premium With Intel Optane System Acceleration**		Configures SATA devices to the Intel RST Premium With Intel Optane System Acceleration mode.

➔ **Hot Plug [Disabled]**

Use the **Hot Plug** option to designate the correspondent port as hot-pluggable.

➔ **Disabled**   **DEFAULT**   Disables the hot-pluggable function of the SATA port.

➔ **Enabled**   Designates the SATA port as hot-pluggable.

5.4.2.4 HD Audio Configuration

Use the **HD Audio Configuration** menu (**BIOS Menu 37**) to configure the PCH Azalia settings.
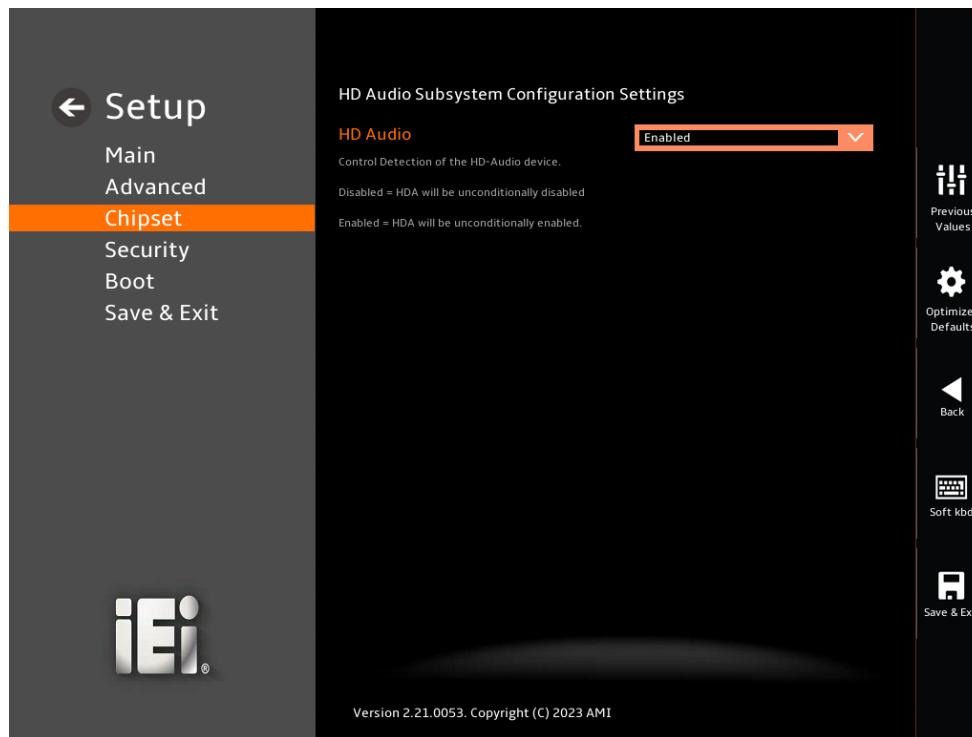
BIOS Menu 37: HD Audio Configuration

➔ **HD Audio [Enabled]**

Use the **HD Audio** option to enable or disable the High Definition Audio controller.

➔ **Disabled**   The onboard High Definition Audio controller is disabled.

➔ **Enabled**   D<small>EFAULT</small>   The onboard High Definition Audio controller is enabled.

5.5 Security

Use the **Security** menu (**BIOS Menu 39**) to set system and user passwords.

BIOS Menu 38: Security (1/2)
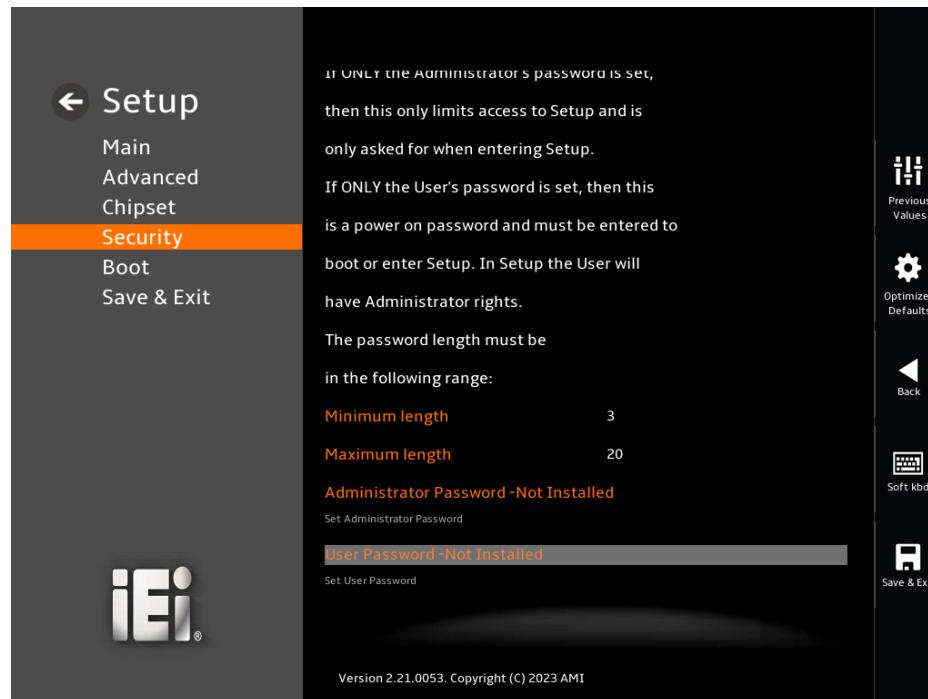
BIOS Menu 39: Security (2/2)

➔ **Administrator Password**

Use the **Administrator Password** to set or change an administrator password.

➔ **User Password**

Use the **User Password** to set or change a user password.

5.6 Boot

Use the **Boot** menu (**BIOS Menu 40**) to configure system boot options.
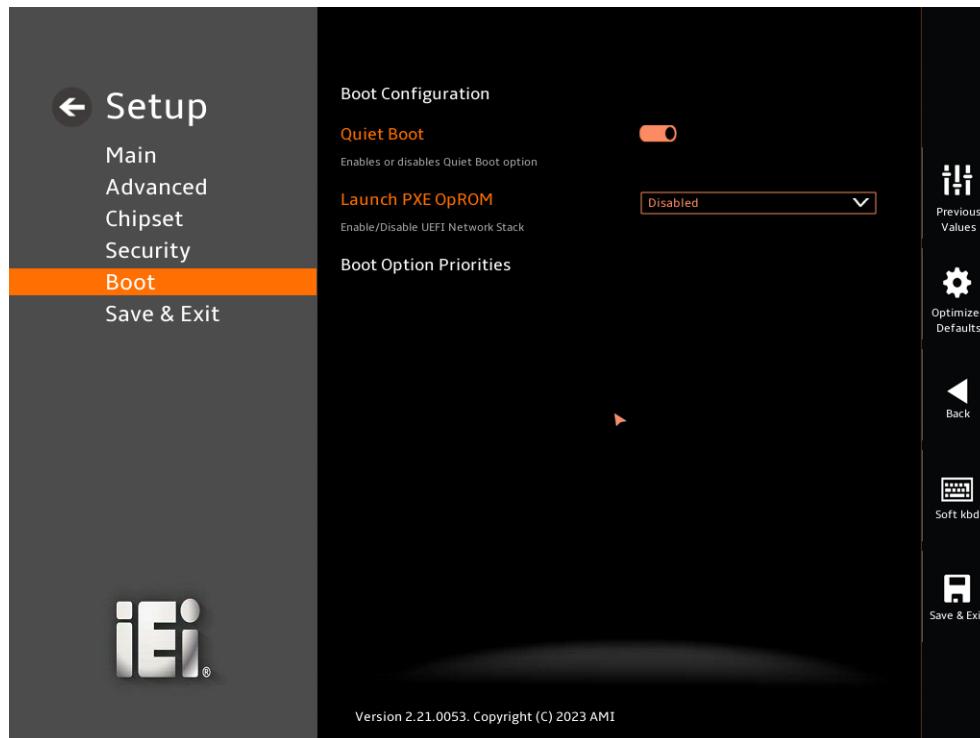
BIOS Menu 40: Boot

5.6.1 Boot Configuration

➔ **Quiet Boot [Enabled]**

Use the **Quiet Boot** BIOS option to select the screen display when the system boots.

➔ **Disabled**   Normal POST messages displayed

➔ **Enabled**   DEFAULT   OEM Logo displayed instead of POST messages

➔ **Launch PXE OpROM [Disabled]**

Use the **Launch PXE OpROM** option to enable or disable boot option for legacy network devices.

➔ **Disabled**   DEFAULT   Ignore all PXE Option ROMs

➔ **Enabled**   Load PXE Option ROMs.

5.7 Save & Exit

Use the **Save & Exit** menu (**BIOS Menu 41**) to load default BIOS values, optimal failsafe values and to save configuration changes.
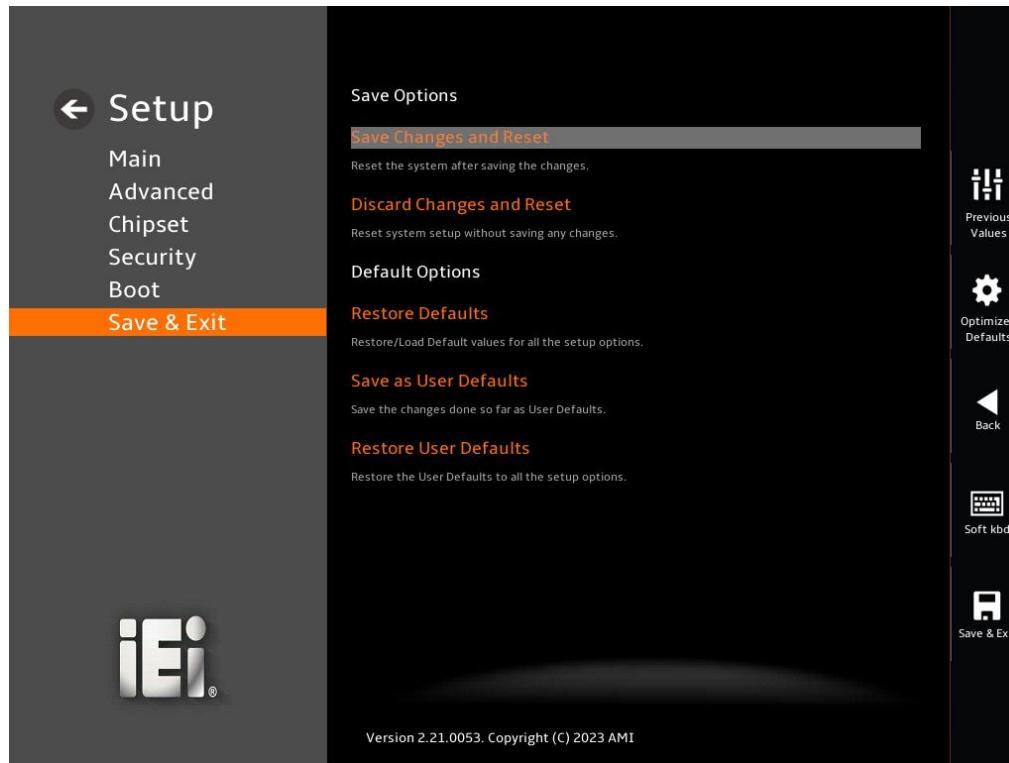
    BIOS Menu 41: Save & Exit

➔ **Save Changes and Reset**

Use the **Save Changes and Reset** option to save the changes made to the BIOS options and reset the system.

➔ **Discard Changes and Reset**

Use the **Discard Changes and Reset** option to exit the system without saving the changes made to the BIOS configuration setup program.

➔ **Restore Defaults**

Use the **Restore Defaults** option to load the optimal default values for each of the parameters on the Setup menus. **F3 key can be used for this operation.**

➔ **Save as User Defaults**

Use the **Save as User Defaults** option to save the changes done so far as user defaults.

➔ **Restore User Defaults**

Use the **Restore User Defaults** option to restore the user defaults to all the setup options.

A

Regulatory Compliance

DECLARATION OF CONFORMITY

This equipment has been tested and found to comply with specifications for CE marking. If the user modifies and/or installs other devices in the equipment, the CE conformity declaration may no longer apply.

FCC WARNING

This equipment complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

- This device may not cause harmful interference, and
- This device must accept any interference received, including interference that may cause undesired operation.

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

Appendix

B

Product Disposal

⚠ **CAUTION:**

Risk of explosion if battery is replaced by an incorrect type. Only certified engineers should replace the on-board battery.

Dispose of used batteries according to instructions and local regulations.

▪ Outside the European Union–If you wish to dispose of used electrical and electronic products outside the European Union, please contact your local authority so as to comply with the correct disposal method.

▪ Within the European Union–The device that produces less waste and is easier to recycle is classified as electronic device in terms of the European Directive 2012/19/EU (WEEE), and must not be disposed of as domestic garbage.
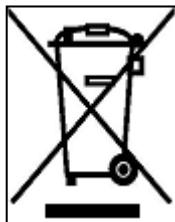
EU-wide legislation, as implemented in each Member State, requires that waste electrical and electronic products carrying the mark (left) must be disposed of separately from normal household waste. This includes monitors and electrical accessories, such as signal cables or power cords. When you need to dispose of your device, please follow the guidance of your local authority, or ask the shop where you purchased the product. The mark on electrical and electronic products only applies to the current European Union Member States.

Please follow the national guidelines for electrical and electronic product disposal.

Appendix

C

Error Beep Code

C.1 PEI Beep Codes

Number of Beeps	Description
1	Memory not Installed
1	Memory was installed twice (InstallPeiMemory routine in PEI Core called twice)
2	Recovery started
3	DXEIPL was not found
3	DXE Core Firmware Volume was not found
4	Recovery failed
4	S3 Resume failed
7	Reset PPI is not available

C.2 DXE Beep Codes

Number of Beeps	Description
1	Invalid password
4	Some of the Architectural Protocols are not available
5	No Console Output Devices are found
5	No Console Input Devices are found
6	Flash update is failed
7	Reset protocol is not available
8	Platform PCI resource requirements cannot be met

NOTE:

If you have any question, please contact IEI for further assistance.

Appendix

D

Watchdog Timer

NOTE:

When exiting a program it is necessary to disable the Watchdog Timer, otherwise the system resets.

EXAMPLE PROGRAM:

; INITIAL TIMER PERIOD COUNTER

;

W_LOOP:

;

```
        MOV         AX, 6F02H           ;setting the time-out value
        MOV         BL, 30              ;time-out value is 48 seconds
        INT     15H
```

;

; ADD THE APPLICATION PROGRAM HERE

;

```
        CMP         EXIT_AP, 1          ;is the application over?
        JNE     W_LOOP              ;No, restart the application

        MOV         AX, 6F02H           ;disable Watchdog Timer
        MOV         BL, 0           ;
        INT     15H
```

;

; EXIT ;

E

Hazardous Materials Disclosure

E.1 RoHS II Directive (2015/863/EU)

The details provided in this appendix are to ensure that the product is compliant with the RoHS II Directive (2015/863/EU). The table below acknowledges the presences of small quantities of certain substances in the product, and is applicable to RoHS II Directive (2015/863/EU).

Please refer to the following table.

Part Name	Toxic or Hazardous Substances and Elements									
	Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (CR(VI))	Polybrominated Biphenyls	Polybrominated Diphenyl Ethers	Bis(2-ethylhexyl) phthalate	Butyl benzyl phthalate (BBP)	Dibutyl phthalate (DBP)	Diisobutyl phthalate (DIBP)
Housing	O	O	O	O	O	O	O	O	O	O
Printed Circuit Board	O	O	O	O	O	O	O	O	O	O
Metal Fasteners	O	O	O	O	O	O	O	O	O	O
Cable Assembly	O	O	O	O	O	O	O	O	O	O
Fan Assembly	O	O	O	O	O	O	O	O	O	O
Power Supply Assemblies	O	O	O	O	O	O	O	O	O	O
Battery	O	O	O	O	O	O	O	O	O	O
O: This toxic or hazardous substance is contained in all of the homogeneous materials for the part is below the limit requirement in Directive (EU) 2015/863.										
X: This toxic or hazardous substance is contained in at least one of the homogeneous materials for this part is above the limit requirement in Directive (EU) 2015/863.										

E.2 China RoHS

此附件旨在确保本产品符合中国 RoHS 标准。以下表格标示此产品中某有毒物质的含量符合中国 RoHS 标准规定的限量要求。

本产品上会附有"环境友好使用期限"的标签，此期限是估算这些物质"不会有泄漏或突变"的年限。本产品可能包含有较短的环境友好使用期限的可替换元件，像是电池或灯管，这些元件将会单独标示出来。

部件名称	有毒有害物质或元素					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (CR(VI))	多溴联苯 (PBB)	多溴二苯醚 (PBDE)
壳体	O	O	O	O	O	O
印刷电路板	O	O	O	O	O	O
金属螺帽	O	O	O	O	O	O
电缆组装	O	O	O	O	O	O
风扇组装	O	O	O	O	O	O
电力供应组装	O	O	O	O	O	O
电池	O	O	O	O	O	O
O：表示该有毒有害物质在该部件所有物质材料中的含量均在 SJ/T11364-2014 與 GB/T26572-2011 标准规定的限量要求以下。 X：表示该有毒有害物质至少在该部件的某一均质材料中的含量超出 SJ/T11364-2014 與 GB/T26572-2011 标准规定的限量要求。						