

**IEI**

# **IFM-3010IL/WL**

# **Datasheet**

mSATA 3SE 一般溫	mSATA 3SE 寬溫
IFM-3010IL-32GB	IFM-3010WL-32GB
IFM-3010IL-16GB	IFM-3010WL-16GB
IFM-3010IL-8GB	IFM-3010WL-8GB
IFM-3010IL-4GB	IFM-3010WL-4GB
IFM-3010IL-2GB	IFM-3010WL-2GB
IFM-3010IL-1GB	IFM-3010WL-1GB

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# 1. Product Overview

## 1.1 Introduction of IEI SATADOM-SL 3SE

IEI Serial ATA Disk on Module (SATADOM) supports SATA III standard (6.0Gb/s) interface with excellent performance, and SATADOM-SL 3SE is designed as the smallest form factor size that could enhance compatibility with various design applications. Particularly the 7th pin of standard SATA 7pin connector can optionally be the built-in power VCC pin. In other words, it could be connected directly to the SATA on-board socket on customers' system without additional power cable. Besides, the booting time for operation and the power consumption is less than hard disk drive (HDD). SATADOM-SL 3SE can work under harsh environment compile with ATA protocol, no additional drives are required, and the SSD can be configured as a boot device or data storage device.

## 1.2 Product View and Models

IEI SATADOM-SL 3SE is available in follow capacities within SLC flash ICs.

[SATADOM-SL 3SE 1GB](#)

[SATADOM-SL 3SE 2GB](#)

[SATADOM-SL 3SE 4GB](#)

[SATADOM-SL 3SE 8GB](#)

[SATADOM-SL 3SE 16GB](#)

[SATADOM-SL 3SE 32GB](#)



**Figure 1: IEI SATADOM-SL 3SE**

## 1.3 SATA Interface

IEI SATADOM-SL 3SE supports SATA III interface, and compliant with SATA I and SATA II. SATA III interface can work with Serial Attached SCSI (SAS) host system, which is used in server computer. IEI SATADOM-SL 3SE is compliant with Serial ATA Gen 1, Gen 2 and Gen 3 specification (Gen 3 supports 1.5Gbps /3.0Gbps/6.0Gbps data rate). SATA connector uses a standard 7-pin signal segment.

## 2. Product Specifications

### 2.1 Capacity and Device Parameters

SATADOM-SL 3SE device parameters are shown in Table 1.

**Table 1: Device parameters**

Capacity	Cylinders	Heads	Sectors	LBA	user space
1GB	1959	16	63	1974672	964
2GB	3897	16	63	3928176	1,918
4GB	7773	16	63	7835184	3,826
8GB	15525	16	63	15649200	7,641
16GB	16383	16	63	31277232	15,272
32GB	16383	16	63	62533296	30,534

### 2.2 Performance

Burst Transfer Rate: 6.0Gbps

**Table 2: Performance**

Capacity	1GB	2GB	4GB	8GB	16GB	32GB
Sequential Read (max.)	23 MB/sec	24MB/sec	220 MB/sec	250 MB/sec	290 MB/sec	300 MB/sec
Sequential Write (max.)	9 MB/sec	18 MB/sec	50 MB/sec	60 MB/sec	120 MB/sec	130 MB/sec

Note: the information is based on CrystalDiskMark 3.01 with file size 1000MB test patent

### 2.3 Electrical Specifications

#### 2.3.1 Power Requirement

**Table 3: IEI SATADOM-SL 3SE Power Requirement**

Item	Symbol	Rating	Unit
Input voltage	V <sub>IN</sub>	+5 DC +- 5%	V

#### 2.3.2 Power Consumption

**Table 4: Power Consumption**

Mode	Power Consumption (mA)
Read	120 (max.)
Write	130 (max.)
Idle	100 (max.)

\* Target: 32GB SATADOM-SL 3SE

## 2.4 Environmental Specifications

### 2.4.1 Temperature Ranges

**Table 5: Temperature range for SATADOM-SL 3SE**

Temperature	Range
Operating	Standard Grade: 0°C to +70°C
	Industrial Grade:-40°C to +85°C
Storage	-55°C to +95°C

### 2.4.2 Humidity

Relative Humidity: 10-95%, non-condensing

### 2.4.3 Shock and Vibration

**Table 6: Shock/Vibration Testing for SATADOM-SL 3SE**

Reliability	Test Conditions	Reference Standards
Vibration	7 Hz to 2K Hz, 20G, 3 axes	IEC 68-2-6
Mechanical Shock	Duration: 0.5ms, 1500 G, 3 axes	IEC 68-2-27

### 2.4.4 Mean Time between Failures (MTBF)

Table 7 summarizes the MTBF prediction results for various SATADOM-SL 3SE configurations. The analysis was performed using a RAM Commander™ failure rate prediction.

- **Failure Rate:** The total number of failures within an item population, divided by the total number of life units expended by that population, during a particular measurement interval under stated condition.
- **Mean Time between Failures (MTBF):** A basic measure of reliability for repairable items: The mean number of life units during which all parts of the item perform within their specified limits, during a particular measurement interval under stated conditions.

**Table 7: SATADOM-SL 3SE MTBF**

Product	Condition	MTBF (Hours)
IEI SATADOM-SL 3SE	Telcordia SR-332 GB, 25°C	>3,000,000

## 2.5 CE and FCC Compatibility



SATADOM-SL 3SE conforms to CE and FCC requirements.

## 2.6 RoHS Compliance

SATADOM-SL 3SE is fully compliant with RoHS directive.

## 2.7 Reliability

Parameter	Value
Read Cycles	Unlimited Read Cycles
Flash endurance	100,000 P/E cycles
Wear-Leveling Algorithm	Support
Bad Blocks Management	Support
Error Correct Code	Support
TBW	
1GB	90 (Sequential write)
2GB	180 (Sequential write)
4GB	360 (Sequential write)
8GB	720 (Sequential write)
16GB	1440 (Sequential write)
32GB	2880 (Sequential write)

## 2.8 Transfer Mode

SATADOM-SL 3SE support following transfer mode:

Serial ATA I 1.5Gbps

Serial ATA II 3.0Gbps

Serial ATA III 6.0Gbps

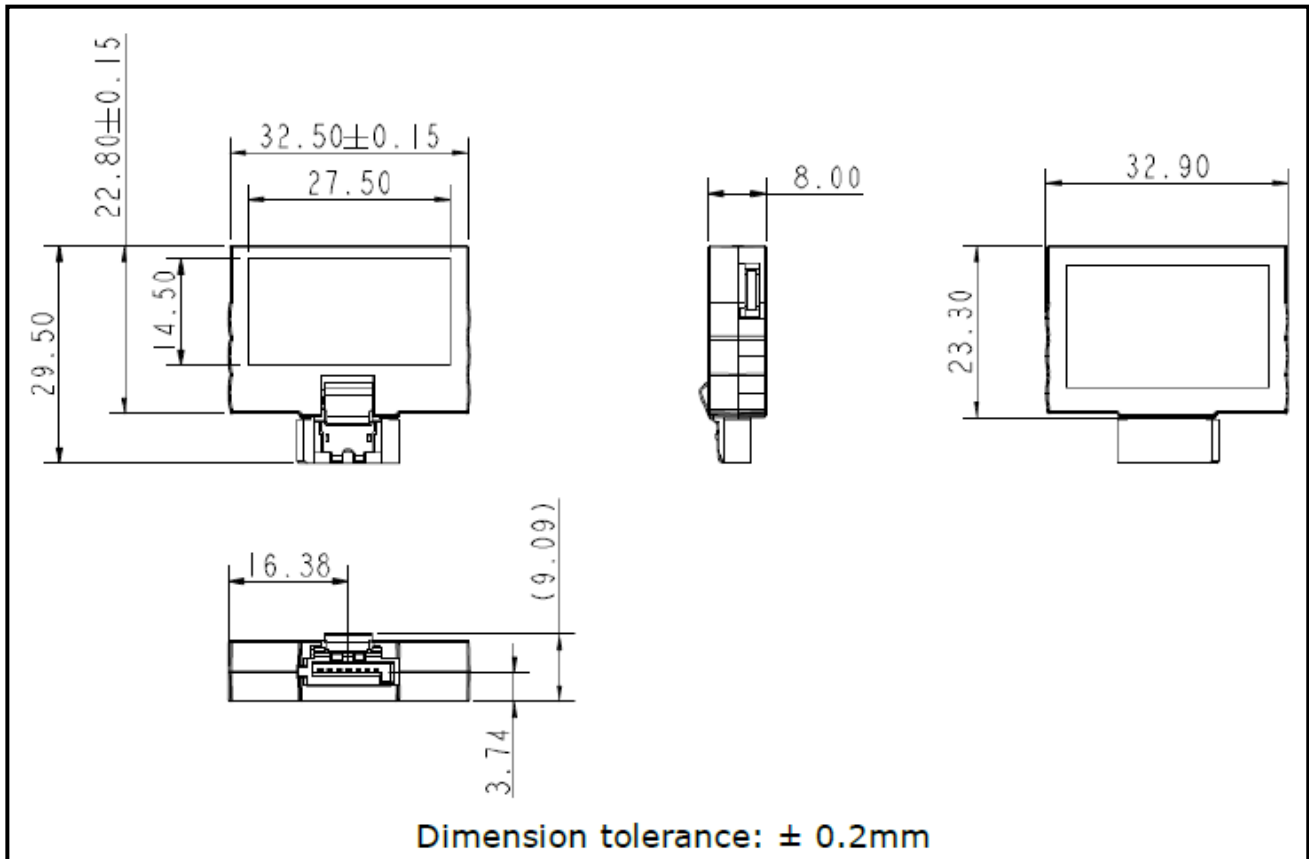
## 2.9 Pin Assignment

IEI SATADOM-SL 3SE uses a standard SATA pin-out. See Table 8 for SATADOM-SL 3SE pin assignment.

**Table 8: IEI SATADOM-SL 3SE Pin Assignment**

Name	Type	Description
Pin 1	GND	Shielding
Pin 2	A+	Differential signal to A
Pin 3	A-	Differential signal to A-
Pin 4	GND	Shielding
Pin 5	B-	Differential signal to B-
Pin 6	B+	Differential signal to B
Pin 7	GND/VCC	Shielding/Power

## 2.10 Mechanical Dimensions



## 2.11 Assembly Weight

An IEI SATADOM-SL 3SE within flash ICs, 32GB's weight is 7 grams approximately.

## 2.12 Seek Time

IEI SATADOM-SL 3SE is not a magnetic rotating design. There is no seek or rotational latency required.

## 2.13 Hot Plug

The SSD support hot plug function and can be removed or plugged-in during operation. User has to avoid hot plugging the SSD which is configured as boot device and installed operation system.

Surprise hot plug : The insertion of a SATA device into a backplane (combine signal and power) that has power present. The device powers up and initiates an OOB sequence.

Surprise hot removal: The removal of a SATA device from a powered backplane, without first being placed in a quiescent state.

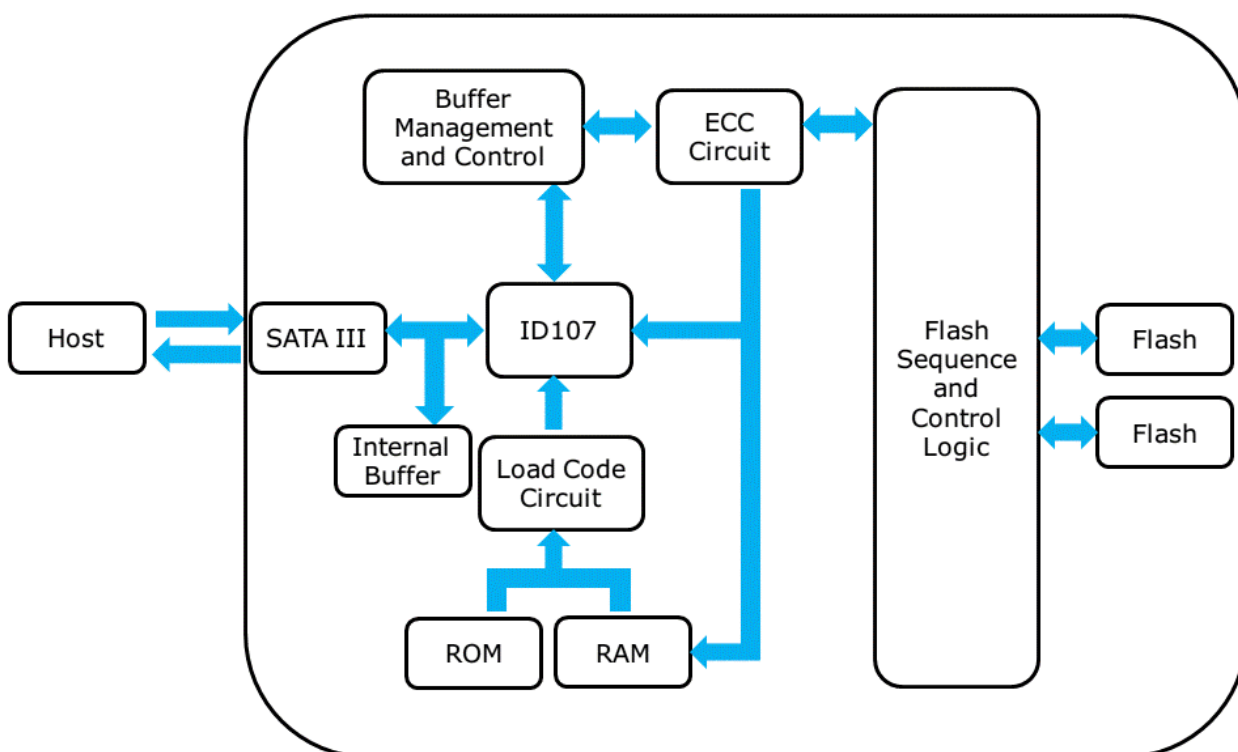
## 2.14 NAND Flash Memory

IEI SATADOM-SL 3SE uses Single Level Cell (SLC) NAND flash memory, which is non-volatility, high reliability and high speed memory storage. There are only two statuses 0 or 1 of one cell. Read or Write data to flash memory for SSD is control by microprocessor.

## 3. Theory of Operation

### 3.1 Overview

Figure 2 shows the operation of IEI SATADOM-SL 3SE from the system level, including the major hardware blocks.



**Figure 2: IEI SATADOM-SL 3SE Block Diagram**

IEI SATADOM-SL 3SE integrates a SATA III controller and NAND flash memories. Communication with the host occurs through the host interface, using the standard ATA protocol. Communication with the flash device(s) occurs through the flash interface.

### 3.2 SATA III Controller

IEI SATADOM-SL 3SE is designed with ID 107, a SATA III 6.0Gbps (Gen. 3) controller. The Serial ATA physical, link and transport layers are compliant with Serial ATA Gen 1, Gen 2 and Gen 3 specification (Gen 3 supports 1.5Gbps/3.0Gbps/6.0Gbps data rate). The controller has 2 channels for flash interface.

### 3.3 Error Detection and Correction

Highly sophisticated Error Correction Code algorithms are implemented. The ECC unit consists of the Parity Unit (parity-byte generation) and the Syndrome Unit (syndrome-byte computation). This unit implements an algorithm that can correct 40 bits per 1024 bytes in an ECC block. Code-byte generation during write operations, as well as error detection during read operation, is implemented on the fly without any speed penalties.

### 3.4 Wear-Leveling

Flash memory can be erased within a limited number of times. This number is called the **erase cycle limit** or **write endurance limit** and is defined by the flash array vendor. The erase cycle limit applies to each individual erase block in the flash device.

IEI SATADOM-SL 3SE uses a static wear-leveling algorithm to ensure that consecutive writes of a specific sector are not written physically to the same page/block in the flash. This spreads flash media usage evenly across all pages, thereby extending flash lifetime.

### 3.5 Bad Blocks Management

Bad Blocks are blocks that contain one or more invalid bits whose reliability are not guaranteed. The Bad Blocks may be presented while the SSD is shipped, or may develop during the life time of the SSD. When the Bad Blocks is detected, it will be flagged, and not be used anymore. The SSD implement Bad Blocks management, Bad Blocks replacement, Error Correct Code to avoid data error occurred. The functions will be enabled automatically to transfer data from Bad Blocks to spare blocks, and correct error bit.

### 3.6 Power Cycling

IEI's power cycling management is a comprehensive data protection mechanism that functions before and after a sudden power outage to SSD. Low-power detection terminates data writing before an abnormal power-off, while table-remapping after power-on deletes corrupt data and maintains data integrity. IEI's power cycling provides effective power cycling management, preventing data stored in flash from degrading with use.

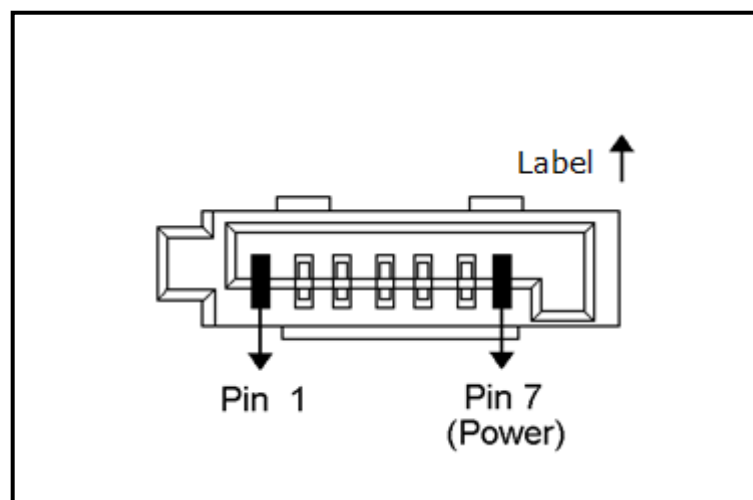
### 3.7 Garbage Collection

Garbage collection is used to maintain data consistency and perform continual data cleansing on

SSDs. It runs as a background process, freeing up valuable controller resources while sorting good data into available blocks, and deleting bad blocks. It also significantly reduces write operations to the drive, thereby increasing the SSD's speed and lifespan.

## 4. Installation Requirements

### 4.1 SATADOM-SL 3SE Pin Directions



**Figure 3: Signal Segment and Power Segment**

### 4.2 Electrical Connections for SATADOM-SL 3SE

A Serial ATA device may be either directly connected to a host or connected to a host through a cable. For connection via cable, the cable should be no longer than 1meter. The SATA interface has a separate connector for the power supply. Please refer to the pin description for further details.

### 4.3 Device Drive

No additional device drives are required. The IEI SATADOM-SL 3SE can be configured as a boot device.

### 4.4 Pin7 VCC

IEI SATADOM series products have an optional design to provide power supply through the 7<sup>th</sup> Pin of SATA connector, and customers DO NOT have to use the power cable for power supply. Such a cable-less design of SATADOM series products with Pin7 VCC brings more convenience to customers' system. The followings are the points customers have to be careful of while designing in SATADOM series products with Pin7 VCC.

SATADOM series products with Pin7 VCC is designed with a fuse (poly switch 500mA, 6V) on Pin7's circuit. Such a design could avoid any potential damage to customers' system.

To have the advantages of SATADOM series with Pin7 VCC, and to avoid any potential damage to customers' board designed with VCC power supply, IEI suggests that customers MUST design their board with a fuse which should be designed before the SATA socket Pin7 VCC. In other words, customers are suggested NOT TO layout 5V VCC to SATA socket on board directly. A circuit diagram example to explain this is shown as below.

