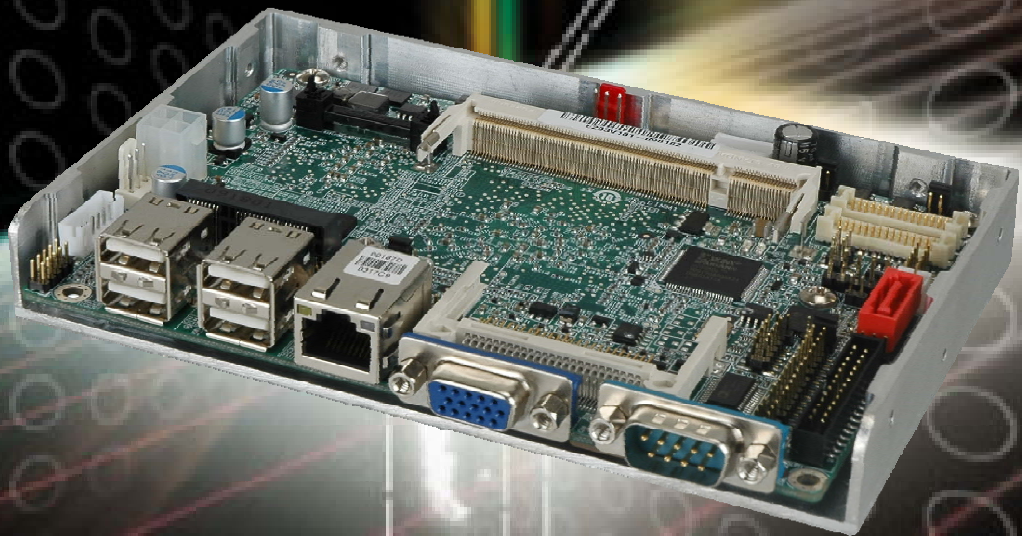




IEI Technology Corp.



**MODEL:**

**WAFER-PV-D5252/D4252/N4552**

**3.5" SBC with Intel® Atom™ Processor  
Up to 2.0 GB DDR3, VGA, LVDS, PCIe Mini, RS-232/422/485,  
SATA 3Gb/s, CompactFlash®, Eight USB, RoHS**

## **User Manual**

Rev. 1.02 – 20 March, 2013



# Revision

Date	Version	Changes
20 March, 2013	1.02	Modified supported memory specifications ( <b>Table 1-1</b> )
4 March, 2011	1.01	Modified LVDS connector pinouts ( <b>Table 3-11</b> ) Modified LVDS resolution select jumper settings ( <b>Table 4-5</b> )
11 January, 2011	1.00	Initial release

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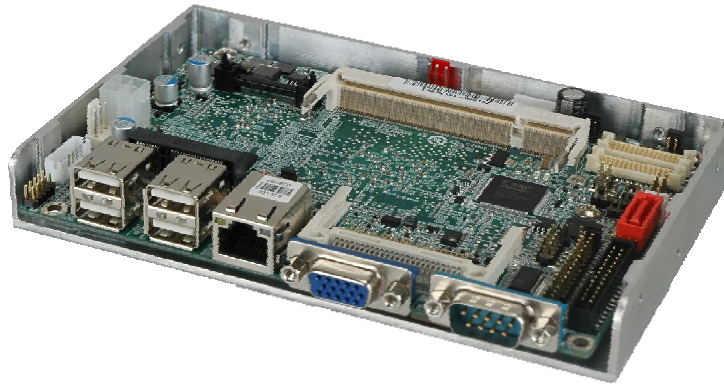
Chapter

1

# Introduction

---

## 1.1 Introduction



**Figure 1-1: WAFER-PV-D5252/D4252/N4552**

The WAFER-PV-D5252/D4252/N4552 is a 3.5" SBC with a Intel® Atom™ D525, D425 or N455 CPU.

The WAFER-PV-D5252/D4252/N4552 is designed for fanless operation. The low power CPUs don't require active cooling and stay within specified heat range using the included heatsink.

Storage on the board is handled by a SATA 3Gb/s port for connecting a hard drive, optical drive or SSD. The CompactFlash® slot allows a Type II CompactFlash® card to be installed.

The board has two graphics outputs. A VGA output connects to a traditional VGA monitor and one LVDS connector supports 18-bit single-channel display.

Other slots and connectors include a PCIe Mini cards slot, RS-232, RS-232/422/485, Gigabit Ethernet, USB ports, and digital I/O.

## 1.2 Benefits

Some of the WAFER-PV-D5252/D4252/N4552 motherboard benefits include:

- Low power consumption
- Wide range of I/O interfaces
- Dual display support

## WAFER-PV-D5252/D4252/N4552 SBC

### 1.3 Features

Some of the WAFER-PV-D5252/D4252/N4552 motherboard features are listed below:

- 3.5" form factor
- Fanless design
- RoHS compliant
- Eight USB 2.0 ports
- Six serial ports
- PCIe Mini card slot
- CompactFlash® Type II slot
- Gigabit Ethernet

### 1.4 Connectors

The connectors on the WAFER-PV-D5252/D4252/N4552 are shown in the figure below.

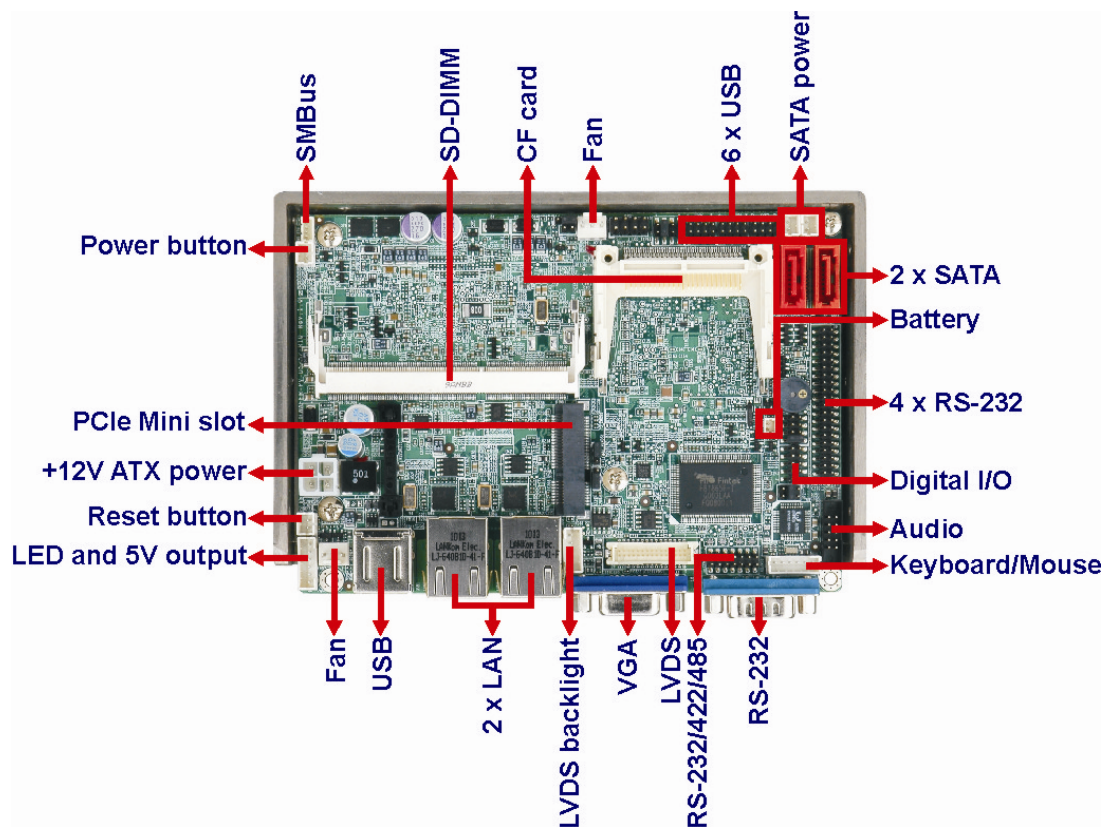


Figure 1-2: Connectors

## 1.5 Dimensions

The main dimensions of the WAFER-PV-D5252/D4252/N4552 are shown in the diagram below.

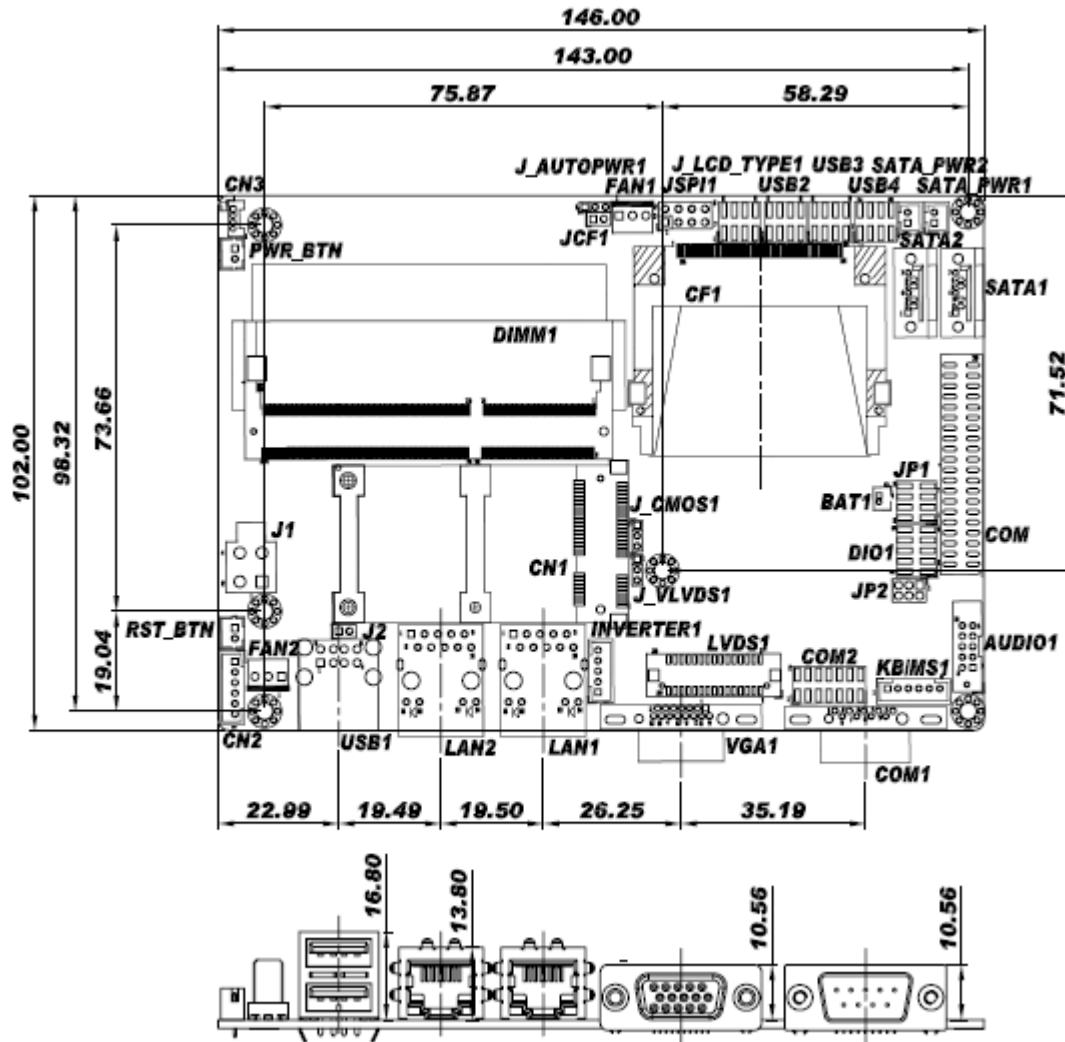
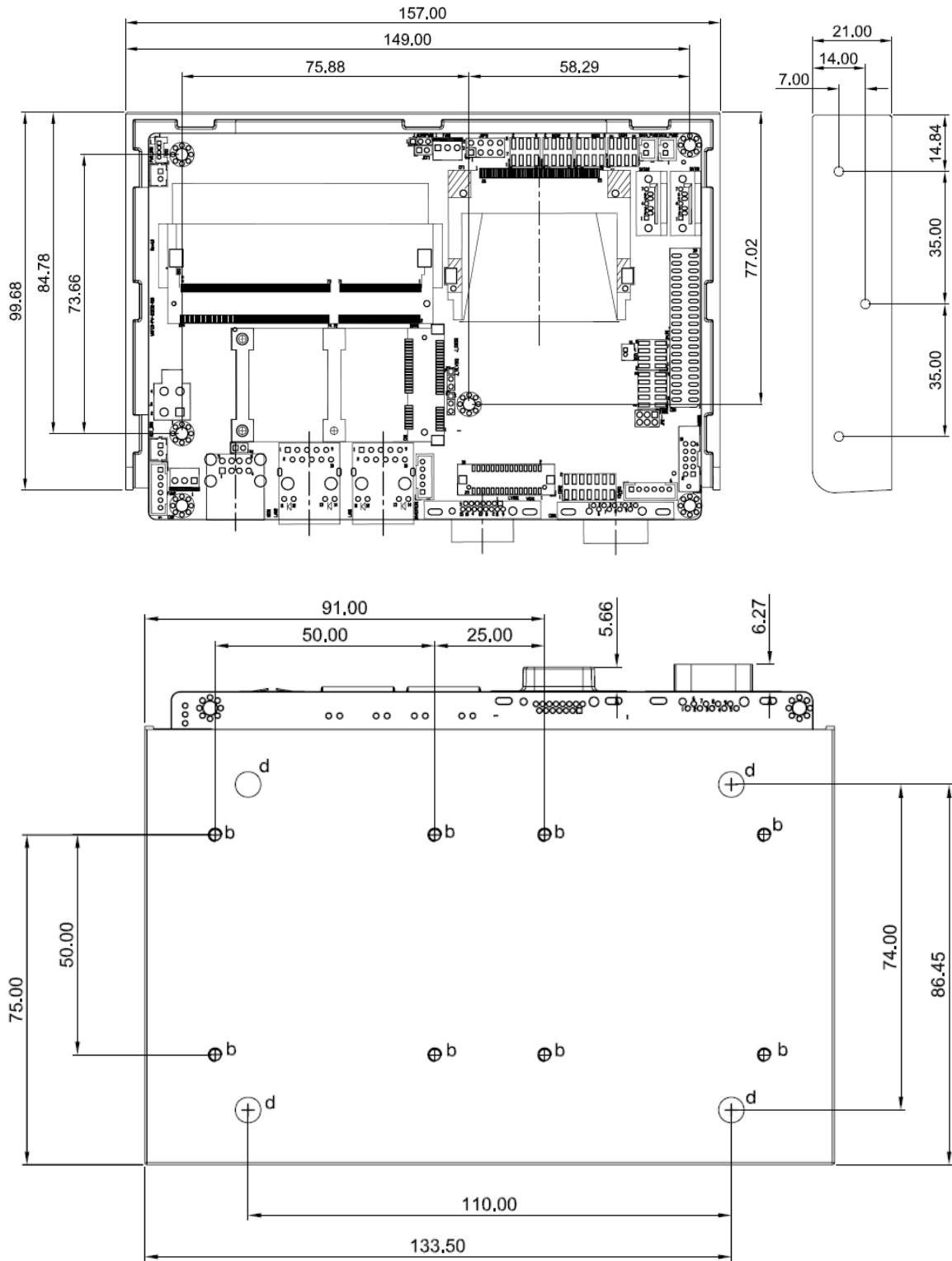


Figure 1-3: Dimensions without Heatsink (mm)



**WAFER-PV-D5252/D4252/N4552 SBC**

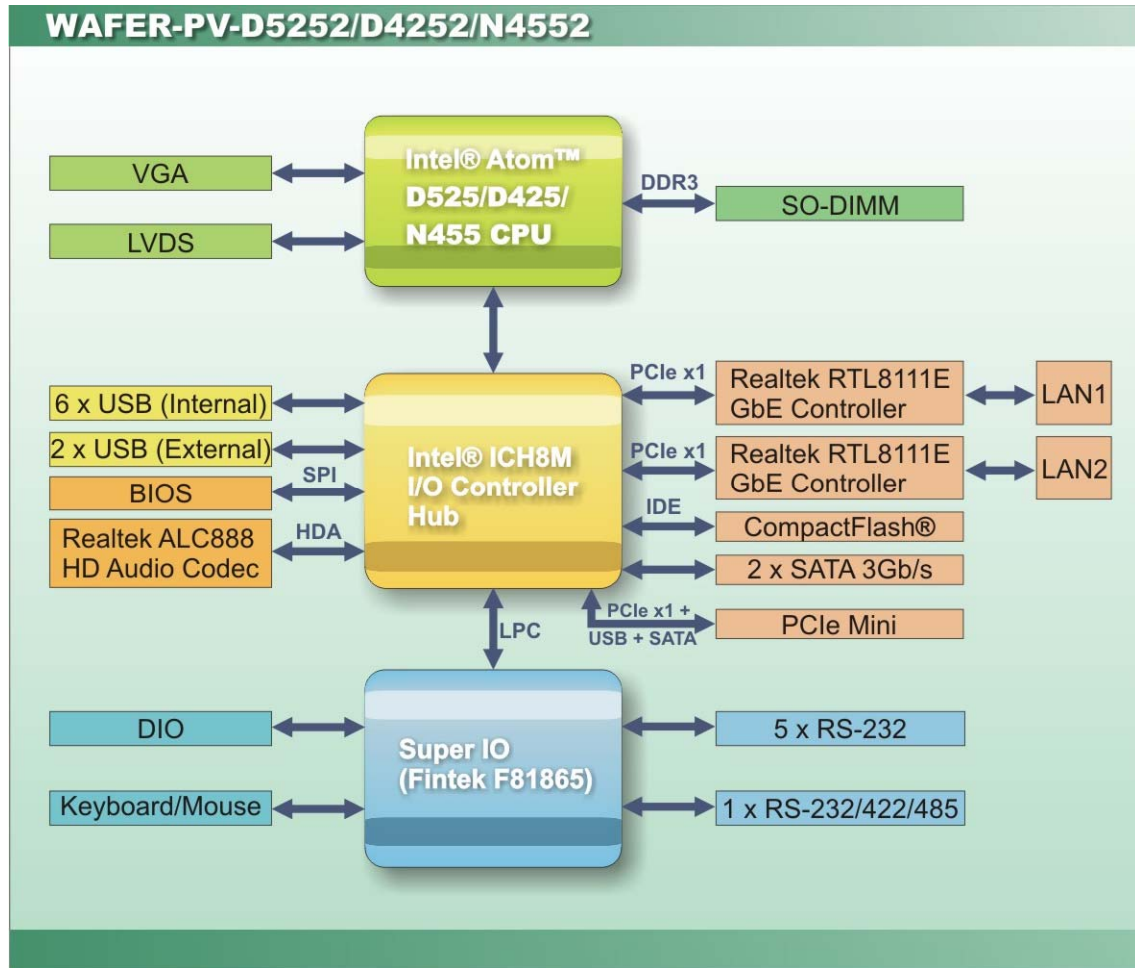


**Figure 1-4: Dimensions with Heatsink (mm)**



## 1.6 Data Flow

Figure 1-5 shows the data flow between the system chipset, the CPU and other components installed on the motherboard.



**Figure 1-5: Data Flow Diagram**

## 1.7 Technical Specifications

WAFER-PV-D5252/D4252/N4552 technical specifications are listed in Table 1-1.

Specification	WAFER-PV-D5252/D4252/N4552
<b>Form Factor</b>	3.5"
<b>Socket</b>	Embedded
<b>CPU Supported</b>	1.8 GHz Intel® Atom™ D525 dual-core CPU with 1 MB L2 cache 1.8 GHz Intel® Atom™ D425 single-core CPU with 512 KB L2 cache 1.66 GHz Intel® Atom™ N455 single-core CPU with 512 KB L2 cache
<b>System Chipset</b>	Intel® ICH8M
<b>Memory</b>	One 204-pin 800 MHz DDR3 SDRAM SO-DIMM <ul style="list-style-type: none"> <li>▪ D525/D425: system max. 4.0 GB</li> <li>▪ N455: system max. 2.0 GB</li> </ul>
<b>Graphic Engine</b>	Intel® GMA3150 <ul style="list-style-type: none"> <li>▪ D525/D425: Gen 3.5 DX9, 400 MHz</li> <li>▪ N455: Gen 3.5 DX9, 200 MHz</li> </ul>
<b>Integrated Graphics</b>	Analog CRT (CRT hot plug supported) <ul style="list-style-type: none"> <li>▪ D525/D425: up to 2048 x 1536</li> <li>▪ N455: up to 1400 x 1050</li> </ul> 18-bit single-channel LVDS
<b>BIOS</b>	UEFI BIOS
<b>Digital I/O</b>	8-bit, 4-bit input/4-bit output
<b>Ethernet Controllers</b>	Two Realtek RTL8111E PCIe GbE controllers (LAN1 supports ASF2.0)
<b>Audio</b>	Realtek ALC888 HD Audio codec
<b>Super I/O Controller</b>	Fintek F81865F
<b>Watchdog Timer</b>	Software programmable supports 1~255 sec. system reset
<b>Expansion</b>	PCIe Mini slot (PCIe + USB + SATA signal) supports IEI PCIe Mini disk on module (DOM) cards

<b>I/O Interface Connectors</b>	
<b>Fan connector</b>	Two 3-pin wafer for system fan
<b>Keyboard/Mouse</b>	One internal 6-pin wafer connector
<b>Serial Ports</b>	Five RS-232 COM connectors One RS-232/422/485 COM connector
<b>USB 2.0/1.1 ports</b>	Six internal via pin header Two external USB ports
<b>SMBus</b>	One 4-pin wafer connector
<b>Storage</b>	
<b>IDE</b>	CompactFlash® Type II slot
<b>SATA</b>	Two independent SATA channels with 3.0 Gb/s data transfer rates
<b>Environmental and Power Specifications</b>	
<b>Power Supply</b>	AT or ATX, 12 V only
<b>Power Consumption</b>	12 V @ 1.79 A (1.8 GHz Intel® Atom™ D525 dual-core CPU with one 1 GB DDR3 SO-DIMM)
<b>Operating temperature</b>	D525: -20°C ~ 60°C with free air; -20°C ~ 70°C with force air D425: -20°C ~ 65°C with free air; -20°C ~ 70°C with force air N455: -20°C ~ 70°C with free air; -20°C ~ 75°C with force air
<b>Humidity</b>	0% ~ 95% (non-condensing)
<b>Physical Specifications</b>	
<b>Dimensions</b>	102 mm x 146 mm
<b>Weight GW/NW</b>	600 g/250 g

**Table 1-1: Technical Specifications**

Chapter

2

# Packing List

---

## 2.1 Anti-static Precautions

---



### WARNING!

Static electricity can destroy certain electronics. Make sure to follow the ESD precautions to prevent damage to the product, and injury to the user.

---

Make sure to adhere to the following guidelines:

- **Wear an anti-static wristband:** - Wearing an anti-static wristband can prevent electrostatic discharge.
- **Self-grounding:**- Touch a grounded conductor every few minutes to discharge any excess static buildup.
- **Use an anti-static pad:** When configuring any circuit board, place it on an anti-static mat.
- **Only handle the edges of the PCB:**- Don't touch the surface of the motherboard. Hold the motherboard by the edges when handling.

## 2.2 Unpacking Precautions

When the WAFER-PV-D5252/D4252/N4552 is unpacked, please do the following:

- Follow the antistatic guidelines above.
- Make sure the packing box is facing upwards when opening.
- Make sure all the packing list items are present.









## 2.3 Packing List






**NOTE:**

If any of the components listed in the checklist below are missing, do not proceed with the installation. Contact the IEI reseller or vendor the WAFER-PV-D5252/D4252/N4552 was purchased from or contact an IEI sales representative directly by sending an email to [sales@iei.com.tw](mailto:sales@iei.com.tw).

The WAFER-PV-D5252/D4252/N4552 is shipped with the following components:



Quantity	Item and Part Number	Image
1	WAFER-PV-D5252, WAFER-PV-D4252 or WAFER-PV-N4552	
1	SATA and power cable (P/N: 32801-000201-100-RS)	
1	4 COM (wo bracket) (P/N: 32200-025401-RS)	
1	KB/MS PS/2 Y-cable (P/N: 32000-023800-RS)	
1	AT 12 V cable (P/N: 32100-087100-RS)	
1	Audio cable (P/N: 32000-072100-RS)	

Quantity	Item and Part Number	Image
1	Mini jumper pack (2.0mm) (P/N:33100-000033-RS)	
1	Utility CD	
1	Quick installation guide	

**Table 2-1: Packing List**

## 2.4 Optional Items

These optional items are available.

Item and Part Number	Image
Dual USB cable (wo bracket) (P/N: 32000-070301-RS)	
RS-232/422/485 cable (P/N: 32205-000300-100-RS)	

**Table 2-2: Optional Items**

Chapter

3

# Connector Pinouts

---

### 3.1 Peripheral Interface Connectors

Section 3.1.2 shows peripheral interface connector locations. Section 3.1.2 lists all the peripheral interface connectors seen in Section 3.1.2.

#### 3.1.1 Layout

The figure below shows the on-board peripheral connectors, rear panel peripheral connectors and on-board jumpers.

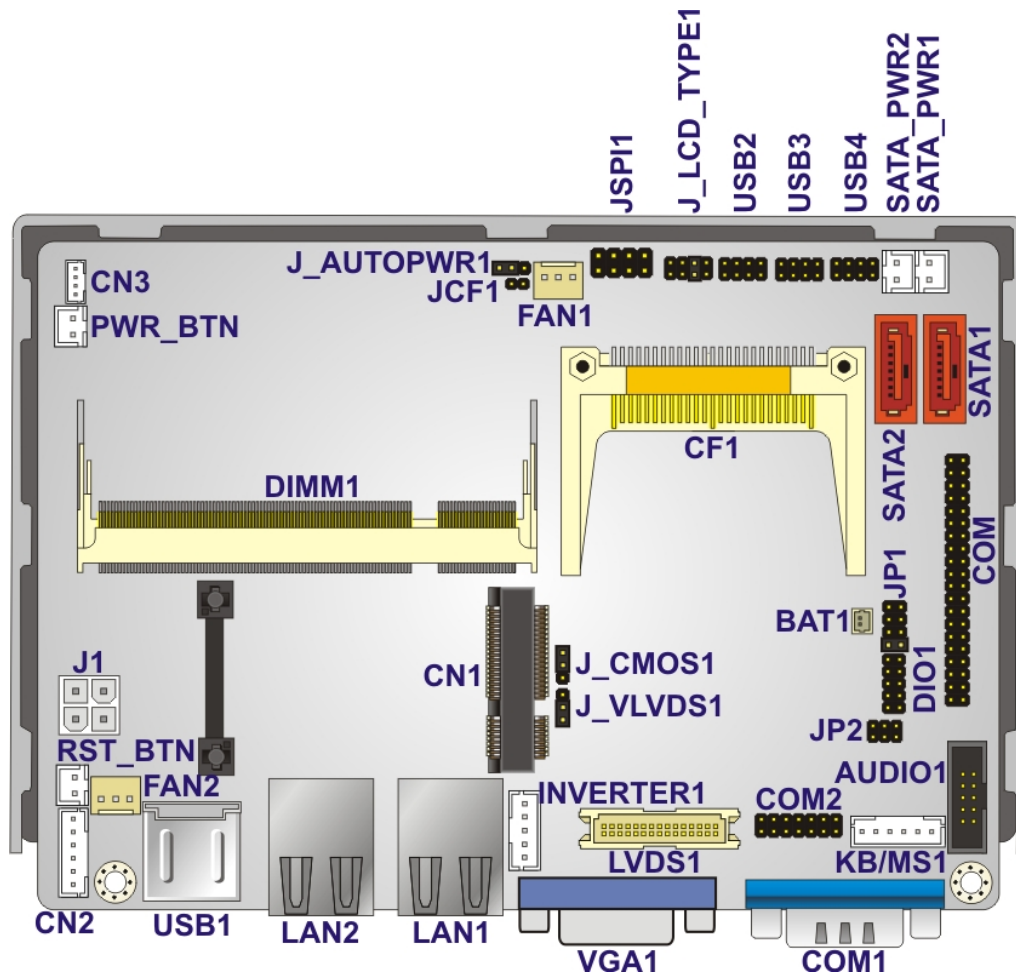


Figure 3-1: Connector and Jumper Locations

## WAFER-PV-D5252/D4252/N4552 SBC

### 3.1.2 Peripheral Interface Connectors

The table below shows a list of the peripheral interface connectors on the WAFER-PV-D5252/D4252/N4552. Detailed descriptions of these connectors can be found below.

Connector	Type	Label
+12 V ATX power connector	4-pin connector	J1
Audio connector	10-pin box header	AUDIO1
Battery connector	2-pin wafer	BT1
CompactFlash® card slot	CF card slot	CF1
Digital I/O connector	10-pin header	DIO1
Fan connectors	3-pin wafer	FAN1, FAN2
Keyboard/mouse connector	6-pin wafer	KB/MS1
LED indicators and 5 V power output connector	6-pin wafer	CN2
LVDS connector	30-pin crimp	LVDS1
LVDS backlight connector	5-pin box header	INVERTER1
PCIe Mini card slot	PCIe Mini card slot	CN1
Power button connector	2-pin wafer	PWR_BTN
Reset button connector	2-pin wafer	RST_BTN
RS-232 serial port (COM3~COM6)	40-pin header	COM
RS-232/422/485 serial port (COM2)	14-pin header	COM2
SATA connectors	SATA connector	SATA1, SATA2
SATA power connectors	2-pin wafer	SATA_PWR1 SATA_PWR2
SMBus connector	4-pin wafer	CN3
SO-DIMM connector	SO-DIMM connector	DIMM1
SPI Flash	8-pin header	JSPI1



Connector	Type	Label
USB connectors	8-pin header	USB2, USB3, USB4

**Table 3-1: Peripheral Interface Connectors**

### 3.1.3 External Interface Panel Connectors

The table below lists the rear panel connectors on the WAFER-PV-D5252/D4252/N4552. Detailed descriptions of these connectors can be found in a later section.

Connector	Type	Label
USB connectors	Dual-USB	USB1
LAN connectors	RJ-45	LAN1, LAN2
Serial port connector	DB-9	COM1
VGA connector	15-pin female VGA	VGA1

**Table 3-2: Rear Panel Connectors**

## 3.2 Internal Peripheral Connectors

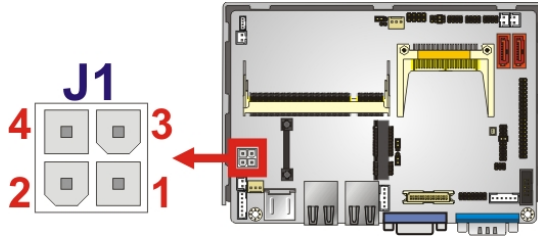
Internal peripheral connectors are found on the motherboard and are only accessible when the motherboard is outside of the chassis. This section has complete descriptions of all the internal, peripheral connectors on the WAFER-PV-D5252/D4252/N4552.

### 3.2.1 ATX Power Connector

- CN Label:** J1
- CN Type:** 4-pin connector
- CN Location:** See **Figure 3-2**
- CN Pinouts:** See **Table 3-3**

The ATX connector is connected to an external ATX power supply. Power is provided to the system, from the power supply through this connector.

# WAFER-PV-D5252/D4252/N4552 SBC



**Figure 3-2: ATX Power Connector Location**

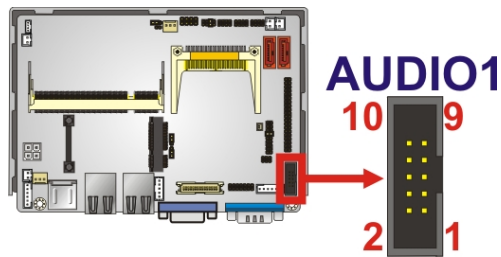
Pin	Description	Pin	Description
1	Ground	2	Ground
3	+12 V	4	+12V

**Table 3-3: ATX Power Connector Pinouts**

## 3.2.2 Audio Connector

- CN Label:** AUDIO1
- CN Type:** 10-pin header
- CN Location:** See **Figure 3-3**
- CN Pinouts:** See **Table 3-4**

This connector connects to speakers, a microphone and an audio input.



**Figure 3-3: Audio Connector Location**

Pin	Description	Pin	Description
1	LINE_OUTR	2	LINEIN_R
3	GND_AUDIO	4	GND_AUDIO
5	LINE_OUTL	6	LINEIN_L

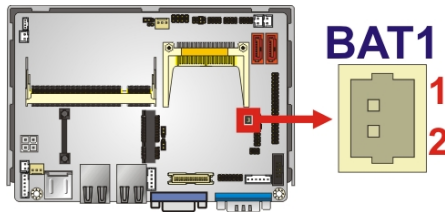
Pin	Description	Pin	Description
7	GND_AUDIO	8	GND_AUDIO
9	MICIN	10	MICIN

**Table 3-4: Audio Connector Pinouts**

### 3.2.3 Battery Connector

- CN Label:**           **BAT1**
- CN Type:**            2-pin wafer
- CN Location:**      See **Figure 3-4**
- CN Pinouts:**        See **Table 3-5**

This is connected to the system battery. The battery provides power to the system clock to retain the time when power is turned off.


**Figure 3-4: Battery Connector Location**

Pin	Description
1	Battery+
2	Ground

**Table 3-5: Battery Connector Pinouts**

### 3.2.4 CompactFlash® Slot

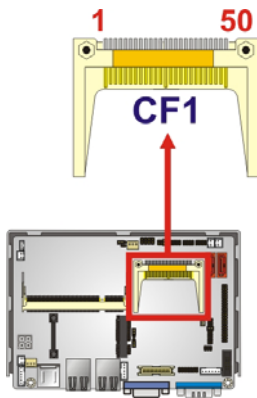
- CN Label:**           **CF1**
- CN Type:**            CompactFlash® card slot
- CN Location:**      See **Figure 3-5**
- CN Pinouts:**        See **Table 3-6**



**NOTE:**

1. Both CompactFlash® Type I and Type II cards are supported.
2. To use SATA and CF, CF must be set to slave.

A CompactFlash® Type I or Type II card can be used in this slot.



**Figure 3-5: CompactFlash® Slot Location**

Pin	Description	Pin	Description
1	GND	26	CD1#
2	D3	27	D11
3	D4	28	D12
4	D5	29	D13
5	D6	30	D14
6	D7	31	D15
7	CE#	32	CE2#
8	A10	33	VS1#
9	OE#	34	IOR#
10	A9	35	IOW#
11	A8	36	WE#
12	A7	37	IRQ
13	VCC	38	VCC

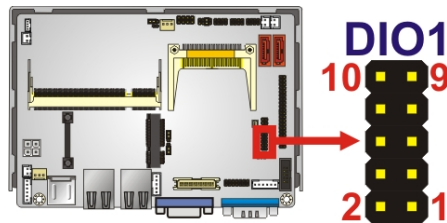
Pin	Description	Pin	Description
14	A6	39	CSEL#
15	A5	40	VS2#
16	A4	41	RESET#
17	A3	42	WAIT#
18	A2	43	INPACK#
19	A1	44	REG#
20	A0	45	BVD2
21	D0	46	BVD1
22	D1	47	D8
23	D2	48	D9
24	IOCS16#	49	D10
25	CD2#	50	GND2

**Table 3-6: CompactFlash® Slot Pinouts**

### 3.2.5 Digital I/O Connector

- CN Label:** DIO1
- CN Type:** 10-pin header
- CN Location:** See **Figure 3-6**
- CN Pinouts:** See **Table 3-7**

The digital I/O connector provides programmable input and output for external devices. The digital I/O provides 4-bit output and 4-bit input.



**Figure 3-6: Digital I/O Connector Location**



## WAFER-PV-D5252/D4252/N4552 SBC

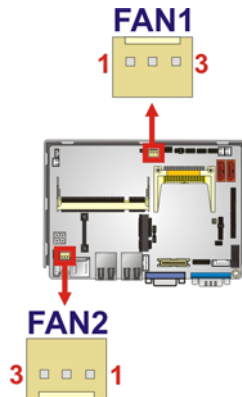
Pin	Description	Pin	Description
1	GND	2	VCC
3	Output 3	4	Output 2
5	Output 1	6	Output 0
7	Input 3	8	Input 2
9	Input 1	10	Input 0

**Table 3-7: Digital I/O Connector Pinouts**

### 3.2.6 Fan Connectors

- CN Label:** FAN1, FAN2
- CN Type:** 3-pin wafer
- CN Location:** See **Figure 3-7**
- CN Pinouts:** See **Table 3-8**

The fan connector attaches to a cooling fan.



**Figure 3-7: Fan Connector Locations**

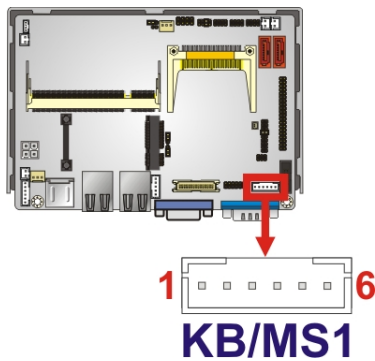
Pin	Description
1	FANIO1
2	+12V (PWM)
3	GND

**Table 3-8: Fan Connector Pinouts**

### 3.2.7 Keyboard/Mouse Connector

- CN Label:** KB/MS1
- CN Type:** 6-pin wafer
- CN Location:** See **Figure 3-8**
- CN Pinouts:** See **Table 3-9**

The keyboard/mouse connector connects to a PS/2 Y-cable that can be connected to a PS/2 keyboard and mouse.



**Figure 3-8: Keyboard/Mouse Connector Location**

Pin	Description
1	VCC
2	Mouse Data
3	Mouse Clock
4	Keyboard Data
5	Keyboard Clock
6	GND

**Table 3-9: Keyboard/Mouse Connector Pinouts**

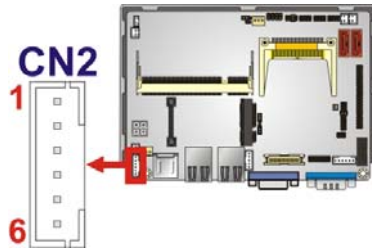
### 3.2.8 LED and +5 V Power Output Connector

- CN Label:** CN2
- CN Type:** 6-pin wafer
- CN Location:** See **Figure 3-9**

## WAFER-PV-D5252/D4252/N4552 SBC

**CN Pinouts:** See **Table 3-10**

The connector connects to the hard drive activity LED and power LED on the system front panel. +5 V power output is also provided via this connector.



**Figure 3-9: LED and +5 V Power Output Connector Location**

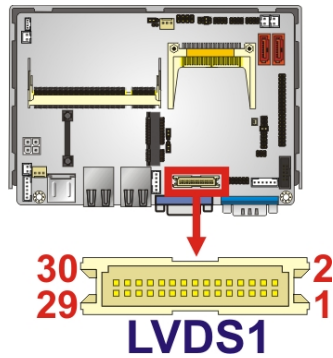
	Pin	Description
+5 V Power Output	1	+5 V
	2	GND
Power LED	3	VCC
	4	GND
HDD LED	5	VCC
	6	-HDLED

**Table 3-10: LED and +5 V Power Output Connector Pinouts**

### 3.2.9 LVDS Connector

**CN Label:** LVDS1  
**CN Type:** 30-pin crimp  
**CN Location:** See **Figure 3-10**  
**CN Pinouts:** See **Table 3-11**

The LVDS connector is for an LCD panel connected to the board.


**Figure 3-10: LVDS Connector Location**

Pin	Description	Pin	Description
1	GND1	2	GND2
3	A_Y0	4	A_Y0#
5	A_Y1	6	A_Y1#
7	A_Y2	8	A_Y2#
9	A_CK	10	A_CK#
11	NC	12	NC
13	GND3	14	GND4
15	NC	16	NC
17	NC	18	NC
19	NC	20	NC
21	NC	22	NC
23	NC	24	NC
25	GND5	26	GND6
27	VCC_LCD	28	VCC_LCD
29	VCC_LCD	30	VCC_LCD

**Table 3-11: LVDS Connector Pinouts**

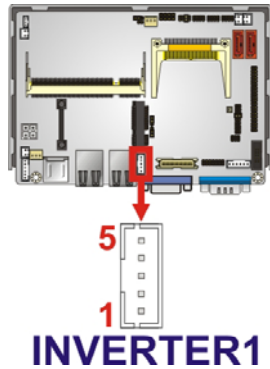
### 3.2.10 LVDS Backlight Connector

**CN Label:**           **INVERTER1**  
**CN Type:**            5-pin wafer  
**CN Location:**       See **Figure 3-11**

## WAFER-PV-D5252/D4252/N4552 SBC

**CN Pinouts:** See **Table 3-12**

The backlight inverter connector provides power to an LCD panel.



**Figure 3-11: Backlight Inverter Connector**

Pin	Description
1	LCD_BKLTCTL
2	GROUND
3	+12V
4	GROUND
5	BACKLIGHT ENABLE

**Table 3-12: Backlight Inverter Connector**

### 3.2.11 PCIe Mini Card Slot

**CN Label:** CN1

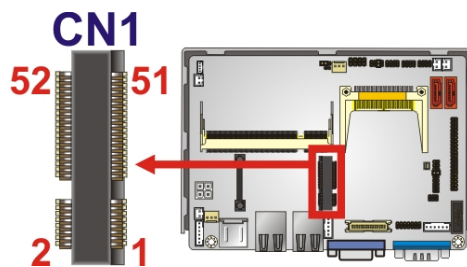
**CN Type:** PCIe Mini card slot

**CN Location:** See **Figure 3-12**

**CN Pinouts:** See **Table 3-13**

The PCIe mini card slot enables a PCIe mini card expansion module to be connected to the board. Cards supported include among others wireless LAN (WLAN) cards and IEI PCIe Mini disk on module (DOM) SSD cards.




**Figure 3-12: PCIe Mini Card Slot Location**

Pin	Description	Pin	Description
1	PCIE_WAKE#	2	VCC3
3	N/C	4	GND
5	N/C	6	1.5V
7	CLKREQ#	8	LFRAME#
9	GND	10	LAD3
11	CLK-	12	LAD2
13	CLK+	14	LAD1
15	GND	16	LAD0
17	PCIRST#	18	GND
19	LPC	20	VCC3
21	GND	22	PCIRST#
23	PERN2	24	3VDual
25	PERP2	26	GND
27	GND	28	1.5V
29	GND	30	SMBCLK
31	PETN2	32	SMBDATA
33	PETP2	34	GND
35	GND	36	USBD-
37	GND	38	USBD+
39	SATARXP	40	GND
41	SATARXN	42	N/C
43	N/C	44	RF_LINK#
45	SATATXN	46	BLUELED#
47	SATATXP	48	1.5V

## WAFER-PV-D5252/D4252/N4552 SBC

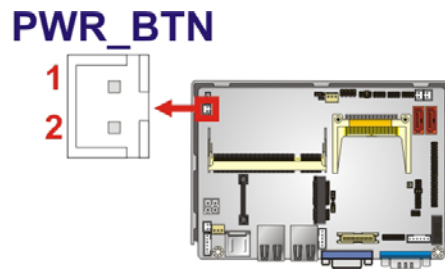
Pin	Description	Pin	Description
49	GND	50	GND
51	N/C	52	VCC3

**Table 3-13: PCIe Mini Card Slot Pinouts**

### 3.2.12 Power Button Connector

- CN Label:** PWR\_BTN
- CN Type:** 2-pin wafer
- CN Location:** See **Figure 3-13**
- CN Pinouts:** See **Table 3-14**

The power button connector is connected to a power switch on the system chassis.



**Figure 3-13: Power Button Connector Location**

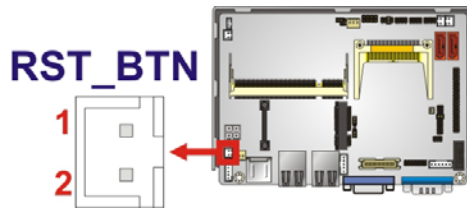
Pin	Description
1	Power Switch-
2	GND

**Table 3-14: Power Button Connector Pinouts**

### 3.2.13 Reset Button Connector

- CN Label:** RST\_BTN
- CN Type:** 2-pin wafer
- CN Location:** See **Figure 3-14**
- CN Pinouts:** See **Table 3-15**

The reset button connector is connected to a reset switch on the system chassis.



**Figure 3-14: Reset Button Connector Location**

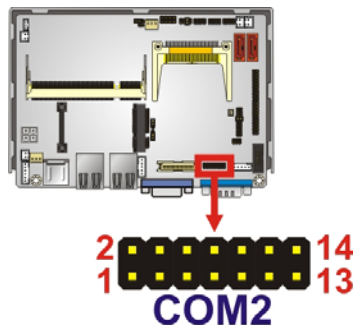
Pin	Description
1	PM_RESET
2	GND

**Table 3-15: Reset Button Connector Pinouts**

### 3.2.14 RS-232/422/485 Serial Port Connector (COM2)

- CN Label:** COM2
- CN Type:** 14-pin header
- CN Location:** See **Figure 3-15**
- CN Pinouts:** See **Table 3-16**

This connector provides RS-232, RS-422 or RS-485 communications.



**Figure 3-15: RS-232/422/485 Serial Port Connector Location**

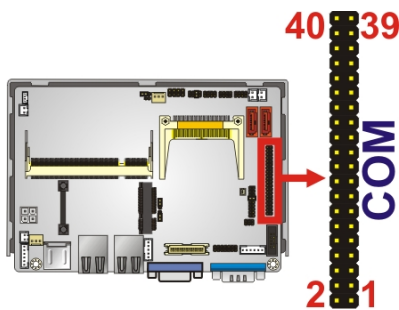
Pin	Description	Pin	Description
1	Data Carrier Direct (DCD)	2	Data Set Ready (DSR)
3	Receive Data (RXD)	4	Request To Send (RTS)
5	Transmit Data (TXD)	6	Clear To Send (CTS)
7	Data Terminal Ready (DTR)	8	Ring Indicator (RI)
9	Ground (GND)	10	N/C
11	TXD485+	12	TXD485#
13	RXD485+	14	RXD485#

**Table 3-16: RS-232/422/485 Serial Port Connector Pinouts**

### 3.2.15 RS-232 Serial Port Connector (COM3~COM6)

- CN Label:** COM
- CN Type:** 40-pin header
- CN Location:** See **Figure 3-16**
- CN Pinouts:** See **Table 3-17**

This connector provides RS-232 connections for four serial ports (COM3, COM4, COM5 and COM6).



**Figure 3-16: RS-232 Serial Port Connector Location**

Pin	Description	Pin	Description
1	DATA CARRIER DETECT (DCD3)	2	DATA SET READY (DSR3)
3	RECEIVE DATA (RXD3)	4	REQUEST TO SEND (RTS3)
5	TRANSMIT DATA (TXD3)	6	CLEAR TO SEND (CTS3)
7	DATA TERMINAL READY (DTR3)	8	RING INDICATOR (RI3)

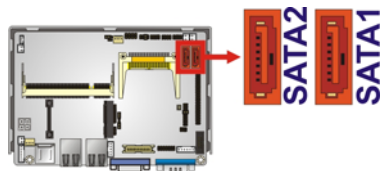
Pin	Description	Pin	Description
9	GND	10	GND
11	DATA CARRIER DETECT (DCD4)	12	DATA SET READY (DSR4)
13	RECEIVE DATA (RXD4)	14	REQUEST TO SEND (RTS4)
15	TRANSMIT DATA (TXD4)	16	CLEAR TO SEND (CTS4)
17	DATA TERMINAL READY (DTR4)	18	RING INDICATOR (RI4)
19	GND	20	GND
21	DATA CARRIER DETECT (DCD5)	22	DATA SET READY (DSR5)
23	RECEIVE DATA (RXD5)	24	REQUEST TO SEND (RTS5)
25	TRANSMIT DATA (TXD5)	26	CLEAR TO SEND (CTS5)
27	DATA TERMINAL READY (DTR5)	28	RING INDICATOR (RI5)
29	GND	30	GND
31	DATA CARRIER DETECT (DCD6)	32	DATA SET READY (DSR6)
33	RECEIVE DATA (RXD6)	34	REQUEST TO SEND (RTS6)
35	TRANSMIT DATA (TXD6)	36	CLEAR TO SEND (CTS6)
37	DATA TERMINAL READY (DTR6)	38	RING INDICATOR (RI6)
39	GND	40	GND

**Table 3-17: Serial Port Connector Pinouts**

### 3.2.16 SATA Drive Connectors

- CN Label:** SATA1, SATA2
- CN Type:** 7-pin SATA drive connectors
- CN Location:** See **Figure 3-17**
- CN Pinouts:** See **Table 3-18**

The SATA connectors connect to SATA hard drives or optical drives.



**Figure 3-17: SATA Drive Connector Location**



## WAFER-PV-D5252/D4252/N4552 SBC

Pin	Description
1	GND
2	TX+
3	TX-
4	GND
5	RX-
6	RX+
7	GND

Table 3-18: SATA Drive Connector Pinouts

### 3.2.17 SATA Power Connectors

**CN Label:** SATA\_PWR1, SATA\_PWR2

**CN Type:** 2-pin wafer

**CN Location:** See Figure 3-18

**CN Pinouts:** See Table 3-19

Use the SATA Power Connector to connect to SATA device power connections.

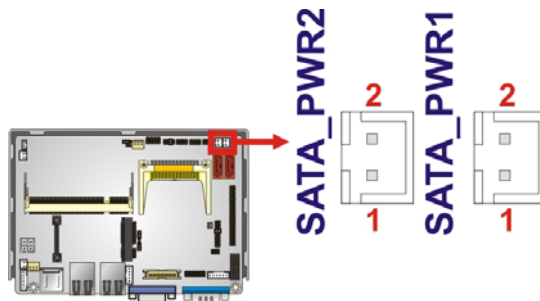


Figure 3-18: SATA Power Connector Locations

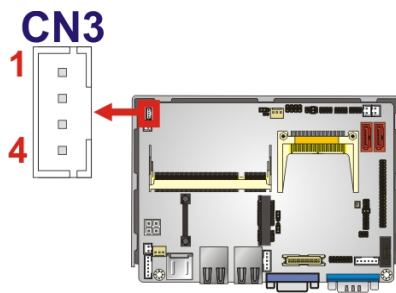
Pin	Description
1	VCC5
2	GND

Table 3-19: SATA Power Connector Pinouts

### 3.2.18 SMBus Connector

- CN Label:** CN3
- CN Type:** 4-pin wafer
- CN Location:** See **Figure 3-19**
- CN Pinouts:** See **Table 3-20**

The SMBus (System Management Bus) connector provides low-speed system management communications.



**Figure 3-19: SMBus Connector Location**

Pin	Description
1	GND
2	SMBDATA
3	SMBCLK
4	+5V

**Table 3-20: SMBus Connector Pinouts**

### 3.2.19 SO-DIMM Connector

- CN Label:** DIMM1
- CN Type:** 204-pin DDR3 SO-DIMM connector
- CN Location:** See **Figure 3-20**

The SO-DIMM connector is for installing memory on the system.

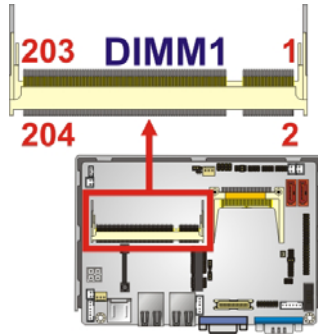


Figure 3-20: SO-DIMM Connector Location

### 3.2.20 SPI Flash Connector

- CN Label:** JSPI1
- CN Type:** 8-pin header
- CN Location:** See Figure 3-21
- CN Pinouts:** See Table 3-21

The 8-pin SPI Flash connector is used to flash the BIOS.

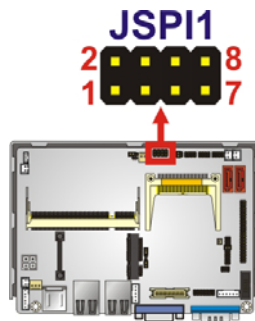


Figure 3-21: SPI Flash Connector Location

Pin	Description	Pin	Description
1	VCC	2	GND
3	CS#	4	CLOCK
5	SO	6	SI
7	NC	8	NC

Table 3-21: SPI Flash Connector Pinouts

### 3.2.21 USB Connectors

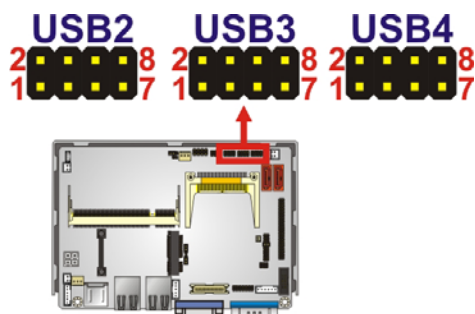
**CN Label:** USB2, USB3, USB4

**CN Type:** 8-pin header

**CN Location:** See **Figure 3-22**

**CN Pinouts:** See **Table 3-22**

The USB header can connect to two USB devices.



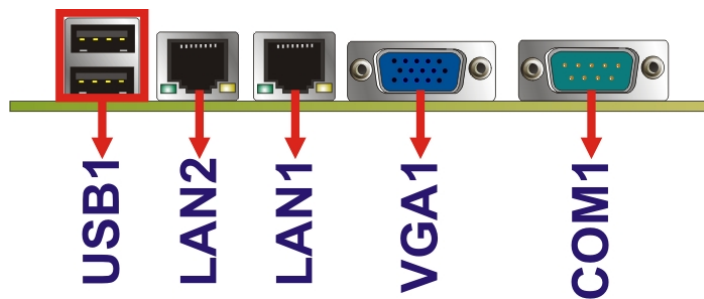
**Figure 3-22: USB Connector Location**

Pin	Description	Pin	Description
1	VCC	2	GND
3	DATA-	4	DATA+
5	DATA+	6	DATA-
7	GND	8	VCC

**Table 3-22: USB Port Connector Pinouts**

### 3.3 External Interface Connectors

Figure 3-23 shows the WAFER-PV-D5252/D4252/N4552 motherboard external interface connectors. The WAFER-PV-D5252/D4252/N4552 on-board external interface connectors are shown in Figure 3-23.



**Figure 3-23: External Interface Connectors**

#### 3.3.1 Ethernet Connectors

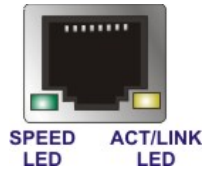
- CN Label:** LAN1, LAN2
- CN Type:** RJ-45
- CN Location:** See **Figure 3-23**
- CN Pinouts:** See **Table 3-23**

A 10/100/1000 Mb/s connection can be made to a Local Area Network.

Pin	Description	Pin	Description
1	MDIA3-	5	MDIA1+
2	MDIA3+	6	MDIA2+-
3.	MDIA2-	7	MDIA0-
4.	MDIA1-	8	MDIA0+

**Table 3-23: Ethernet Connector Pinouts**




**Figure 3-24: Ethernet Connector**

The RJ-45 Ethernet connector has two status LEDs, one green and one yellow. The green LED indicates activity on the port and the yellow LED indicates the port is linked. See **Table 3-24**.

SPEED LED		ACT/LINK LED	
STATUS	DESCRIPTION	STATUS	DESCRIPTION
OFF	10Mbps connection	OFF	No link
GREEN	100Mbps connection	YELLOW	Linked
ORANGE	1Gbps connection	BLINKING	Data Activity

**Table 3-24: RJ-45 Ethernet Connector LEDs**

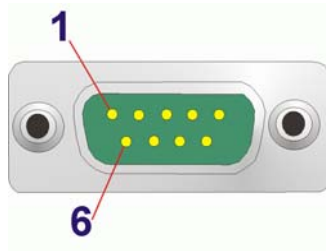
### 3.3.2 Serial Port Connector

- CN Label:** COM1
- CN Type:** DB-9 Male connector
- CN Location:** See **Figure 3-23**
- CN Pinouts:** See **Table 3-25** and **Figure 3-25**

The RS-232 serial connector provides serial connection in the RS-232 mode.

Pin	Description	Pin	Description
1	DCD	6	DSR
2	RXD	7	RTS
3	TXD	8	CTS
4	DTR	9	RI
5	GND		

**Table 3-25: Serial Port Pinouts**



**Figure 3-25: Serial Port Pinout Locations**

### 3.3.3 USB Ports

- CN Label:** USB1
- CN Type:** USB Combo ports
- CN Location:** See **Figure 3-23**
- CN Pinouts:** See **Table 3-26**

The USB ports attach to standard USB devices.

Pin	Description
1	VCC
2	Data1-
3	Data1+
4	Ground
5	VCC
6	Data2-
7	Data2+
8	Ground

**Table 3-26: USB Connector Pinouts**

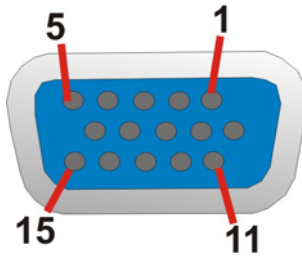
### 3.3.4 VGA Connector

- CN Label:** VGA1
- CN Type:** D-sub 15-pin female connector
- CN Location:** See **Figure 3-23**
- CN Pinouts:** See **Figure 3-26** an **Table 3-27**

The standard 15-pin female VGA connector connects to a CRT or LCD monitor.

Pin	Description	Pin	Description	Pin	Description
1	RED	6	GROUND	11	NC
2	GREEN	7	GROUND	12	SDA
3	BLUE	8	GROUND	13	HSYNC
4	+5V	9	NC	14	VSYNC
5	GROUND	10	GROUND	15	SCL

**Table 3-27: VGA Connector Pinouts**



**Figure 3-26: VGA Connector**

Chapter

4

# Installation

---

## 4.1 Anti-static Precautions

---



### WARNING:

Failure to take ESD precautions during installation may result in permanent damage to the product and severe injury to the user.

---

Electrostatic discharge (ESD) can cause serious damage to electronic components, including the WAFER-PV-D5252/D4252/N4552. Dry climates are especially susceptible to ESD. It is therefore critical to strictly adhere to the following anti-static precautions whenever the WAFER-PV-D5252/D4252/N4552, or any other electrical component, is handled.

- **Wear an anti-static wristband:** - Wearing a simple anti-static wristband can help to prevent ESD from damaging the board.
- **Self-grounding:** - Before handling the board touch any grounded conducting material. During the time the board is handled, frequently touch any conducting materials that are connected to the ground.
- **Use an anti-static pad:** When configuring the WAFER-PV-D5252/D4252/N4552, place it on an anti-static pad. This reduces the possibility of ESD damaging the WAFER-PV-D5252/D4252/N4552.
- **Only handle the edges of the PCB:-** When handling the PCB, hold it by the edges.



## 4.2 Installation Considerations

---



### NOTE:

The following installation notices and installation considerations should be read and understood before the WAFER-PV-D5252/D4252/N4552 is installed. All installation notices pertaining to the installation of WAFER-PV-D5252/D4252/N4552 should be strictly adhered to. Failing to adhere to these precautions may lead to severe damage of the WAFER-PV-D5252/D4252/N4552 and injury to the person installing the motherboard.

---



### WARNING:

The installation instructions described in this manual should be carefully followed in order to prevent damage to the WAFER-PV-D5252/D4252/N4552, WAFER-PV-D5252/D4252/N4552 components and injury to the user.

---

Before and during the installation please **DO** the following:

- **Read the user manual:**
  - The user manual provides a complete description of the installation instructions and configuration options.
- **Wear an electrostatic discharge cuff (ESD):**
  - Electronic components are easily damaged by ESD. Wearing an ESD cuff removes ESD from the body and helps prevent ESD damage.
- **Place on an antistatic pad:**
  - When installing or configuring the motherboard, place it on an antistatic pad. This helps to prevent potential ESD damage.
- **Turn all power off:**
  - Make sure the product is disconnected from all power supplies and that no electricity is being fed into the system.

Before and during the installation of the WAFER-PV-D5252/D4252/N4552 **DO NOT**:

- **DO NOT** remove any of the stickers on the PCB board. These stickers are required for warranty validation.
- **DO NOT** use the product before verifying all the cables and power connectors are properly connected.
- **DO NOT** allow screws to come in contact with the PCB circuit, connector pins, or its components.

### 4.3 SO-DIMM Installation

To install an SO-DIMM, please follow the steps below and refer to Figure 4-1.

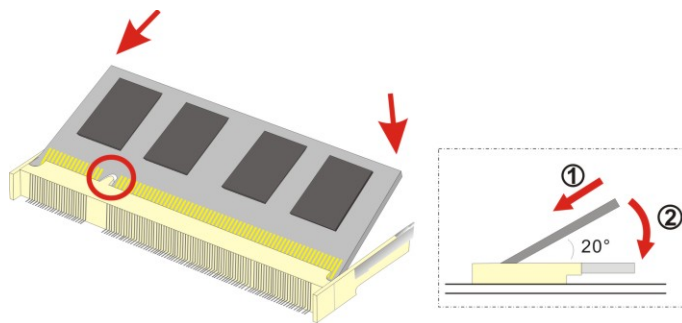


Figure 4-1: SO-DIMM Installation

- Step 1:** **Locate the SO-DIMM socket.** Place the board on an anti-static mat.
- Step 2:** **Align the SO-DIMM with the socket.** Align the notch on the memory with the notch on the memory socket.
- Step 3:** **Insert the SO-DIMM.** Push the memory in at a 20° angle. (See Figure 4-1)
- Step 4:** **Seat the SO-DIMM.** Gently push downwards and the arms clip into place. (See Figure 4-1)

## 4.4 CompactFlash® Installation



### NOTE:

Both CompactFlash® Type I and Type II cards are supported.

To install the CompactFlash® card, please follow the steps below.

- Step 1:** **Locate the CF card socket.** Locate the CompactFlash® slot.
- Step 2:** **Align the CF card.** Align the CompactFlash® card. The label side should be facing away from the board. The grooves on the CompactFlash® slot ensure that the card cannot be inserted the wrong way.
- Step 3:** **Insert the CF card.** Push until the CompactFlash® card is firmly seated in the slot. See Figure 4-2.

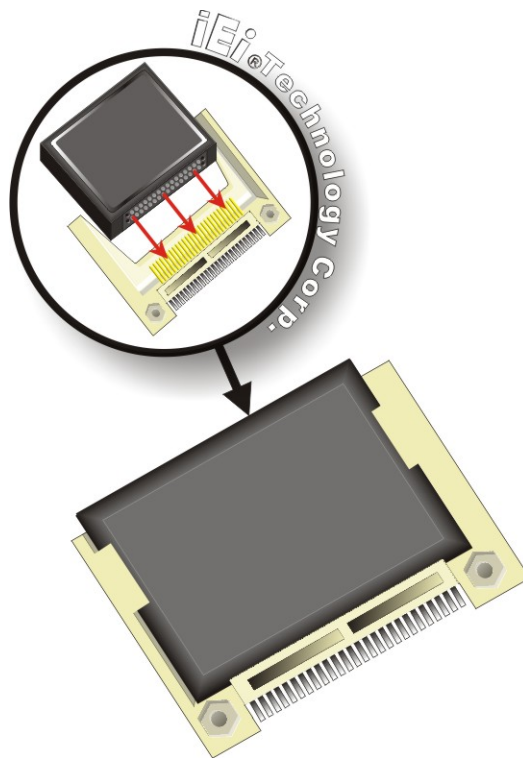


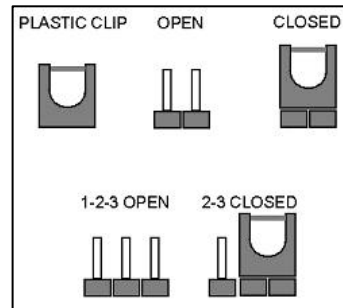
Figure 4-2: CompactFlash® Card Installation

## 4.5 Jumper Settings



### NOTE:

A jumper is a metal bridge that is used to close an electrical circuit. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To CLOSE/SHORT a jumper means connecting the pins of the jumper with the plastic clip and to OPEN a jumper means removing the plastic clip from a jumper.



Before the WAFER-PV-D5252/D4252/N4552 is installed in the system, the jumpers must be set in accordance with the desired configuration. The jumpers on the WAFER-PV-D5252/D4252/N4552 are listed in Table 4-1.

Description	Label	Type
AT/ATX mode selection	J_AUTOPWR1	3-pin header
Clear CMOS	J_CMOS1	3-pin header
LVDS voltage selection	J_VLVDS1	3-pin header
LVDS resolution selection	J_LCD_TYPE1	8-pin header
CompactFlash® master/slave selection	JCF1	2-pin header
COM2 RS-232/422/485 selection	JP1	8-pin header
COM1 pin-9 voltage selection	JP2	6-pin header

**Table 4-1: Jumpers**

### 4.5.1 AT/ATX Mode Selection

**Jumper Label:** J\_AUTOPWR1

**Jumper Type:** 3-pin header

## WAFER-PV-D5252/D4252/N4552 SBC

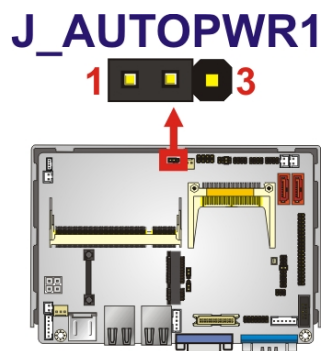
**Jumper Settings:** See Table 4-2

**Jumper Location:** See Figure 4-3

Set both of the jumpers select AT or ATX power mode for the WAFER-PV-D5252/D4252/N4552. AT power mode limits the system to on/off. ATX allows the system to use various power saving states and enter a standby state, so the system can be turned on remotely over a network. The settings on both jumpers should be the same.

Pin	Description
Short 1-2	ATX mode (Default)
Short 2-3	AT mode

**Table 4-2: AT/ATX Mode Selection**



**Figure 4-3: AT/ATX Mode Selection Jumper Location**

### 4.5.2 Clear CMOS Jumper

**Jumper Label:** J\_CMOS1

**Jumper Type:** 3-pin header

**Jumper Settings:** See Table 4-3

**Jumper Location:** See Figure 4-4

If the WAFER-PV-D5252/D4252/N4552 fails to boot due to improper BIOS settings, the clear CMOS jumper clears the CMOS data and resets the system BIOS information. To do this, use the jumper cap to close pins 2 and 3 for a few seconds then reinstall the jumper clip back to pins 1 and 2.



If the “CMOS Settings Wrong” message is displayed during the boot up process, the fault may be corrected by pressing the F1 to enter the CMOS Setup menu. Do one of the following:

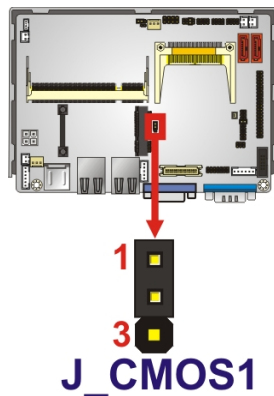
- Enter the correct CMOS setting
- Load Optimal Defaults
- Load Failsafe Defaults.

After having done one of the above, save the changes and exit the CMOS Setup menu.

The clear CMOS jumper settings are shown in Table 4-3.

Pin	Description
1-2	Keep CMOS Setup (Default)
2-3	Clear CMOS Setup

**Table 4-3: Clear CMOS Jumper Settings**



**Figure 4-4: Clear CMOS Jumper Location**

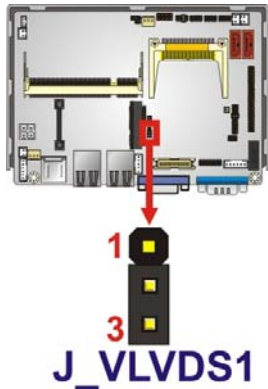
### 4.5.3 LVDS Voltage Selection

<b>Jumper Label:</b>	<b>J_VLVDS1</b>
<b>Jumper Type:</b>	3-pin header
<b>Jumper Settings:</b>	See <b>Table 4-4</b>
<b>Jumper Location:</b>	See <b>Figure 4-5</b>

Selects the voltage of the LVDS connector.

Pin	Description
Short 1-2	+5 V
Short 2-3	+3.3 V (Default)

**Table 4-4: LVDS Voltage Selection**



**Figure 4-5: LVDS Voltage Selection Jumper Location**

#### 4.5.4 LVDS Resolution Selection

- Jumper Label:** J\_LCD\_TYPE1
- Jumper Type:** 8-pin header
- Jumper Settings:** See Table 4-5
- Jumper Location:** See Figure 4-6

Selects the resolution of the LCD panel connected to the LVDS1 connector.

Pin	Description
Open	640 x 480 (18-bit)
Short 1-2	800 x 480 (18-bit)
Short 3-4	800 x 600 (18-bit) (Default)
Short 1-2, 3-4	1024 x 768 (18-bit)
Short 5-6	1280 x 1024 (18-bit)
Short 1-2, 5-6	1366 x 768 (18-bit)
Short 3-4, 5-6	1280 x 800 (18-bit)
Short 1-2, 3-4, 5-6	1280 x 600 (18-bit)

**Table 4-5: LVDS Resolution Selection**

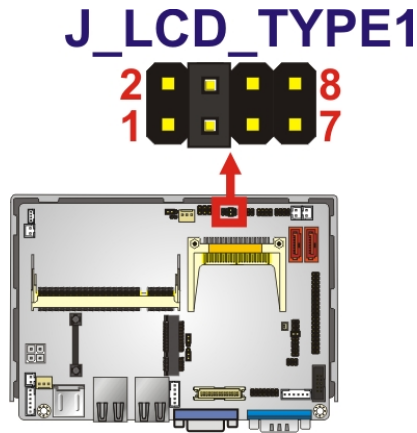


Figure 4-6: LVDS Resolution Selection Jumper Location

#### 4.5.5 CompactFlash® Master/Slave Selection

- Jumper Label:** JCF1
- Jumper Type:** 2-pin header
- Jumper Settings:** See Table 4-6
- Jumper Location:** See Figure 4-7



**NOTE:**

To use SATA and CF, CF must be set as slave

Sets the CompactFlash® slot as the IDE primary master or primary slave.

Pin	Description
Closed	Master
Open	Slave (Default)

Table 4-6: CompactFlash® Master/Slave Selection

WAFER-PV-D5252/D4252/N4552 SBC

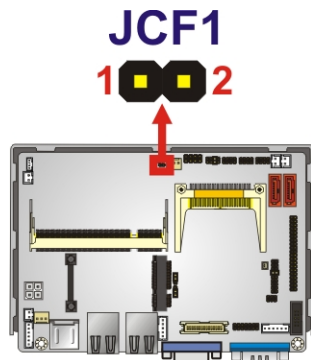


Figure 4-7: CompactFlash® Master/Slave Selection Jumper Location

4.5.6 COM2 RS-232/422/485 Selection

- Jumper Label:** JP1
- Jumper Type:** 8-pin header
- Jumper Settings:** See Table 4-7
- Jumper Location:** See Figure 4-8

Selects the communication method of COM2 serial port.

Pin	Description
Short 1-2	RS-232 (Default)
Short 3-4	RS-422
Short 5-6	RS-485
Short 5-6, 7-8	RS-485 with RTS control

Table 4-7: COM3 RS-232/422/485 Selection

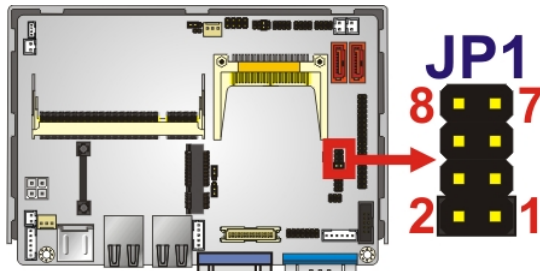


Figure 4-8: COM3 RS-232/422/485 Selection Jumper Location

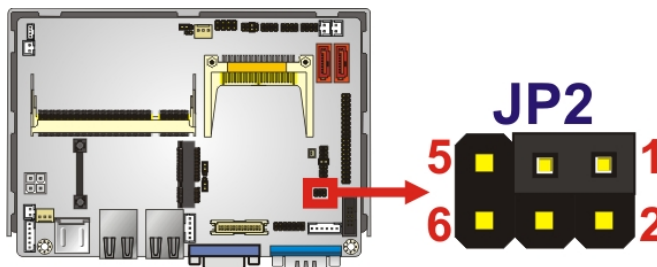
### 4.5.7 COM 1 Pin 9 Setting Jumper

<b>Jumper Label:</b>	<b>JP2</b>
<b>Jumper Type:</b>	6-pin header
<b>Jumper Settings:</b>	See Table 4-8
<b>Jumper Location:</b>	See Figure 4-9

The COM 1 Pin 9 Setting jumper configures pin 9 on COM 1 as either a +5 V, +12 V power source or as a ring-in (RI) line. The COM 1 Pin 9 Setting jumper selection options are shown in Table 4-8.

Setting	Description
Short 1-3	COM 1 RI Pin use +5 V (Default)
Short 3-5	COM 1 RI Pin use +12 V
Short 3-4	COM 1 RI Pin use RI

**Table 4-8: COM 1 Pin 9 Setting Jumper Settings**



**Figure 4-9: COM 1 Pin 9 Setting Jumper Location**



## 4.6 Chassis Installation

### 4.6.1 Airflow

**WARNING:**

Airflow is critical for keeping components within recommended operating temperatures. The chassis should have fans and vents as necessary to keep things cool.

---

The WAFER-PV-D5252/D4252/N4552 must be installed in a chassis with ventilation holes on the sides allowing airflow to travel through the heat sink surface. In a system with an individual power supply unit, the cooling fan of a power supply can also help generate airflow through the board surface.

### 4.6.2 Motherboard Installation

To install the WAFER-PV-D5252/D4252/N4552 motherboard into the chassis please refer to the reference material that came with the chassis.

## 4.7 Internal Peripheral Device Connections

This section outlines the installation of peripheral devices to the onboard connectors.

### 4.7.1 AT Power Connection

Follow the instructions below to connect the WAFER-PV-D5252/D4252/N4552 to an AT power supply.

---

**WARNING:**

Disconnect the power supply power cord from its AC power source to prevent a sudden power surge to the WAFER-PV-D5252/D4252/N4552.

---

**Step 1:** Locate the power cable. The power cable is shown in the packing list in Chapter 3.

**Step 2:** Connect the Power Cable to the Motherboard. Connect the 4-pin (2x2) Molex type power cable connector to the AT power connector on the motherboard. See Figure 4-10.

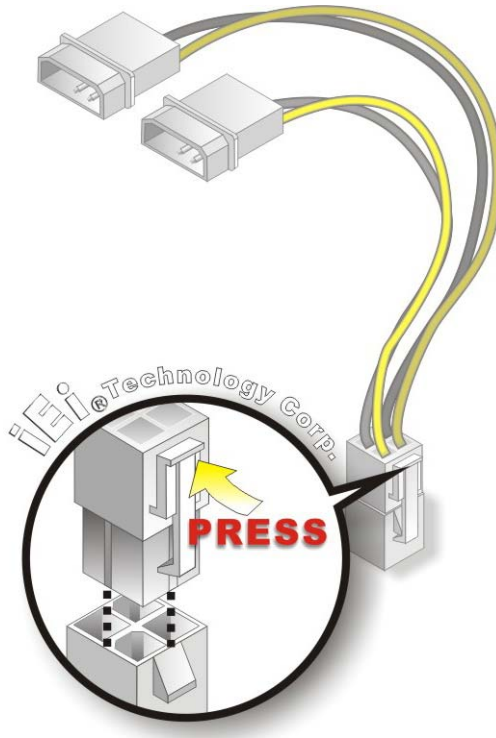


Figure 4-10: Power Cable to Motherboard Connection

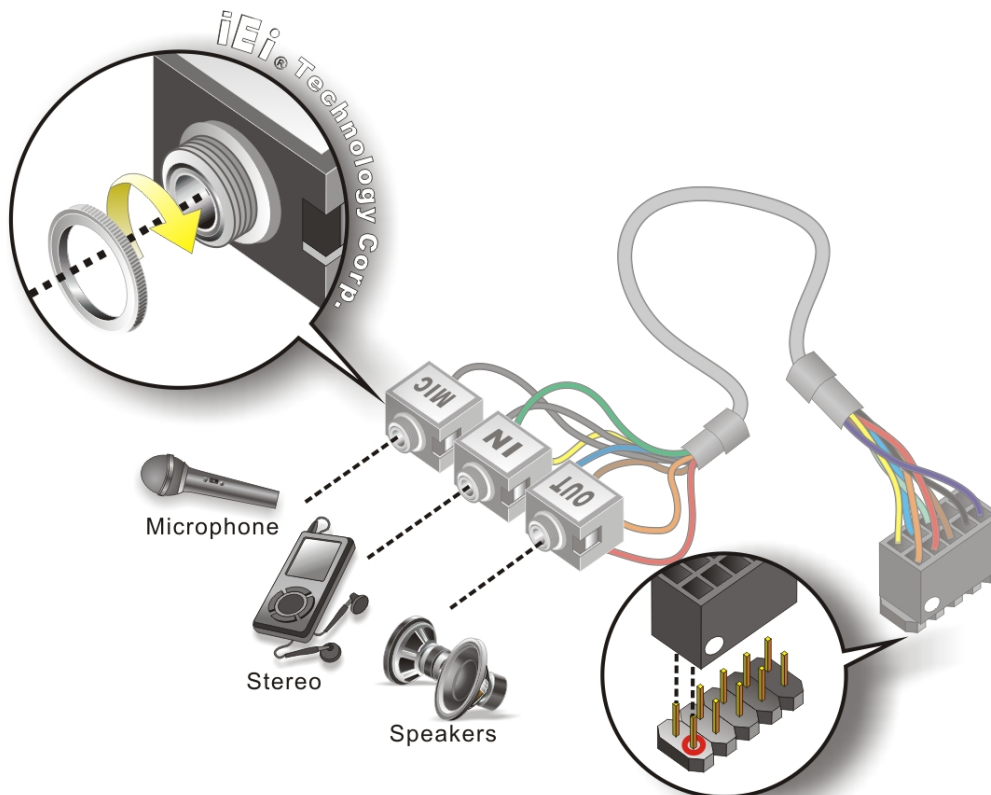
**Step 3:** Connect Power Cable to Power Supply. Connect one of the 4-pin (1x4) Molex type power cable connectors to an AT power supply. See Figure 4-12.

#### 4.7.2 Audio Kit Installation

The Audio Kit that came with the WAFER-PV-D5252/D4252/N4552 connects to the audio connector on the WAFER-PV-D5252/D4252/N4552. The audio kit consists of three audio jacks. Mic-in connects to a microphone. Line-in provides a stereo line-level input to connect to the output of an audio device. Line-out, a stereo line-level output, connects to two amplified speakers. To install the audio kit, please refer to the steps below:

## WAFER-PV-D5252/D4252/N4552 SBC

- Step 1:** **Locate the audio connector.** The location of the 10-pin audio connector is shown in **Chapter 3**.
- Step 2:** **Align pin 1.** Align pin 1 on the on-board connector with pin 1 on the audio kit connector. Pin 1 on the audio kit connector is indicated with a white dot. See Figure 4-11.



**Figure 4-11: Audio Kit Cable Connection**

- Step 3:** **Connect the audio devices.** Connect speakers to the line-out audio jack. Connect the output of an audio device to the line-in audio jack. Connect a microphone to the mic-in audio jack.

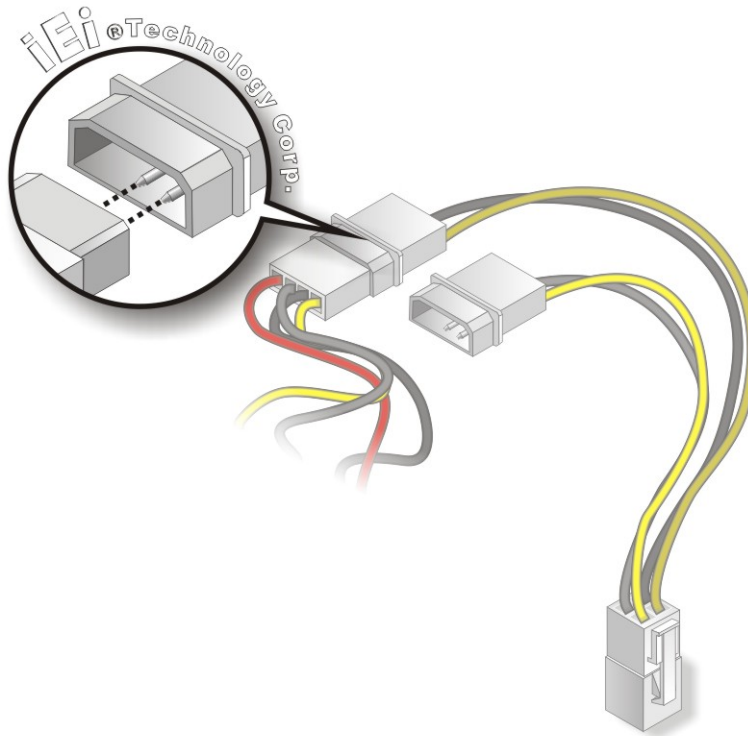


Figure 4-12: Connect Power Cable to Power Supply

### 4.7.3 Keyboard/Mouse Connector

The WAFER-PV-D5252/D4252/N4552 is shipped with a keyboard/mouse Y-cable connector. The keyboard/mouse Y-cable connector connects to a keyboard/mouse connector on the WAFER-PV-D5252/D4252/N4552 and branches into two cables that are each connected to a PS/2 connector, one for a mouse and one for a keyboard. To connect the keyboard/mouse Y-cable connector please follow the steps below.

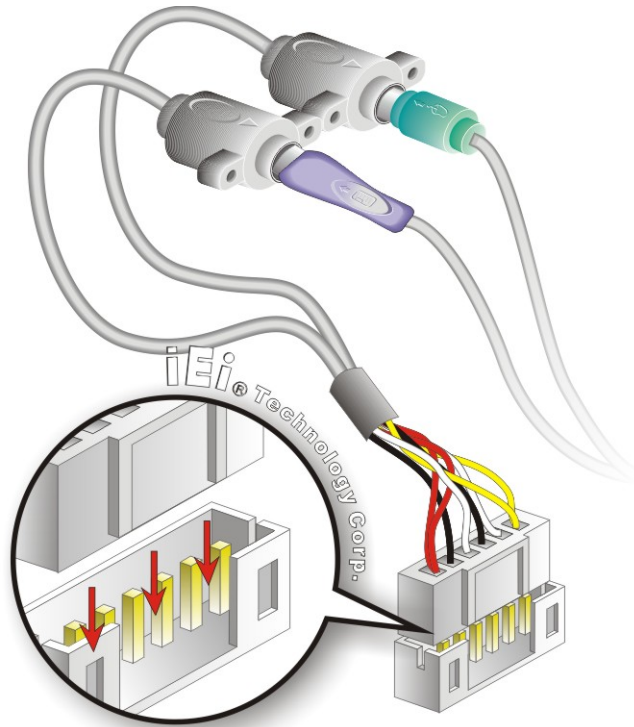
**Step 1: Locate the connector.** The location of the keyboard/mouse Y-cable connector is shown in **Chapter 3**.

**Step 2: Align the connectors.** Correctly align pin 1 on the cable connector with pin 1 on the WAFER-PV-D5252/D4252/N4552 keyboard/mouse connector. See Figure 4-13.



**WAFER-PV-D5252/D4252/N4552 SBC**

**Step 3:** **Insert the cable connectors** Once the cable connector is properly aligned with the keyboard/mouse connector on the WAFER-PV-D5252/D4252/N4552, connect the cable connector to the on-board connectors. See Figure 4-13.



**Figure 4-13: Keyboard/mouse Y-cable Connection**

**Step 4:** **Attach PS/2 connectors to the chassis.** The keyboard/mouse Y-cable connector is connected to two PS/2 connectors. To secure the PS/2 connectors to the chassis please refer to the installation instructions that came with the chassis.

**Step 5:** **Connect the keyboard and mouse.** Once the PS/2 connectors are connected to the chassis, a keyboard and mouse can each be connected to one of the PS/2 connectors. The keyboard PS/2 connector and mouse PS/2 connector are both marked. Please make sure the keyboard and mouse are connected to the correct PS/2 connector.



#### 4.7.4 Four Serial Port Connector

The 40-pin serial port connector connects the board connector to four DB-9 connectors. To install, please follow the steps below.

**Step 1:** **Locate the COM connector.** The locations of the COM port connectors are shown in Chapter 4.

**Step 2:** **Insert the cable connector.** Align the cable connector with the onboard connector. Make sure pin 1 on the board and connector line up..

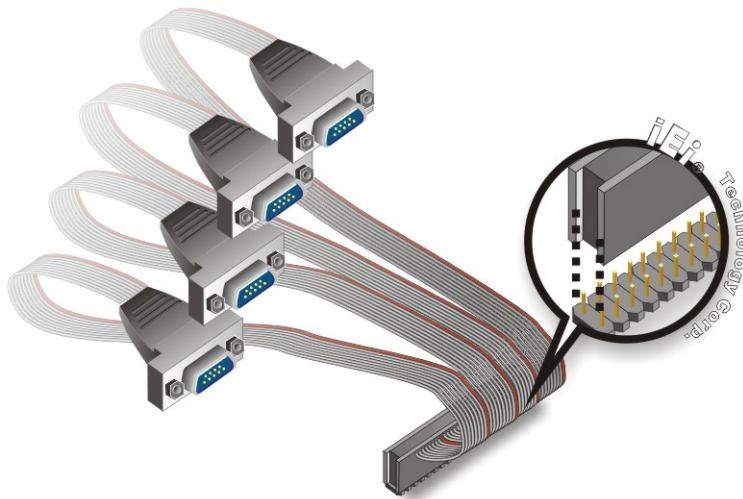


Figure 4-14: Four Serial Port Connector

**Step 3:** **Secure the serial ports to the chassis.** Tighten the screws on the DB-9 connectors to secure them to the chassis.

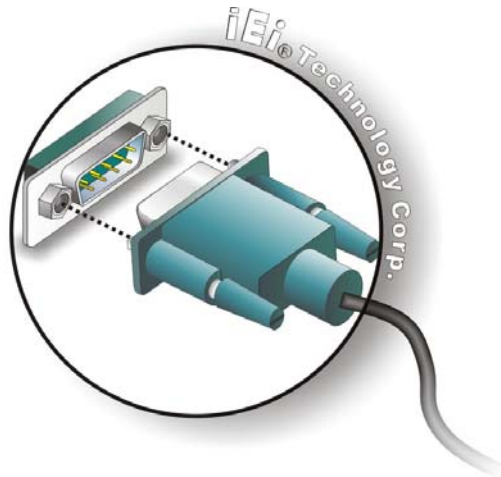


Figure 4-15: Serial Device Connector

## 4.8 External Peripheral Interface Connection

Devices can be connected to the external connectors. To install external devices, follow the directions in the subsections below.

### 4.8.1 LAN Connection (Single Connector)

There are two external RJ-45 LAN connectors. The RJ-45 connectors enable connection to an external network. To connect a LAN cable with an RJ-45 connector, please follow the instructions below.

**Step 1: Locate the RJ-45 connectors.** The locations of the USB connectors are shown in **Chapter 4**.

**Step 2: Align the connectors.** Align the RJ-45 connector on the LAN cable with one of the RJ-45 connectors on the WAFER-PV-D5252/D4252/N4552. See **Figure 4-16**.

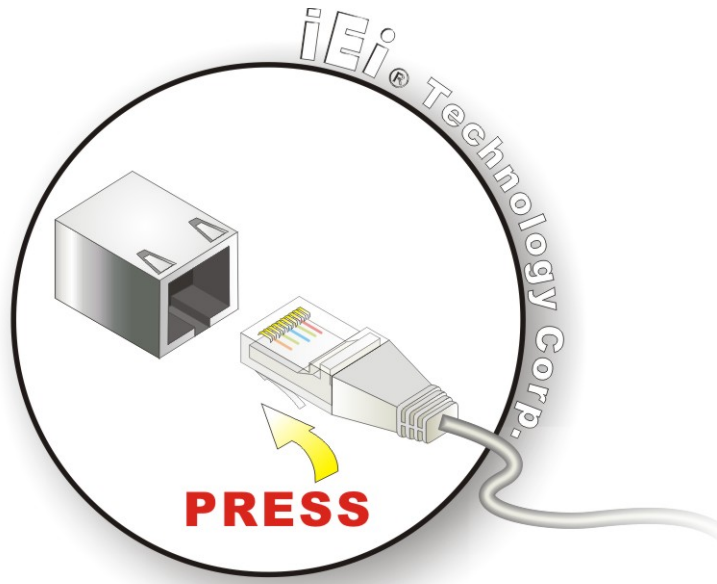


Figure 4-16: LAN Connection

**Step 3:** Insert the LAN cable RJ-45 connector. Once aligned, gently insert the LAN cable RJ-45 connector into the on-board RJ-45 connector.

#### 4.8.2 Serial Device Connection

The WAFER-PV-D5252/D4252/N4552 has a single female DB-9 connector on the external peripheral interface panel for a serial device. Follow the steps below to connect a serial device to the WAFER-PV-D5252/D4252/N4552.

**Step 1:** Locate the DB-9 connector. The location of the DB-9 connector is shown in Chapter 3.

**Step 2:** Insert the serial connector. Insert the DB-9 connector of a serial device into the DB-9 connector on the external peripheral interface. See Figure 4-17.

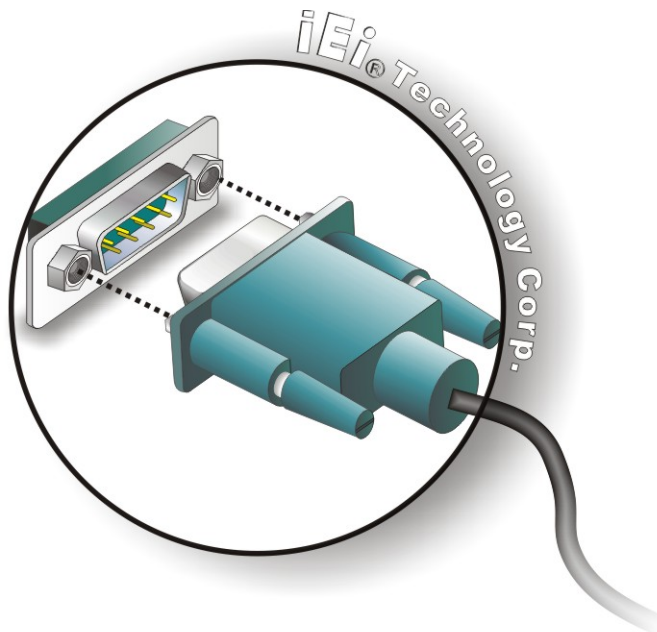


Figure 4-17: Serial Device Connector

**Step 3: Secure the connector.** Secure the serial device connector to the external interface by tightening the two retention screws on either side of the connector.

### 4.8.3 USB Connection (Dual Connector)

The external USB Series "A" receptacle connectors provide easier and quicker access to external USB devices. Follow the steps below to connect USB devices to the WAFER-PV-D5252/D4252/N4552.

**Step 1: Locate the USB Series "A" receptacle connectors.** The location of the USB Series "A" receptacle connectors are shown in **Chapter 3**.

**Step 2: Insert a USB Series "A" plug.** Insert the USB Series "A" plug of a device into the USB Series "A" receptacle on the external peripheral interface. See Figure 4-18.



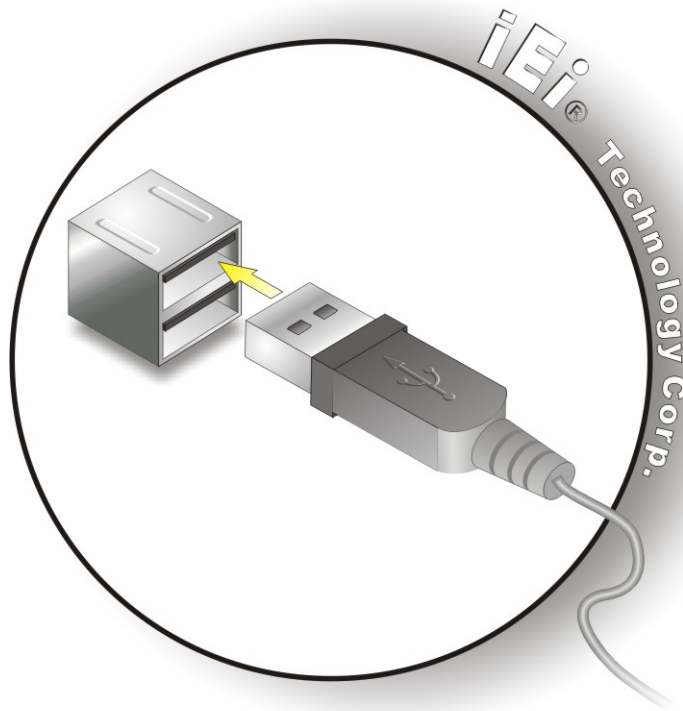


Figure 4-18: USB Connector

#### 4.8.4 VGA Monitor Connection

The WAFER-PV-D5252/D4252/N4552 has a single female DB-15 connector on the external peripheral interface panel. The DB-15 connector is connected to a CRT or VGA monitor. To connect a monitor to the WAFER-PV-D5252/D4252/N4552, please follow the instructions below.

- Step 1:** **Locate the female DB-15 connector.** The location of the female DB-15 connector is shown in **Chapter 3**.
- Step 2:** **Align the VGA connector.** Align the male DB-15 connector on the VGA screen cable with the female DB-15 connector on the external peripheral interface.
- Step 3:** **Insert the VGA connector** Once the connectors are properly aligned with the insert the male connector from the VGA screen into the female connector on the WAFER-PV-D5252/D4252/N4552. See Figure 4-19.



## WAFER-PV-D5252/D4252/N4552 SBC

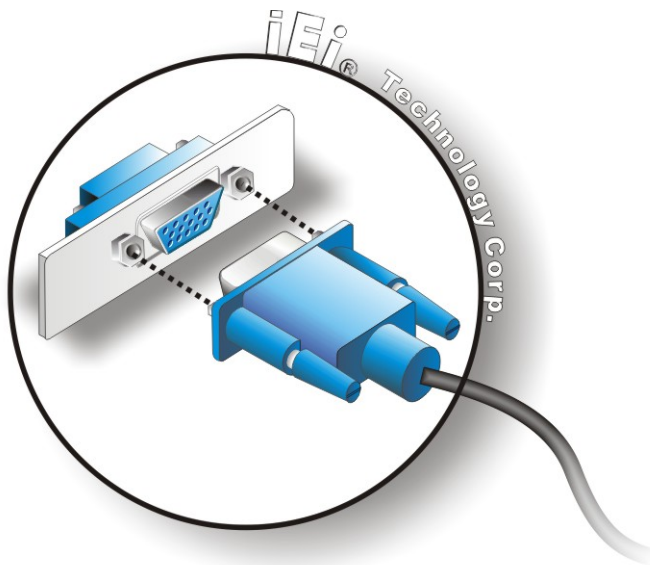


Figure 4-19: VGA Connector

**Step 4:** **Secure the connector.** Secure the DB-15 VGA connector from the VGA monitor to the external interface by tightening the two retention screws on either side of the connector.

## 4.9 Software Installation

All the drivers for the WAFER-PV-D5252/D4252/N4552 are on the CD that came with the system. To install the drivers, please follow the steps below.

**Step 1:** Insert the CD into a CD drive connected to the system.

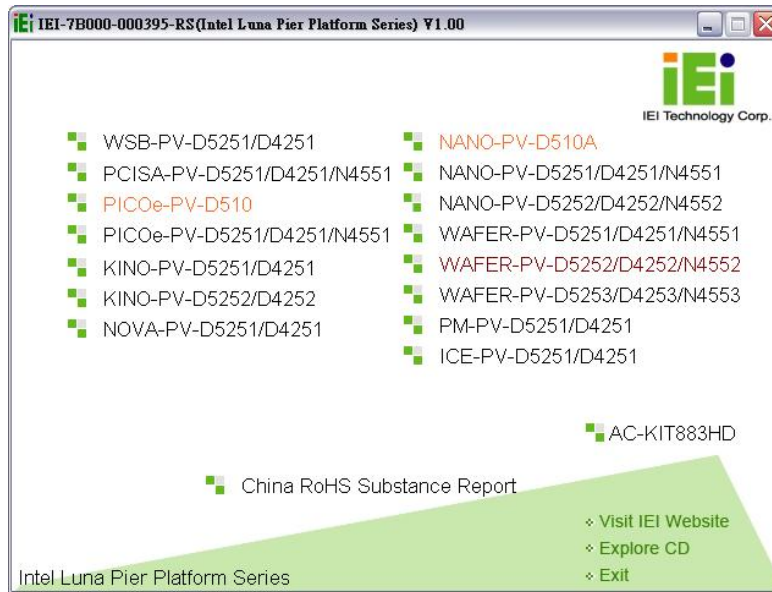


**NOTE:**

If the installation program doesn't start automatically:  
Click "Start->My Computer->CD Drive->autorun.exe"

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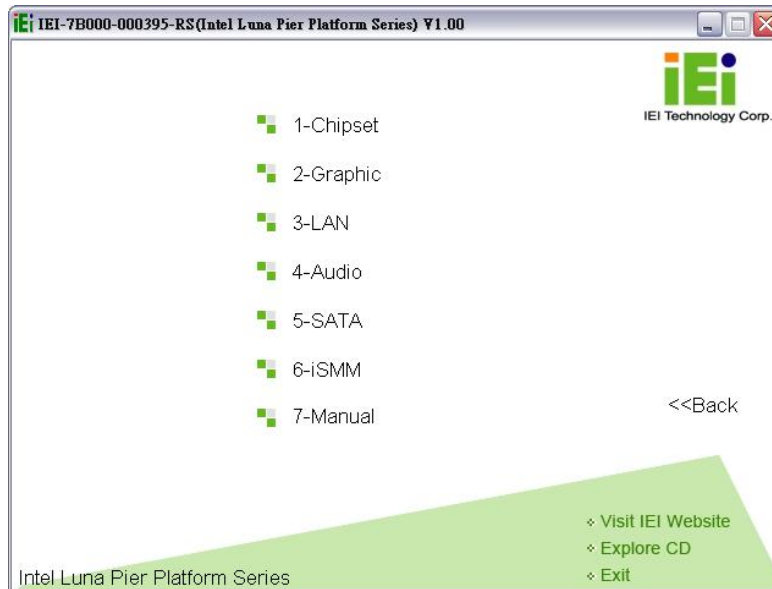
**Step 2:** The driver main menu appears (Figure 4-20).



**Figure 4-20: Introduction Screen**

**Step 3:** Click WAFER-PV-D5252/D4252/N4552.

**Step 4:** A new screen with a list of available drivers appears (Figure 4-21).



**Figure 4-21: Available Drivers**

**Step 5:** Install all of the necessary drivers in this menu.

Chapter

5

**BIOS**

---

## 5.1 Introduction

The BIOS is programmed onto the BIOS chip. The BIOS setup program allows changes to certain system settings. This chapter outlines the options that can be changed.

### 5.1.1 Starting Setup

The UEFI BIOS is activated when the computer is turned on. The setup program can be activated in one of two ways.

1. Press the **F2** key as soon as the system is turned on or
2. Press the **F2** key when the “**Press F2 to enter SETUP**” message appears on the screen.

If the message disappears before the **F2** key is pressed, restart the computer and try again.

### 5.1.2 Using Setup

Use the arrow keys to highlight items, press **ENTER** to select, use the PageUp and PageDown keys to change entries, press **F1** for help and press **ESC** to quit. Navigation keys are shown in.

Key	Function
Up arrow	Move to the item above
Down arrow	Move to the item below
Left arrow	Move to the item on the left hand side
Right arrow	Move to the item on the right hand side
+	Increase the numeric value or make changes
-	Decrease the numeric value or make changes
Page up	Move to the next page
Page down	Move to the previous page

Key	Function
Esc	Main Menu – Quit and do not save changes into CMOS Status Page Setup Menu and Option Page Setup Menu -- Exit current page and return to Main Menu
F1	General help, only for Status Page Setup Menu and Option Page Setup Menu
F9	Load optimized defaults
F10	Save changes and Exit BIOS

**Table 5-1: BIOS Navigation Keys**

### 5.1.3 Getting Help

When **F1** is pressed a small help window describing the appropriate keys to use and the possible selections for the highlighted item appears. To exit the Help Window press **Esc** or the **F1** key again.

### 5.1.4 Unable to Reboot After Configuration Changes

If the computer cannot boot after changes to the system configuration is made, CMOS defaults. Use the jumper described in Chapter 4.

### 5.1.5 BIOS Menu Bar

The **menu bar** on top of the BIOS screen has the following main items:

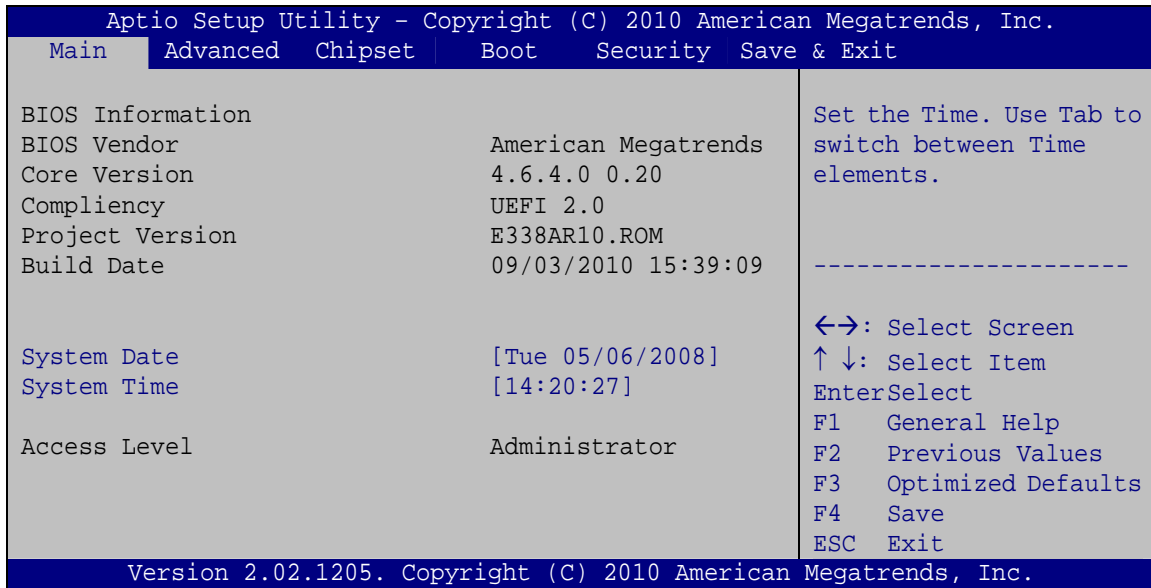
- Main – Changes the basic system configuration.
- Advanced – Changes the advanced system settings.
- Chipset – Changes the chipset settings.
- Boot – Changes the system boot configuration.
- Security – Sets User and Supervisor Passwords.
- Save & Exit – Selects exit options and loads default settings

The following sections completely describe the configuration options found in the menu items at the top of the BIOS screen and listed above.



## 5.2 Main

The **Main** BIOS menu (**BIOS Menu 1**) appears when the **BIOS Setup** program is entered. The **Main** menu gives an overview of the basic system information.



### BIOS Menu 1: Main

#### → BIOS Information

The **BIOS Information** lists a brief summary of the BIOS. The fields in **BIOS Information** cannot be changed. The items shown in the system overview include:

- **BIOS Vendor:** Installed BIOS vendor
- **Core Version:** Current BIOS version
- **Project Version:** the board version
- **Build Date:** Date the current BIOS version was made

The System Overview field also has two user configurable fields:

#### → System Date [xx/xx/xx]

Use the **System Date** option to set the system date. Manually enter the day, month and year.

## WAFER-PV-D5252/D4252/N4552 SBC

### → System Time [xx:xx:xx]

Use the **System Time** option to set the system time. Manually enter the hours, minutes and seconds.

## 5.3 Advanced

Use the **Advanced** menu (**BIOS Menu 2**) to configure the CPU and peripheral devices through the following sub-menus:



### WARNING!

Setting the wrong values in the sections below may cause the system to malfunction. Make sure that the settings made are compatible with the hardware.

```

Aptio Setup Utility - Copyright (C) 2010 American Megatrends, Inc.
Main  Advanced  Chipset  Boot  Security  Save & Exit

> ACPI Settings
> CPU Configuration
> IDE Configuration
> USB Configuration
> Super IO Configuration
> H/M Monitor
> Serial Port Console Redirection

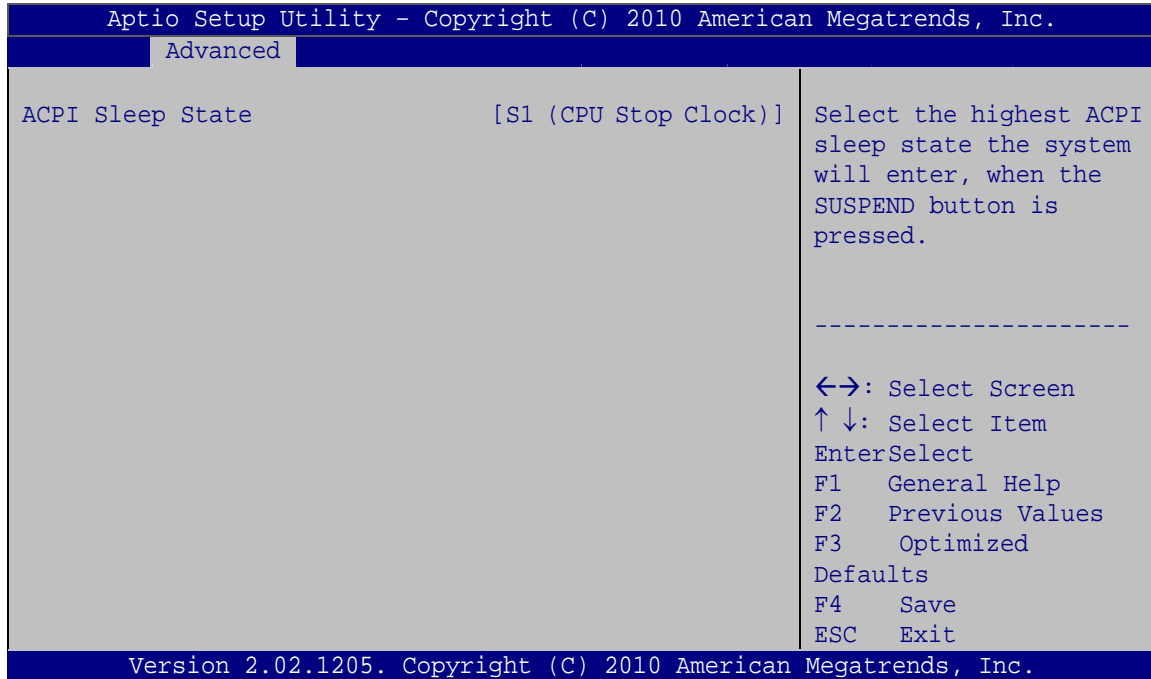
System ACPI Parameters
-----
<=>: Select Screen
↑ ↓: Select Item
Enter>Select
F1  General Help
F2  Previous Values
F3  Optimized
Defaults
F4  Save
ESC Exit

Version 2.02.1205. Copyright (C) 2010 American Megatrends, Inc.
    
```

### BIOS Menu 2: Advanced

#### 5.3.1 ACPI Configuration

The **ACPI Configuration** menu (**BIOS Menu 3**) configures the Advanced Configuration and Power Interface (ACPI) options.



### BIOS Menu 3: ACPI Configuration

#### → ACPI Sleep State [S1 (CPU Stop Clock)]

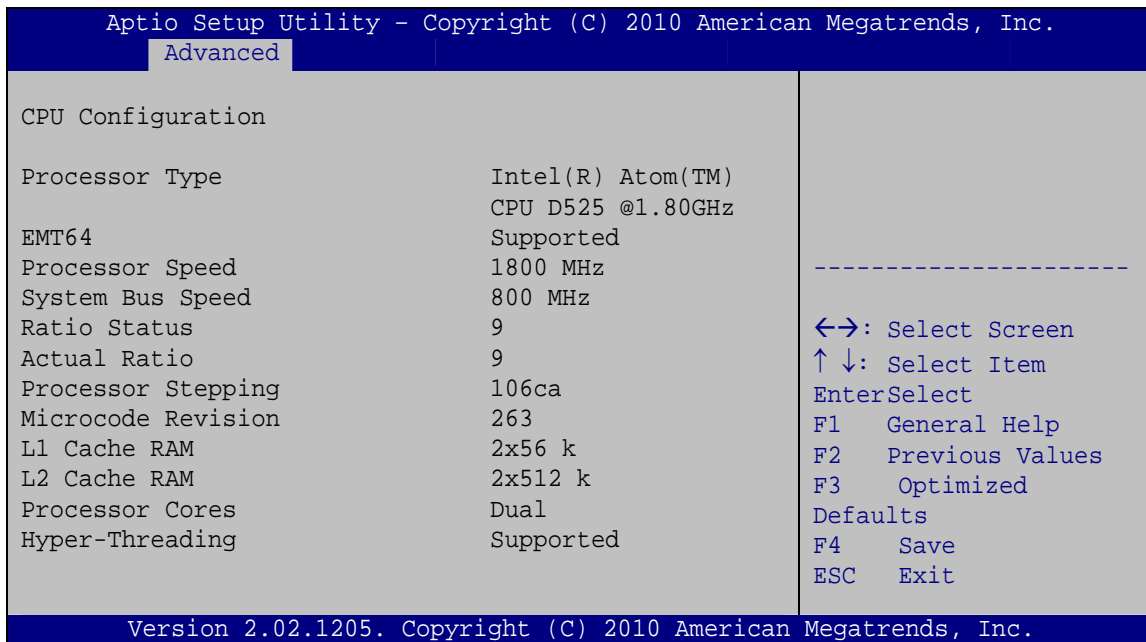
Use the **ACPI Sleep State** option to specify the sleep state the system enters when it is not being used.

- **S1 (CPU Stop Clock)** **DEFAULT** The system enters S1(POS) sleep state. The system appears off. The CPU is stopped; RAM is refreshed; the system is running in a low power mode.
- **S3 (Suspend to RAM)** The caches are flushed and the CPU is powered off. Power to the RAM is maintained. The computer returns slower to a working state, but more power is saved.

## WAFER-PV-D5252/D4252/N4552 SBC

### 5.3.2 CPU Configuration

Use the **CPU Configuration** menu (**BIOS Menu 4**) to view detailed CPU specifications and configure the CPU.



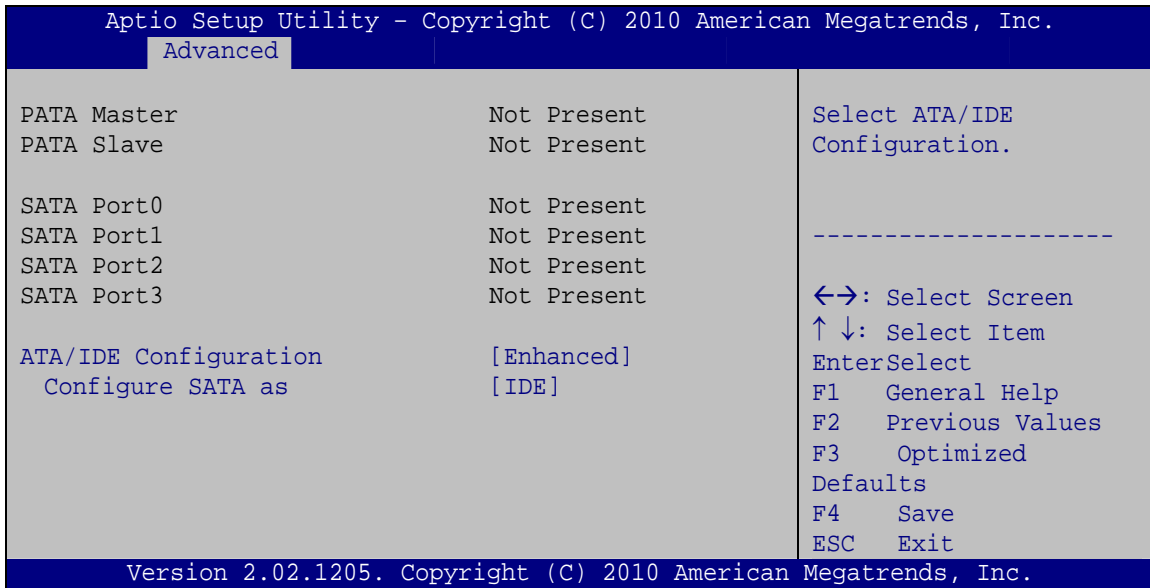
#### BIOS Menu 4: CPU Configuration

The CPU Configuration menu (**BIOS Menu 4**) lists the following CPU details:

- Processor Type: Lists the brand name of the CPU being used
- EMT64: Indicates if the EM64T is supported by the CPU.
- Processor Speed: Lists the CPU processing speed
- Actual Ratio: Lists the ratio of the frequency to the clock speed
- Processor Stepping: Lists the CPU processing stepping
- Microcode Revision: Lists the microcode revision
- L1 Cache RAM: Lists the CPU L1 cache size
- L2 Cache RAM: Lists the CPU L2 cache size
- Processor Cores: Lists the number of the processor core
- Hyper-Threading: Indicates if the Intel HT Technology is supported by the CPU.

### 5.3.3 IDE Configuration

Use the **IDE Configuration** menu (**BIOS Menu 5**) to change and/or set the configuration of the IDE devices installed in the system.



#### BIOS Menu 5: IDE Configuration

##### → ATA/IDE Configurations [Enhanced]

Use the **ATA/IDE Configurations** option to configure the ATA/IDE controller.

- **Disabled**                      Disables the on-board ATA/IDE controller.
- **Compatible**                      Configures the on-board ATA/IDE controller to be in compatible mode. In this mode, a SATA channel will replace one of the IDE channels. This mode supports up to 4 storage devices.
- **Enhanced**                      **DEFAULT**      Configures the on-board ATA/IDE controller to be in Enhanced mode. In this mode, IDE channels and SATA channels are separated. This mode supports up to 6 storage devices. Some legacy OS do not support this mode.



## WAFER-PV-D5252/D4252/N4552 SBC

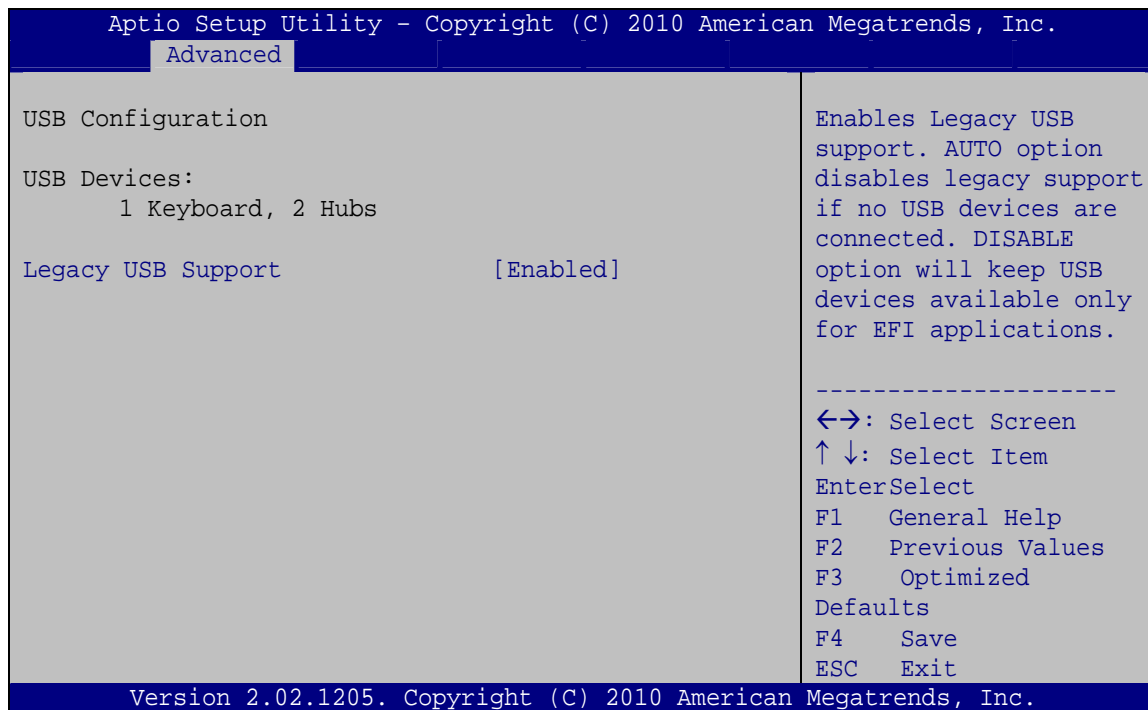
### → Configure SATA as [IDE]

Use the **Configure SATA as** option to configure SATA devices as normal IDE devices.

- **IDE**                      **DEFAULT**      Configures SATA devices as normal IDE device.
- **AHCI**                                           Configures SATA devices as normal AHCI device.

### 5.3.4 USB Configuration

Use the **USB Configuration** menu (**BIOS Menu 6**) to read USB configuration information and configure the USB settings.



#### BIOS Menu 6: USB Configuration

### → USB Devices

The **USB Devices Enabled** field lists the USB devices that are enabled on the system

### → Legacy USB Support [Enabled]

Use the **Legacy USB Support** BIOS option to enable USB mouse and USB keyboard support. Normally if this option is not enabled, any attached USB mouse or USB keyboard

does not become available until a USB compatible operating system is fully booted with all USB drivers loaded. When this option is enabled, any attached USB mouse or USB keyboard can control the system even when there is no USB driver loaded onto the system.

- ➔ **Enabled**      **DEFAULT**      Legacy USB support enabled
- ➔ **Disabled**                      Legacy USB support disabled
- ➔ **Auto**                              Legacy USB support disabled if no USB devices are connected

### 5.3.5 Super IO Configuration

Use the **Super IO Configuration** menu (**BIOS Menu 7**) to set or change the configurations for the FDD controllers, parallel ports and serial ports.

```

Aptio Setup Utility - Copyright (C) 2010 American Megatrends, Inc.
  Advanced
-----
Super IO Configuration
Super IO Chip                Finteck F81865
> Serial Port 0 Configuration
> Serial Port 1 Configuration
> Serial Port 2 Configuration
> Serial Port 3 Configuration
> Serial Port 4 Configuration
> Serial Port 5 Configuration
Set Parameters of Serial Port 0 (COMA)
-----
<=>: Select Screen
↑ ↓: Select Item
EnterSelect
F1  General Help
F2  Previous Values
F3  Optimized
Defaults
F4  Save
ESC Exit
Version 2.02.1205. Copyright (C) 2010 American Megatrends, Inc.

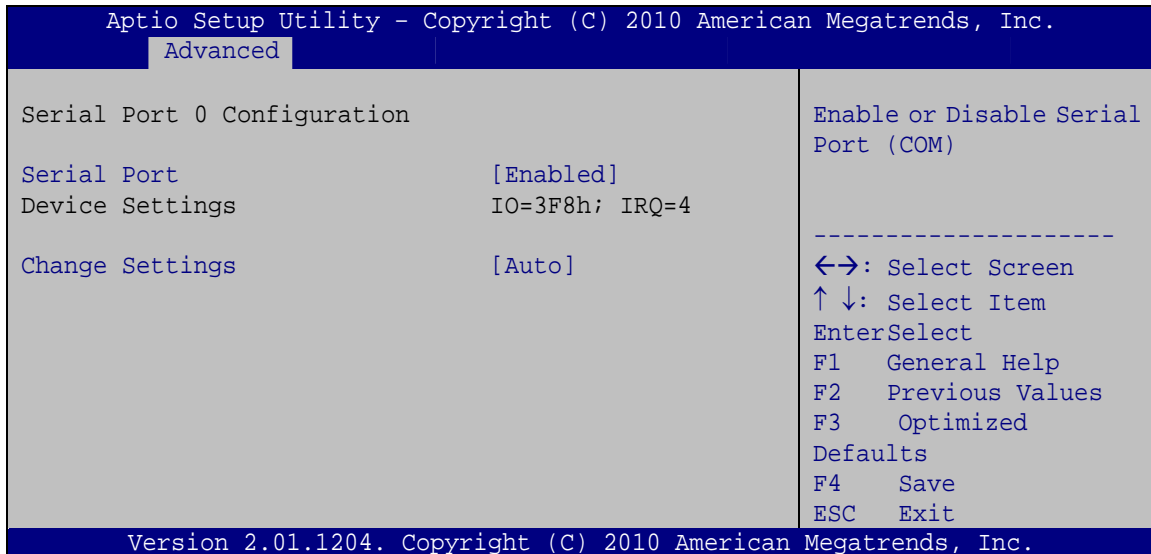
```

**BIOS Menu 7: Super IO Configuration**

## WAFER-PV-D5252/D4252/N4552 SBC

### 5.3.5.1 Serial Port n Configuration

Use the **Serial Port n Configuration** menu (**BIOS Menu 8**) to configure the serial port n.



#### BIOS Menu 8: Serial Port n Configuration Menu

#### 5.3.5.1.1 Serial Port 0 Configuration

##### → Serial Port [Enabled]

Use the **Serial Port** option to enable or disable the serial port.

- **Disabled**                      Disable the serial port
- **Enabled**            **DEFAULT**      Enable the serial port

##### → Change Settings [Auto]

Use the **Change Settings** option to change the serial port IO port address and interrupt address.

- **Auto**            **DEFAULT**      The serial port IO port address and interrupt address are automatically detected.
- **IO=3F8h;**  
**IRQ=4**                      Serial Port I/O port address is 3F8h and the interrupt address is IRQ4

- **IO=3F8h;**  
**IRQ=3, 4**                      Serial Port I/O port address is 3E8h and the interrupt address is IRQ3, 4
- **IO=2F8h;**  
**IRQ=3, 4**                      Serial Port I/O port address is 2F8h and the interrupt address is IRQ3, 4

### 5.3.5.1.2 Serial Port 1 Configuration

#### → Serial Port [Enabled]

Use the **Serial Port** option to enable or disable the serial port.

- **Disabled**                      Disable the serial port
- **Enabled**                      **DEFAULT**                      Enable the serial port

#### → Change Settings [Auto]

Use the **Change Settings** option to change the serial port IO port address and interrupt address.

- **Auto**                      **DEFAULT**                      The serial port IO port address and interrupt address are automatically detected.
- **IO=2F8h;**  
**IRQ=3**                      Serial Port I/O port address is 2F8h and the interrupt address is IRQ3
- **IO=3F8h;**  
**IRQ=3, 4**                      Serial Port I/O port address is 3F8h and the interrupt address is IRQ3, 4
- **IO=2F8h;**  
**IRQ=3, 4**                      Serial Port I/O port address is 2F8h and the interrupt address is IRQ3, 4

### 5.3.5.1.3 Serial Port 2 Configuration

#### → Serial Port [Enabled]

Use the **Serial Port** option to enable or disable the serial port.

- **Disabled**                      Disable the serial port

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→ **Enabled**      **DEFAULT**      Enable the serial port

### → **Change Settings [Auto]**

Use the **Change Settings** option to change the serial port IO port address and interrupt address.

→ **Auto**      **DEFAULT**      The serial port IO port address and interrupt address are automatically detected.

→ **IO=3E8h;**  
**IRQ=11**      Serial Port I/O port address is 3E8h and the interrupt address is IRQ11

→ **IO=3E8h;**  
**IRQ=10, 11**      Serial Port I/O port address is 3E8h and the interrupt address is IRQ10, 11

→ **IO=2E8h;**  
**IRQ=10, 11**      Serial Port I/O port address is 2E8h and the interrupt address is IRQ10, 11

### 5.3.5.1.4 Serial Port 3 Configuration

#### → **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

→ **Disabled**      Disable the serial port

→ **Enabled**      **DEFAULT**      Enable the serial port

#### → **Change Settings [Auto]**

Use the **Change Settings** option to change the serial port IO port address and interrupt address.

→ **Auto**      **DEFAULT**      The serial port IO port address and interrupt address are automatically detected.



- **IO=2E8h;**  
**IRQ=10**                      Serial Port I/O port address is 2E8h and the interrupt address is IRQ10
- **IO=3E8h;**  
**IRQ=10, 11**                Serial Port I/O port address is 3E8h and the interrupt address is IRQ10, 11
- **IO=2E8h;**  
**IRQ=10, 11**                Serial Port I/O port address is 2E8h and the interrupt address is IRQ10, 11

### 5.3.5.1.5 Serial Port 4 Configuration

#### → Serial Port [Enabled]

Use the **Serial Port** option to enable or disable the serial port.

- **Disabled**                      Disable the serial port
- **Enabled**                      **DEFAULT**                      Enable the serial port

#### → Change Settings [Auto]

Use the **Change Settings** option to change the serial port IO port address and interrupt address.

- **Auto**                      **DEFAULT**                      The serial port IO port address and interrupt address are automatically detected.
- **IO=2C0h;**  
**IRQ=11**                      Serial Port I/O port address is 2C0h and the interrupt address is IRQ11
- **IO=2C0h;**  
**IRQ=10, 11**                Serial Port I/O port address is 2C0h and the interrupt address is IRQ10, 11
- **IO=2C8h;**  
**IRQ=10, 11**                Serial Port I/O port address is 2C8h and the interrupt address is IRQ10, 11

### 5.3.5.1.6 Serial Port 5 Configuration

➔ **Serial Port [Enabled]**

Use the **Serial Port** option to enable or disable the serial port.

- ➔ **Disabled**                      Disable the serial port
- ➔ **Enabled      DEFAULT**      Enable the serial port

➔ **Change Settings [Auto]**

Use the **Change Settings** option to change the serial port IO port address and interrupt address.

- ➔ **Auto              DEFAULT**      The serial port IO port address and interrupt address are automatically detected.
- ➔ **IO=2C8h;  
IRQ=10**                      Serial Port I/O port address is 2C8h and the interrupt address is IRQ10
- ➔ **IO=2C0h;  
IRQ=10, 11**                      Serial Port I/O port address is 2C0h and the interrupt address is IRQ10, 11
- ➔ **IO=2C8h;  
IRQ=10, 11**                      Serial Port I/O port address is 2C8h and the interrupt address is IRQ10, 11

### 5.3.6 H/W Monitor

The H/W Monitor menu (**BIOS Menu 9**) shows the operating temperature, fan speeds and system voltages.

```

Aptio Setup Utility - Copyright (C) 2010 American Megatrends, Inc.
-----
Advanced
PC Health Status
CPU Temperature           :+43 C
SYS Temperature          :+49 C
CPU FAN Speed            :N/A
SYS FAN Speed            :N/A
VCC3C                    :+3.328 V
V_core                   :+1.152 V
V1_05V                   :+1.040 V
VCC3V                    :+3.312 V
Vcc                      :+5.003 V
VSB3V                    :+3.328 V
VBAT                     :+3.216 V
CPU Smart Fan control    [Auto Mode]
Temperature Bound 1     60
Temperature Bound 2     50
Temperature Bound 3     40
Temperature Bound 4     30
SYS Smart Fan control    [Auto Mode]
Temperature Bound 1     60
Temperature Bound 2     50
Temperature Bound 3     40
Temperature Bound 4     30
-----
<=>: Select Screen
↑ ↓: Select Item
EnterSelect
F1   General Help
F2   Previous Values
F3   Optimized
Defaults
F4   Save
ESC  Exit
Version 2.02.1205. Copyright (C) 2010 American Megatrends, Inc.

```

#### BIOS Menu 9: Hardware Health Configuration

##### → PC Health Status

The following system parameters and values are shown. The system parameters that are monitored are:

- System Temperatures:
  - CPU Temperature
  - System Temperature
- Fan Speeds:
  - CPU Fan Speed
  - System Fan Speed

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- Voltages:
  - VCC3V
  - V\_core
  - V1\_05V
  - VCC3V
  - Vcc
  - VSB3V
  - VBAT

### → CPU Smart Fan control [Auto Mode]

Use the **CPU Smart Fan control** option to configure the CPU fan.

#### → Auto Mode

The fan adjusts its speed using these settings:

Temperature Bound 1

Temperature Bound 2

Temperature Bound 3

Temperature Bound 4

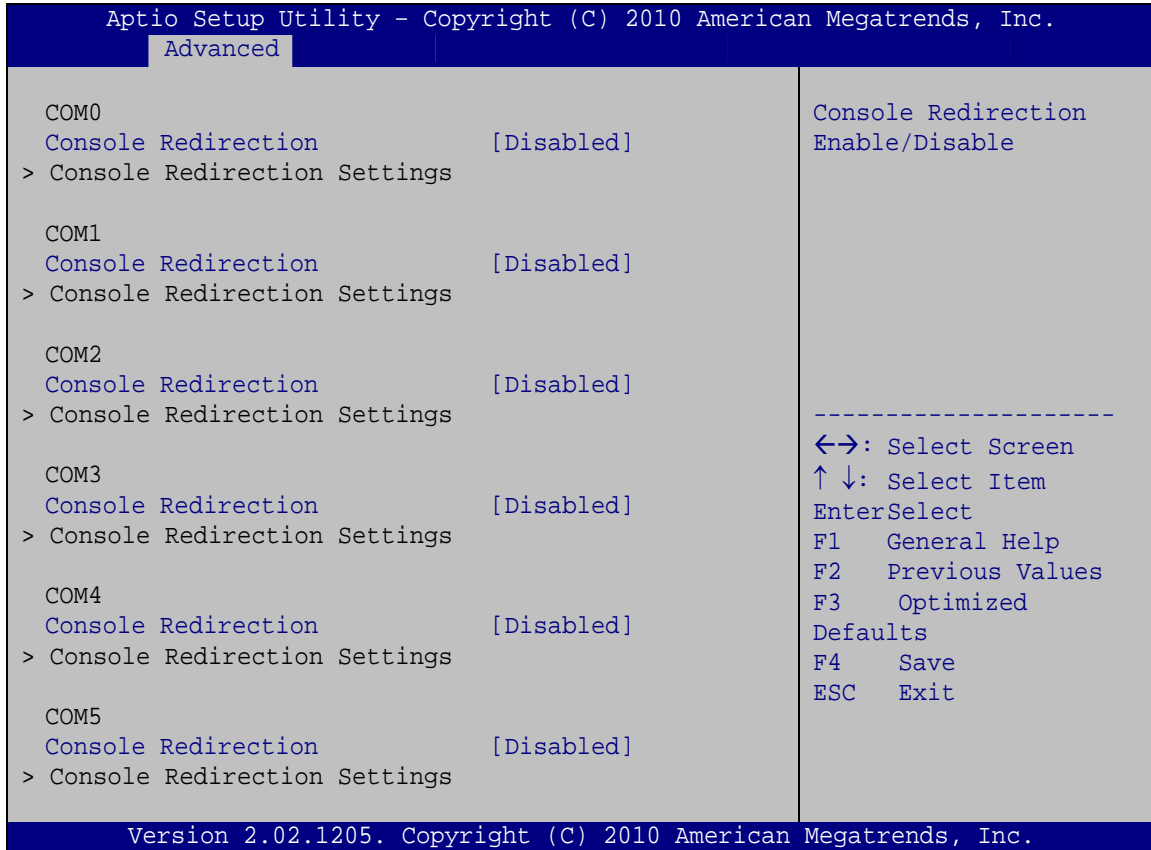
#### → Manual Mode

The fan spins at the speed set in:

Manual Duty Cycle Setting

### 5.3.7 Serial Port Console Redirection

The **Serial Port Console Redirection** menu (**BIOS Menu 10**) allows the console redirection options to be configured. Console redirection allows users to maintain a system remotely by re-directing keyboard input and text output through the serial port.



### BIOS Menu 10: Serial Port Console Redirection

#### → Console Redirection [Disabled]

Use **Console Redirection** option to enable or disable the console redirection function.

- **Disabled**    **DEFAULT**    Disabled the console redirection function
- **Enabled**    Enabled the console redirection function



## 5.4 Chipset

Use the **Chipset** menu (**BIOS Menu 11**) to access the Northbridge and Southbridge configuration menus



### WARNING!

Setting the wrong values for the Chipset BIOS selections in the Chipset BIOS menu may cause the system to malfunction.

```

Aptio Setup Utility - Copyright (C) 2010 American Megatrends, Inc.
Main   Advanced  Chipset  Boot   Security  Save & Exit
-----
> Host Bridge                               Host Bridge Parameters
> South Bridge
> Intel IGD SWSCI OpRegion

-----
<=>: Select Screen
↑ ↓: Select Item
EnterSelect
F1   General Help
F2   Previous Values
F3   Optimized
Defaults
F4   Save
ESC  Exit

Version 2.02.1205. Copyright (C) 2010 American Megatrends, Inc.
    
```

**BIOS Menu 11: Chipset**

## 5.4.1 Host Bridge

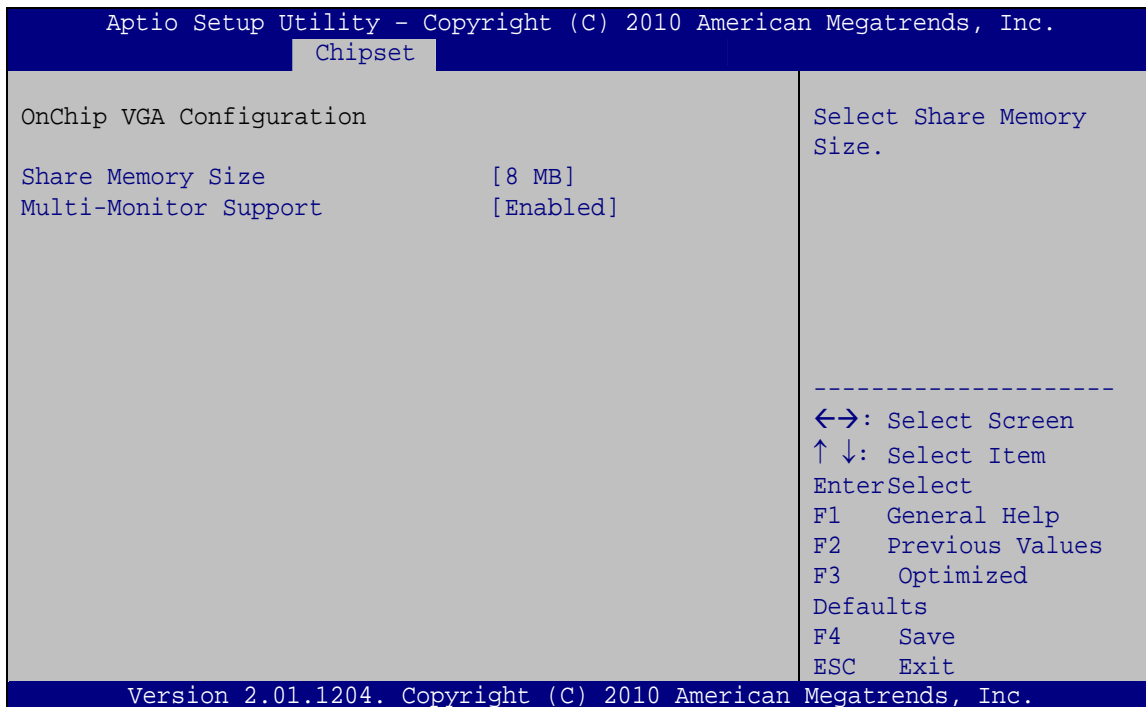
Use the **Host Bridge** menu (**BIOS Menu 12**) to configure the host bridge chipset.

```
Aptio Setup Utility - Copyright (C) 2010 American Megatrends, Inc.
Chipset
> OnChip VGA Configuration                               Config On Chip VGA
                                                         Settings
***** Memory Information *****
Memory Frequency                                     800 Mhz
Total Memory                                         2048 MB
DIMM#0                                               2048 MB
DIMM#1                                               Not present
-----
<=>: Select Screen
↑↓: Select Item
EnterSelect
F1   General Help
F2   Previous Values
F3   Optimized
Defaults
F4   Save
ESC  Exit
Version 2.02.1205. Copyright (C) 2010 American Megatrends, Inc.
```

### BIOS Menu 12:Host Bridge Configuration

### 5.4.1.1 OnChip VGA Configuration

Use the **OnChip VGA Configuration** menu (**BIOS Menu 12**) to configure the OnChip VGA.



#### BIOS Menu 13: OnChip VGA Configuration

##### → Share Memory Size [8 MB]

Use the **Share Memory Size** option to set the amount of system memory allocated to the integrated graphics processor when the system boots. The system memory allocated can then only be used as graphics memory, and is no longer available to applications or the operating system. Configuration options are listed below:

- Disabled
- 1 MB
- 8 MB                      **Default**

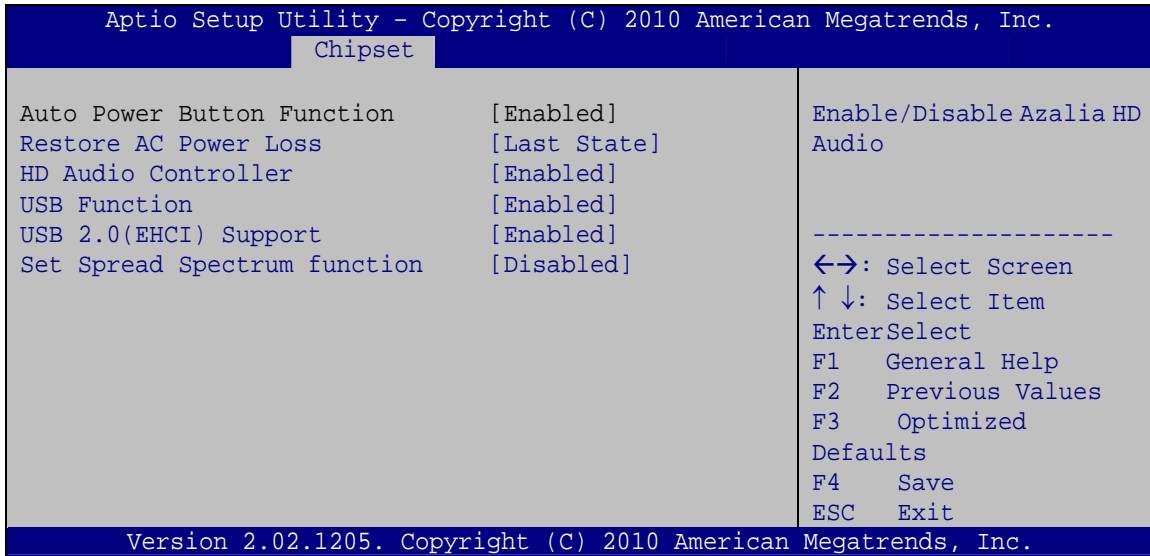
##### → Multi-Monitor Support [Enabled]

Use **Multi-Monitor Support** option to enable or disable the multi-monitor function.

- ➔ **Disabled** Disabled the multi-monitor function
- ➔ **Enabled**     **DEFAULT**     Enabled the multi-monitor function

## 5.4.2 South Bridge

Use the **South Bridge** menu (**BIOS Menu 14**) to configure the Southbridge chipset.



### BIOS Menu 14:Southbridge Chipset Configuration

#### ➔ Restore on AC Power Loss [Last State]

Use the **Restore on AC Power Loss** BIOS option to specify what state the system returns to if there is a sudden loss of power to the system.

- ➔ **Power Off** The system remains turned off
- ➔ **Power On** The system turns on
- ➔ **Last State**     **DEFAULT**     The system returns to its previous state. If it was on, it turns itself on. If it was off, it remains off.

#### ➔ HD Audio Controller [Enabled]

The **HD Audio Controller** option enables or disables the HD audio controller.

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→ **Enabled**      **DEFAULT**      The onboard HD audio controller automatically detected and enabled

→ **Disabled**      The onboard HD audio controller is disabled

### → **USB Function [Enabled]**

Use the **USB Function** BIOS option to enable or disable USB function support.

→ **Disabled**      USB function support disabled

→ **Enabled**      **DEFAULT**      USB function support enabled

### → **USB 2.0 (EHCI) Support [Enabled]**

Use the **USB 2.0 (EHCI) Support** BIOS option to enable or disable the USB 2.0 controller.

→ **Enabled**      **DEFAULT**      USB 2.0 controller enabled

→ **Disabled**      USB 2.0 controller disabled

### → **Set Spread Spectrum Function [Disabled]**

The **Set Spread Spectrum Function** option can help to improve CPU EMI issues.

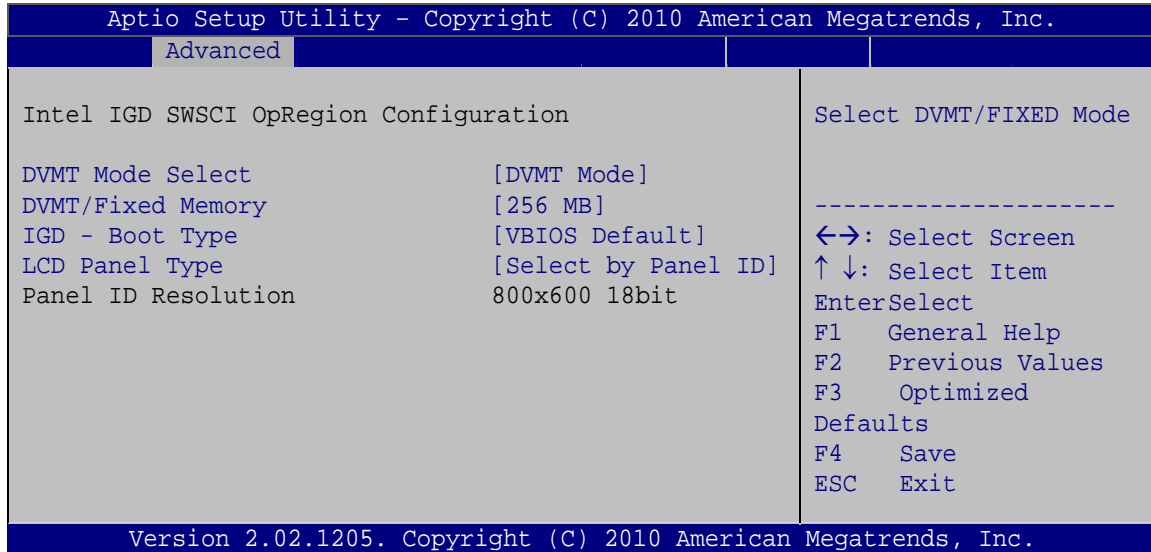
→ **Disabled**      **DEFAULT**      The spread spectrum function is disabled

→ **Enabled**      The spread spectrum function is enabled

## 5.4.3 Intel IGD SWSCI OpRegion

Use the **Intel IGD SWSCI OpRegion** menu to configure the video device connected to the system.





### BIOS Menu 15: Intel IGD SWSCI OpRegion

#### → DVMT Mode Select [DVMT Mode]

Use the **DVMT Mode Select** option to select the Intel Dynamic Video Memory Technology (DVMT) operating mode.

- **Fixed Mode**                      A fixed portion of graphics memory is reserved as graphics memory.
- **DVMT Mode    DEFAULT**      Graphics memory is dynamically allocated according to the system and graphics needs.

#### → DVMT/FIXED Memory [256 MB]

Use the **DVMT/FIXED Memory** option to specify the maximum amount of memory that can be allocated as graphics memory. Configuration options are listed below.

- 128 MB
- 256 MB                      **Default**
- Maximum

#### → IGD - Boot Type [VBIOS Default]

Use the **IGD - Boot Type** option to select the display device used by the system when it boots. Configuration options are listed below.

## WAFER-PV-D5252/D4252/N4552 SBC

- VBIOS Default      **DEFAULT**
- CRT
- LFP
- CRT + LFP

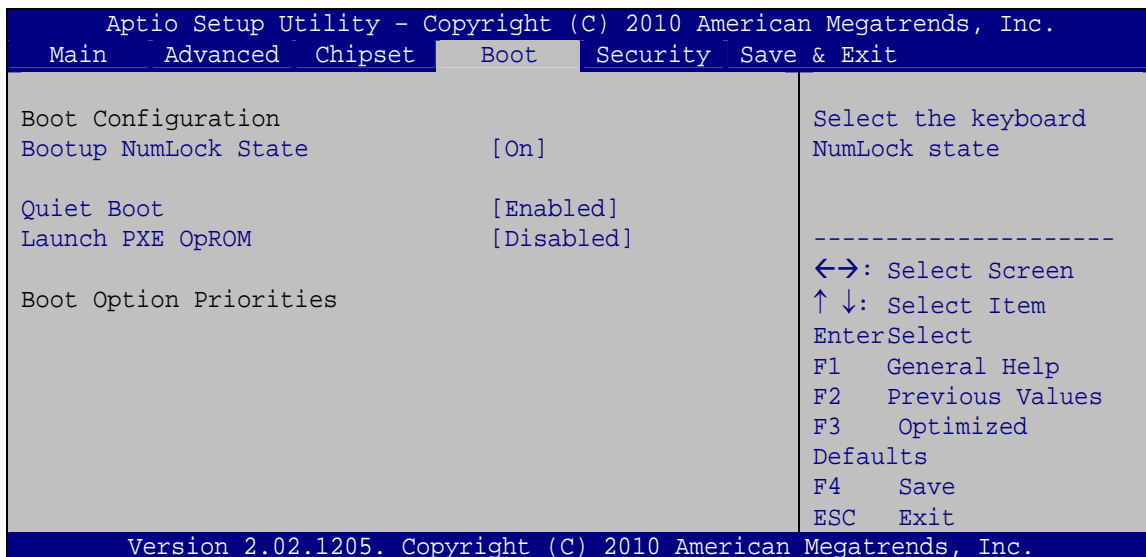
### → LCD Panel Type [VBIOS Default]

Use the **LCD Panel Type** option to select the type of flat panel connected to the system. Configuration options are listed below.

- Select by Panel ID    **DEFAULT**
- 640x480 18bit
- 800x480 18bit
- 800x600 18bit
- 1024x768 18bit
- 1280x1024 18bit
- 1366x768 18bit
- 1280x800 18bit
- 1280x600 18bit

## 5.5 Boot

Use the **Boot** menu (**BIOS Menu 16**) to configure system boot options.



**BIOS Menu 16: Boot**

→ **Bootup NumLock State [On]**

Use the **Bootup NumLock State** BIOS option to specify if the number lock setting must be modified during boot up.

→ **On**                      **DEFAULT**                      Allows the Number Lock on the keyboard to be enabled automatically when the computer system boots up. This allows the immediate use of the 10-key numeric keypad located on the right side of the keyboard. To confirm this, the Number Lock LED light on the keyboard is lit.

→ **Off**    Does not enable the keyboard Number Lock automatically. To use the 10-keys on the keyboard, press the Number Lock key located on the upper left-hand corner of the 10-key pad. The Number Lock LED on the keyboard lights up when the Number Lock is engaged.

→ **Quiet Boot [Enabled]**

Use the **Quiet Boot** BIOS option to select the screen display when the system boots.

→ **Disabled**    Normal POST messages displayed

→ **Enabled**                      **DEFAULT**                      OEM Logo displayed instead of POST messages

→ **Launch PXE OpROM [Disabled]**

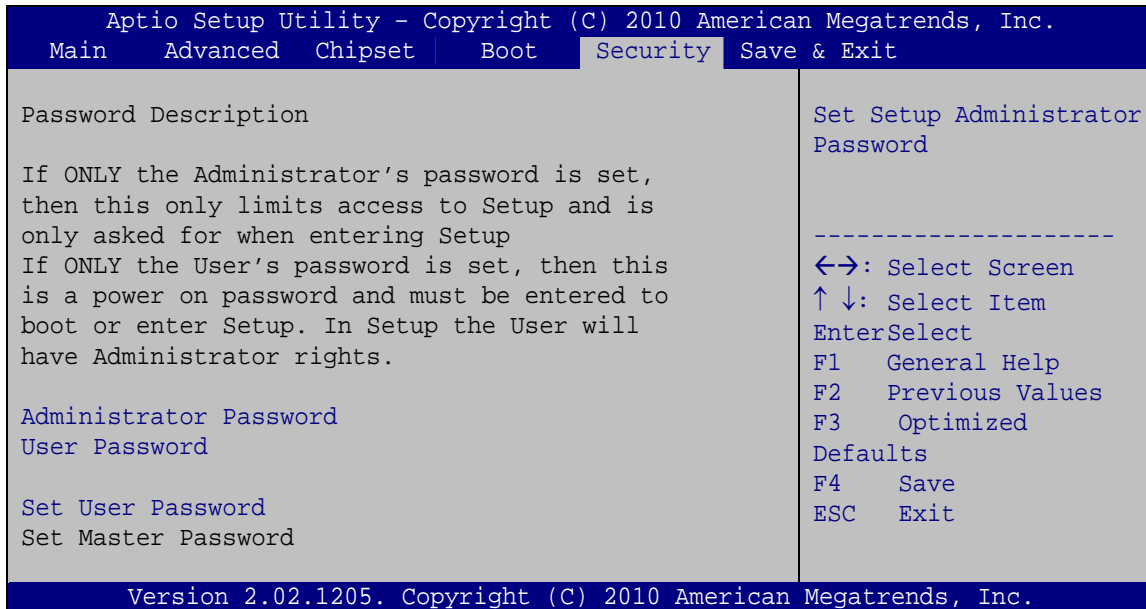
Use the **Launch PXE OpROM** option to enable or disable boot option for legacy network devices.

→ **Disabled**                      **DEFAULT**                      Ignore all PXE Option ROMs

→ **Enabled**    Load PXE Option ROMs.

## 5.6 Security

Use the **Security** menu (**BIOS Menu 17**) to set system and user passwords.



### BIOS Menu 17: Security

#### → Administrator Password

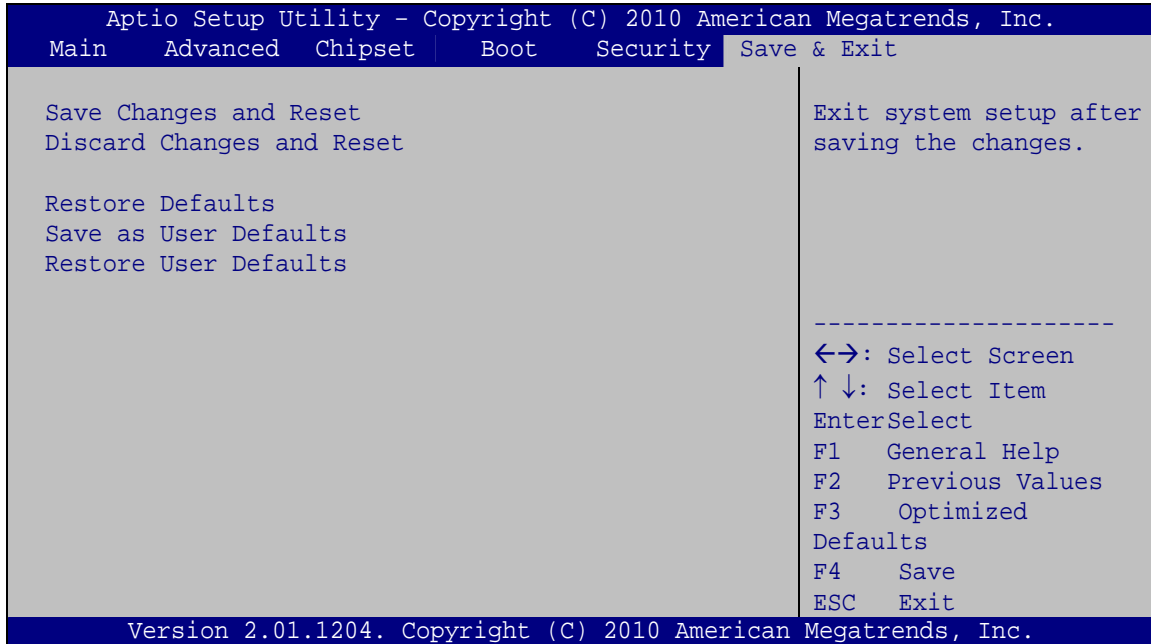
Use the **Administrator Password** to set or change a administrator password.

#### → User Password

Use the **User Password** to set or change a user password.

## 5.7 Exit

Use the **Exit** menu (**BIOS Menu 18**) to load default BIOS values, optimal failsafe values and to save configuration changes.


**BIOS Menu 18:Exit**
**→ Save Changes and Reset**

Use the **Save Changes and Reset** option to save the changes made to the BIOS options and to exit the BIOS configuration setup program.

**→ Discard Changes and Reset**

Use the **Discard Changes and Reset** option to exit the system without saving the changes made to the BIOS configuration setup program.

**→ Restore Defaults**

Use the **Restore Defaults** option to load the optimal default values for each of the parameters on the Setup menus. **F3 key can be used for this operation.**

**→ Save as User Defaults**

Use the **Save as User Defaults** option to save the changes done so far as user defaults.

**→ Restore User Defaults**

Use the **Restore User Defaults** option to restore the user defaults to all the setup options.



Appendix

A

# BIOS Options

---

Below is a list of BIOS configuration options in the BIOS chapter.

<b>BIOS Information</b> .....	<b>66</b>
<b>System Date [xx/xx/xx]</b> .....	<b>66</b>
<b>System Time [xx:xx:xx]</b> .....	<b>67</b>
<b>ACPI Sleep State [S1 (CPU Stop Clock)]</b> .....	<b>68</b>
<b>ATA/IDE Configurations [Enhanced]</b> .....	<b>70</b>
<b>Configure SATA as [IDE]</b> .....	<b>71</b>
<b>USB Devices</b> .....	<b>71</b>
<b>Legacy USB Support [Enabled]</b> .....	<b>71</b>
<b>Serial Port [Enabled]</b> .....	<b>73</b>
<b>Change Settings [Auto]</b> .....	<b>73</b>
<b>Serial Port [Enabled]</b> .....	<b>74</b>
<b>Change Settings [Auto]</b> .....	<b>74</b>
<b>Serial Port [Enabled]</b> .....	<b>74</b>
<b>Change Settings [Auto]</b> .....	<b>75</b>
<b>Serial Port [Enabled]</b> .....	<b>75</b>
<b>Change Settings [Auto]</b> .....	<b>75</b>
<b>Serial Port [Enabled]</b> .....	<b>76</b>
<b>Change Settings [Auto]</b> .....	<b>76</b>
<b>Serial Port [Enabled]</b> .....	<b>77</b>
<b>Change Settings [Auto]</b> .....	<b>77</b>
<b>PC Health Status</b> .....	<b>78</b>
<b>CPU Smart Fan control [Auto Mode]</b> .....	<b>79</b>
<b>Console Redirection [Disabled]</b> .....	<b>80</b>
<b>Share Memory Size [8 MB]</b> .....	<b>83</b>
<b>Multi-Monitor Support [Enabled]</b> .....	<b>83</b>
<b>Restore on AC Power Loss [Last State]</b> .....	<b>84</b>
<b>HD Audio Controller [Enabled]</b> .....	<b>84</b>
<b>USB Function [Enabled]</b> .....	<b>85</b>
<b>USB 2.0 (EHCI) Support [Enabled]</b> .....	<b>85</b>
<b>Set Spread Spectrum Function [Disabled]</b> .....	<b>85</b>
<b>DVMT Mode Select [DVMT Mode]</b> .....	<b>86</b>
<b>DVMT/FIXED Memory [256 MB]</b> .....	<b>86</b>
<b>IGD - Boot Type [VBIOS Default]</b> .....	<b>86</b>

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LCD Panel Type [VBIOS Default].....	87
Bootup NumLock State [On].....	88
Quiet Boot [Enabled] .....	88
Launch PXE OpROM [Disabled].....	88
Administrator Password .....	89
User Password .....	89
Save Changes and Reset .....	90
Discard Changes and Reset .....	90
Restore Defaults .....	90
Save as User Defaults .....	90
Restore User Defaults .....	90

Appendix

**B**

# One Key Recovery

---

## B.1 One Key Recovery Introduction

The IEI one key recovery is an easy-to-use front end for the Norton Ghost system backup and recovery tool. The one key recovery provides quick and easy shortcuts for creating a backup and reverting to that backup or for reverting to the factory default settings.

The IEI One Key Recovery tool menu is shown below.

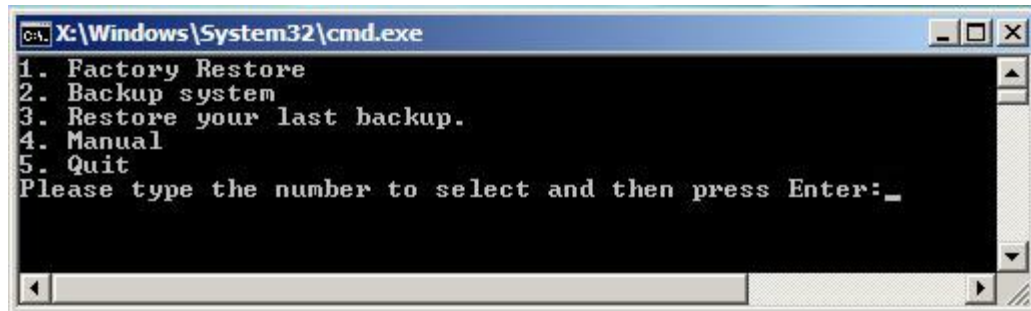


Figure B-1: IEI One Key Recovery Tool Menu

Prior to using the IEI One Key Recovery tool (as shown in **Figure B-1**) to backup or restore Windows system, five setup procedures are required.

1. Hardware and BIOS setup (see **Section B.2.1**)
2. Create partitions (see **Section B.2.2**)
3. Install operating system, drivers and system applications (see **Section B.2.3**)
4. Build-up recovery partition (see **Section B.2.4**)
5. Create factory default image (see **Section B.2.5**)

After completing the five initial setup procedures as described above, users can access the recovery tool by pressing <F3> while booting up the system. The detailed information of each function is described in **Section B.4**.



### NOTE:

The initial setup procedures for Linux system are described in **Section B.3**.

---

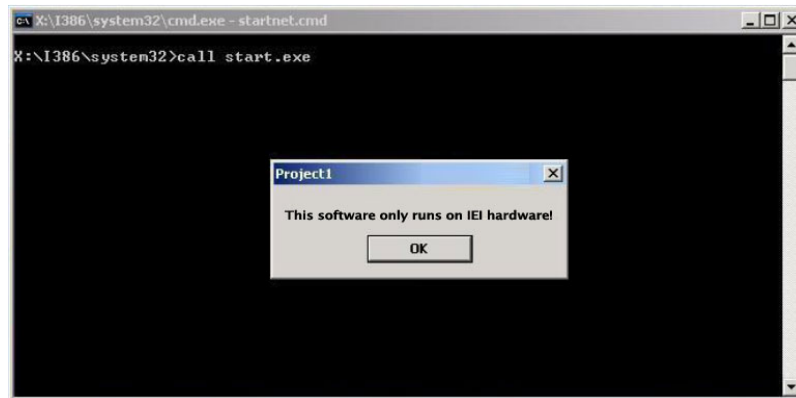


### B.1.1 System Requirement



**NOTE:**

The recovery CD can only be used with IEI products. The software will fail to run and a warning message will appear when used on non-IEI hardware.



To create the system backup, the main storage device must be split into two partitions (three partitions for Linux). The first partition will be for the operating system, while the second partition will be invisible to the operating system and contain the backup made by the one key recovery software.

The partition created for recovery images must be big enough to contain both the factory default image and the user backup image. The size must be calculated before creating the partitions. Please take the following table as a reference when calculating the size of the partition.

	OS	OS Image after Ghost	Compression Ratio
<b>Windows® 7</b>	7 GB	5 GB	70%
<b>Windows® XPE</b>	776 MB	560 MB	70%
<b>Windows® CE 6.0</b>	36 MB	28 MB	77%

**NOTE:**

Specialized tools are required to change the partition size if the operating system is already installed.

**B.1.2 Supported Operating System**

The recovery CD is compatible with both Microsoft Windows and Linux operating system (OS). The supported OS versions are listed below.

- Microsoft Windows
  - Windows XP (Service Pack 2 or 3 required)
  - Windows Vista
  - Windows 7
  - Windows CE 5.0
  - Windows CE 6.0
  - Windows XP Embedded
- Linux
  - Fedora Core 12 (Constantine)
  - Fedora Core 11 (Leonidas)
  - Fedora Core 10 (Cambridge)
  - Fedora Core 8 (Werewolf)
  - Fedora Core 7 (Moonshine)
  - RedHat RHEL-5.4
  - RedHat 9 (Ghirke)
  - Ubuntu 8.10 (Intrepid)
  - Ubuntu 7.10 (Gutsy)
  - Ubuntu 6.10 (Edgy)
  - Debian 5.0 (Lenny)
  - Debian 4.0 (Etch)
  - SuSe 11.2
  - SuSe 10.3

**NOTE:**

Installing unsupported OS versions may cause the recovery tool to fail.

## B.2 Setup Procedure for Windows

Prior to using the recovery tool to backup or restore Windows system, a few setup procedures are required.

**Step 1:** Hardware and BIOS setup (see **Section B.2.1**)

**Step 2:** Create partitions (see **Section B.2.2**)

**Step 3:** Install operating system, drivers and system applications (see **Section B.2.3**)

**Step 4:** Build-up recovery partition (see **Section B.2.4**)

**Step 5:** Create factory default image (see **Section B.2.5**)

The detailed descriptions are described in the following sections.

**NOTE:**

The setup procedures described below are for Microsoft Windows operating system users. For Linux system, most setup procedures are the same with Microsoft Windows except for several steps which is described in **Section B.3**.

### B.2.1 Hardware and BIOS Setup

**Step 1:** Make sure the system is powered off and unplugged.

**Step 2:** Install a hard drive or SSD in the system. An unformatted and unpartitioned disk is recommended.

**Step 3:** Connect an optical disk drive to the system and insert the recovery CD.

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- Step 4:** Turn on the system.
- Step 5:** Press the <DELETE> key as soon as the system is turned on to enter the BIOS.
- Step 6:** Select the connected optical disk drive as the 1<sup>st</sup> boot device. (**Boot** → **Boot Device Priority** → **1<sup>st</sup> Boot Device**).
- Step 7:** Save changes and restart the computer. Continue to the next section for instructions on partitioning the internal storage.

### B.2.2 Create Partitions

To create the system backup, the main storage device must be split into two partitions (three partitions for Linux). The first partition will be for the operating system, while the second partition will be invisible to the operating system and contain the backup made by the one key recovery software.

- Step 1:** Put the recovery CD in the optical drive of the system.
- Step 2:** **Boot the system from recovery CD.** When prompted, press any key to boot from the recovery CD. It will take a while to launch the recovery tool. Please be patient!

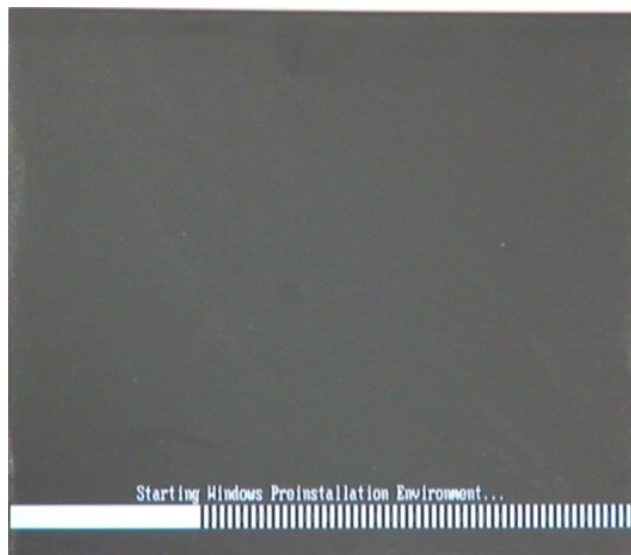


Figure B-2: Launching the Recovery Tool

**Step 3:** The recovery tool setup menu is shown as below.

```
C:\ X:\I386\system32\cmd.exe
1.Ghost Execution
2.System Configuration For Windows
3.System Configuration For Linux
4.Exit
5.CMD
Type the number to print text._
```

**Figure B-3: Recovery Tool Setup Menu**

**Step 4:** Press <5> then <Enter>.

```
C:\ X:\I386\system32\cmd.exe
1.Ghost Execution
2.System Configuration For Windows
3.System Configuration For Linux
4.Exit
5.CMD
Type the number to print text. 5
```

**Figure B-4: Command Mode**

**Step 5:** The command prompt window appears. Type the following commands (marked in red) to create two partitions. One is for the OS installation; the other is for saving recovery files and images which will be an invisible partition.

(Press <Enter> after entering each line below)

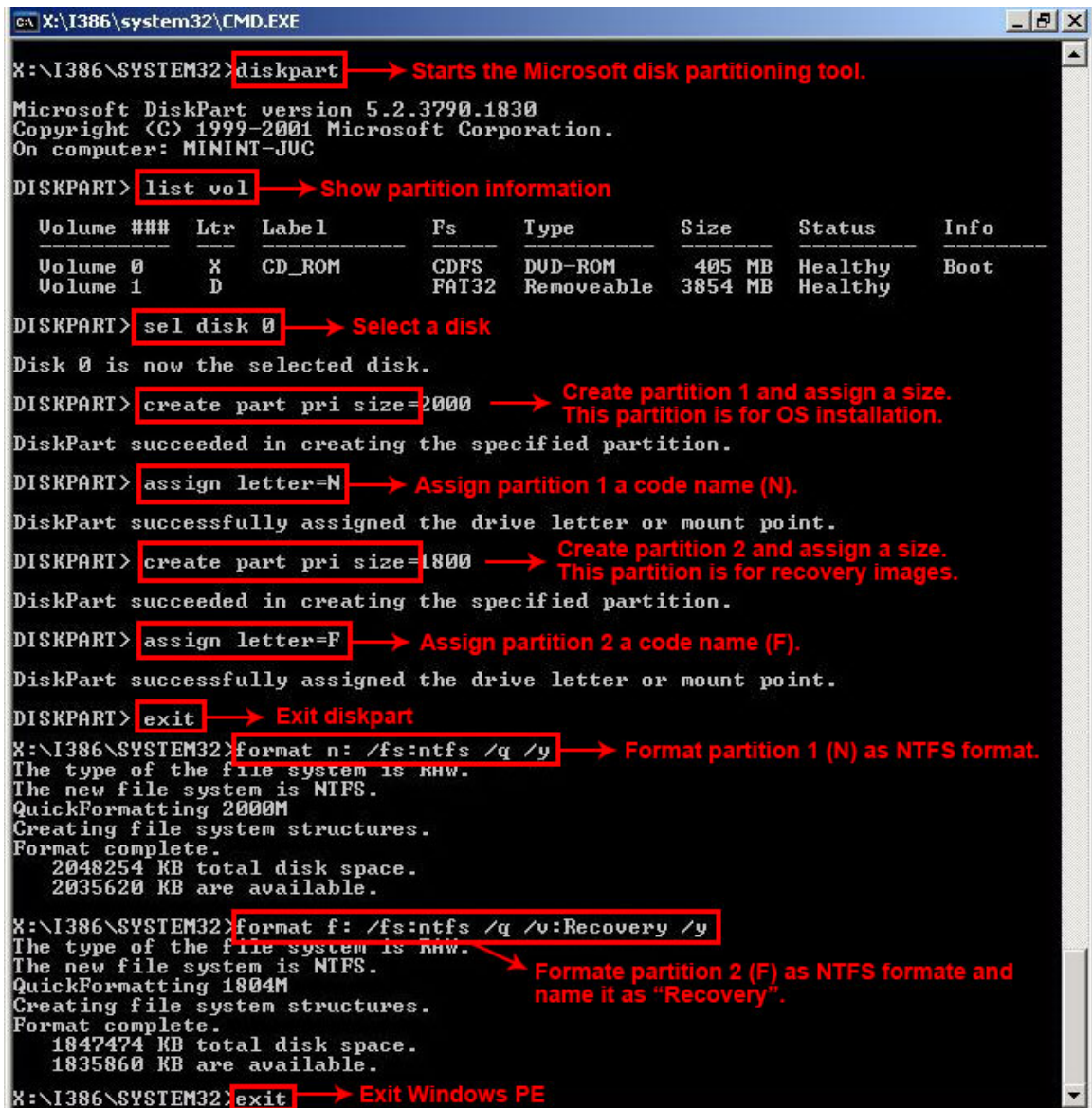
```
system32>diskpart
DISKPART>list vol
DISKPART>sel disk 0
DISKPART>create part pri size= ____
DISKPART>assign letter=N
DISKPART>create part pri size= ____
DISKPART>assign letter=F
DISKPART>exit
system32>format N: /fs:ntfs /q /y
```



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system32>format F: /fs:ntfs /q /v:Recovery /y

system32>exit



```
X:\I386\SYSTEM32\CMD.EXE
X:\I386\SYSTEM32>diskpart → Starts the Microsoft disk partitioning tool.
Microsoft DiskPart version 5.2.3790.1830
Copyright (C) 1999-2001 Microsoft Corporation.
On computer: MININT-JUC
DISKPART> list vol → Show partition information
  Volume ###  Ltr  Label          Fs          Type          Size         Status       Info
  -----  -
  Volume 0             X   CD_ROM        GDFS        DUD-ROM       405 MB       Healthy      Boot
  Volume 1             D
DISKPART> sel disk 0 → Select a disk
Disk 0 is now the selected disk.
DISKPART> create part pri size=2000 → Create partition 1 and assign a size.
                                     This partition is for OS installation.
DiskPart succeeded in creating the specified partition.
DISKPART> assign letter=N → Assign partition 1 a code name (N).
DiskPart successfully assigned the drive letter or mount point.
DISKPART> create part pri size=1800 → Create partition 2 and assign a size.
                                     This partition is for recovery images.
DiskPart succeeded in creating the specified partition.
DISKPART> assign letter=F → Assign partition 2 a code name (F).
DiskPart successfully assigned the drive letter or mount point.
DISKPART> exit → Exit diskpart
X:\I386\SYSTEM32>format n: /fs:ntfs /q /y → Format partition 1 (N) as NTFS format.
The type of the file system is ntfs.
The new file system is NTFS.
QuickFormatting 2000M
Creating file system structures.
Format complete.
2048254 KB total disk space.
2035620 KB are available.
X:\I386\SYSTEM32>format f: /fs:ntfs /q /v:Recovery /y → Formate partition 2 (F) as NTFS formate and
                                                         name it as "Recovery".
The type of the file system is ntfs.
The new file system is NTFS.
QuickFormatting 1804M
Creating file system structures.
Format complete.
1847474 KB total disk space.
1835860 KB are available.
X:\I386\SYSTEM32>exit → Exit Windows PE
```

Figure B-5: Partition Creation Commands


**NOTE:**

Use the following commands to check if the partitions were created successfully.

```

X:\I386\SYSTEM32>diskpart
Microsoft DiskPart version 5.2.3790.1830
Copyright (C) 1999-2001 Microsoft Corporation.
On computer: MININT-JUC

DISKPART> sel disk 0
Disk 0 is now the selected disk.

DISKPART> list part

   Partition ###   Type              Size              Offset
-----
   Partition 1     Primary           2000 MB           32 KB
   Partition 2     Primary           1804 MB          2000 MB

DISKPART> exit
  
```

**Step 6:** Press any key to exit the recovery tool and automatically reboot the system.

Please continue to the following procedure: Build-up Recovery Partition.

### B.2.3 Install Operating System, Drivers and Applications

Install the operating system onto the unlabelled partition. The partition labeled as "Recovery" is for use by the system recovery tool and should not be used for installing the operating system or any applications.


**NOTE:**

The operating system installation program may offer to reformat the chosen partition. DO NOT format the partition again. The partition has already been formatted and is ready for installing the new operating system.

To install the operating system, insert the operating system installation CD into the optical drive. Restart the computer and follow the installation instructions.

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### B.2.4 Build-up Recovery Partition

- Step 1:** Put the recover CD in the optical drive.
- Step 2:** Start the system.
- Step 3:** **Boot the system from recovery CD.** When prompted, press any key to boot from the recovery CD. It will take a while to launch the recovery tool. Please be patient!

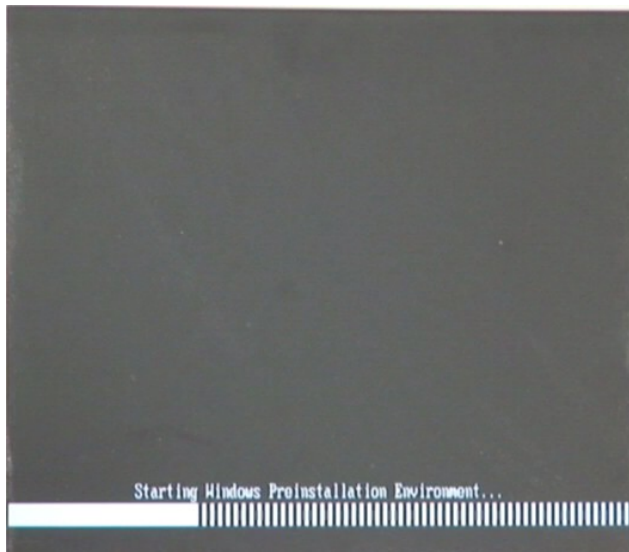


Figure B-6: Launching the Recovery Tool

- Step 4:** When the recovery tool setup menu appears, press <2> then <Enter>.

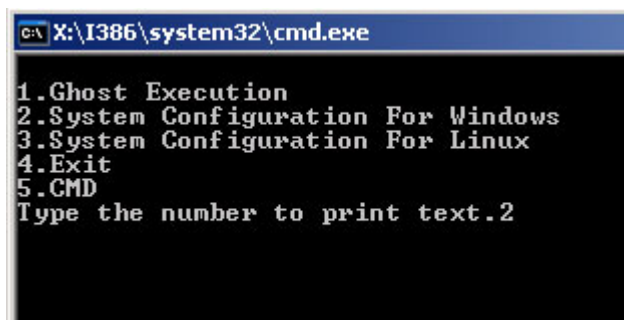
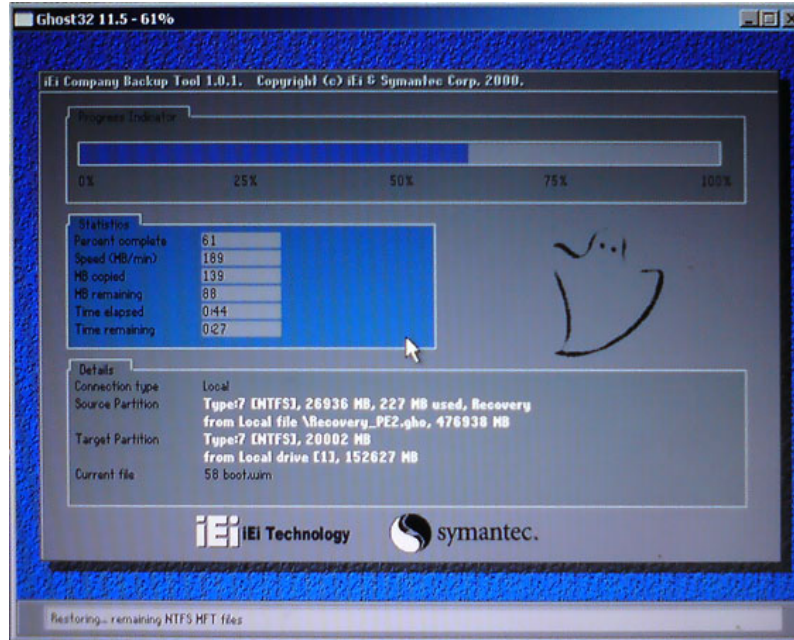


Figure B-7: System Configuration for Windows

- Step 5:** The Symantec Ghost window appears and starts configuring the system to build-up a recovery partition. In this process, the partition which is created for

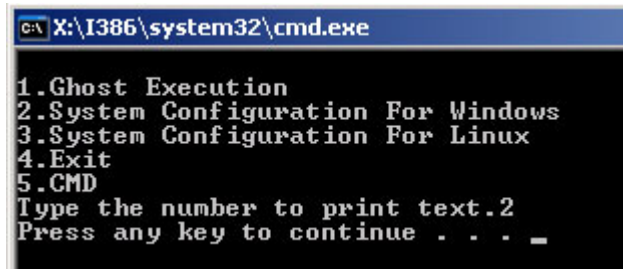


recovery files in **Section B.2.2** is hidden and the recovery tool is saved in this partition.



**Figure B-8: Build-up Recovery Partition**

**Step 6:** After completing the system configuration, press any key in the following window to reboot the system.



**Figure B-9: Press any key to continue**

**Step 7:** Eject the recovery CD.

## B.2.5 Create Factory Default Image



### NOTE:

Before creating the factory default image, please configure the system to a factory default environment, including driver and application installations.

To create a factory default image, please follow the steps below.

**Step 1:** Turn on the system. When the following screen displays (**Figure B-10**), press the <F3> key to access the recovery tool. The message will display for 10 seconds, please press F3 before the system boots into the operating system.

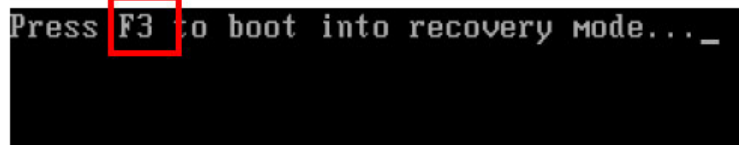


Figure B-10: Press F3 to Boot into Recovery Mode

**Step 2:** The recovery tool menu appears. Type <4> and press <Enter>. (**Figure B-11**)

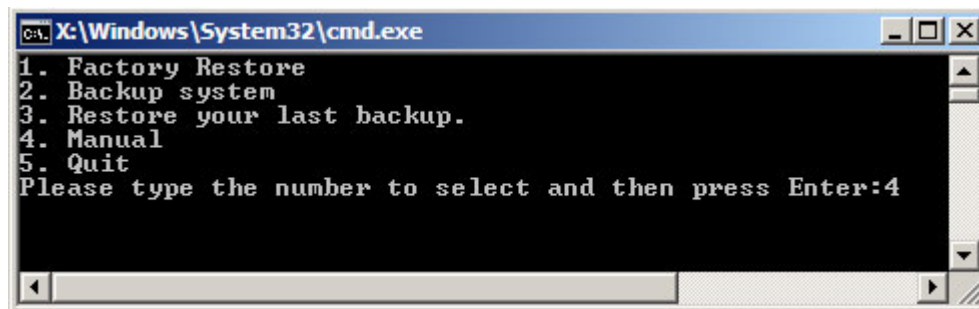


Figure B-11: Recovery Tool Menu

**Step 3:** The About Symantec Ghost window appears. Click **OK** button to continue.



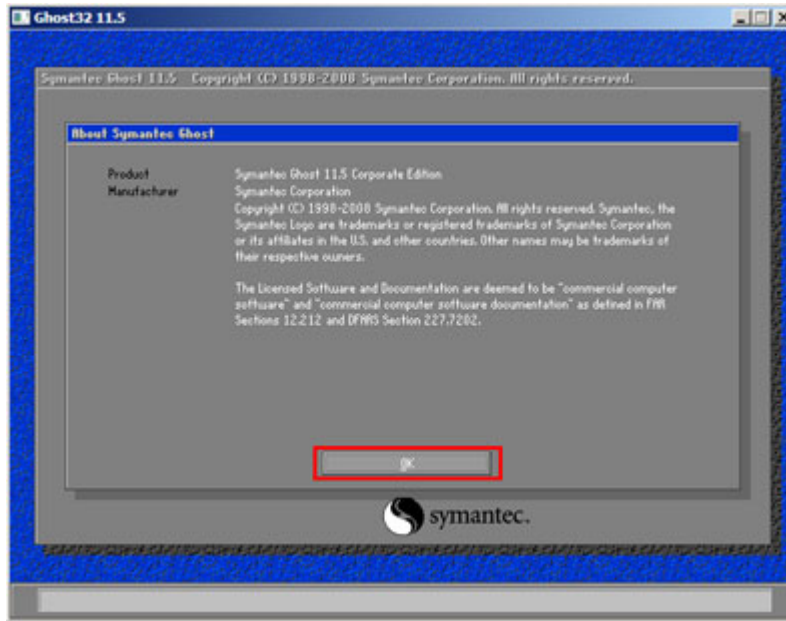


Figure B-12: About Symantec Ghost Window

**Step 4:** Use mouse to navigate to the option shown below (Figure B-13).

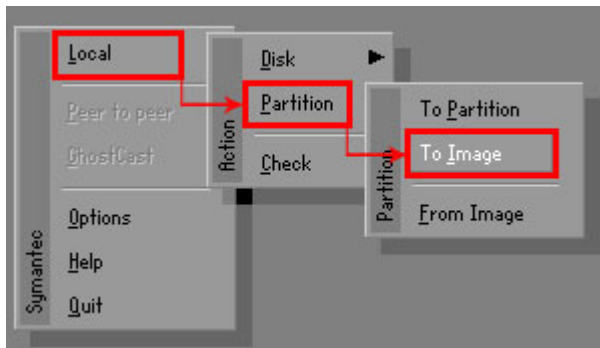


Figure B-13: Symantec Ghost Path

**Step 5:** Select the local source drive (Drive 1) as shown in Figure B-14. Then click OK.

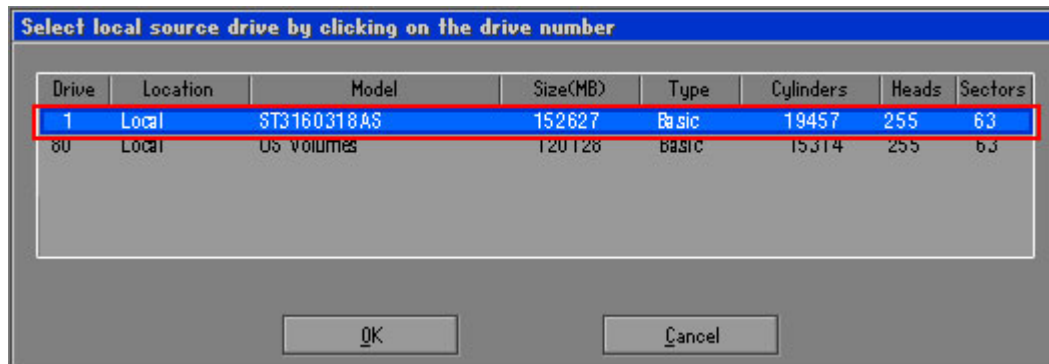


Figure B-14: Select a Local Source Drive

**Step 6:** Select a source partition (Part 1) from basic drive as shown in **Figure B-15**. Then click OK.

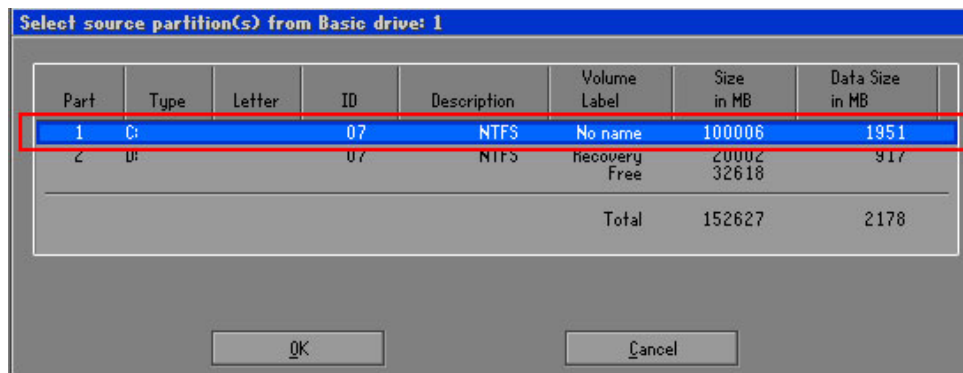


Figure B-15: Select a Source Partition from Basic Drive

**Step 7:** Select **1.2: [Recovery] NTFS drive** and enter a file name called **iei** (**Figure B-16**). Click **Save**. The factory default image will then be saved in the selected recovery drive and named **IEI.GHO**.



**WARNING:**

The file name of the factory default image must be **iei.GHO**.

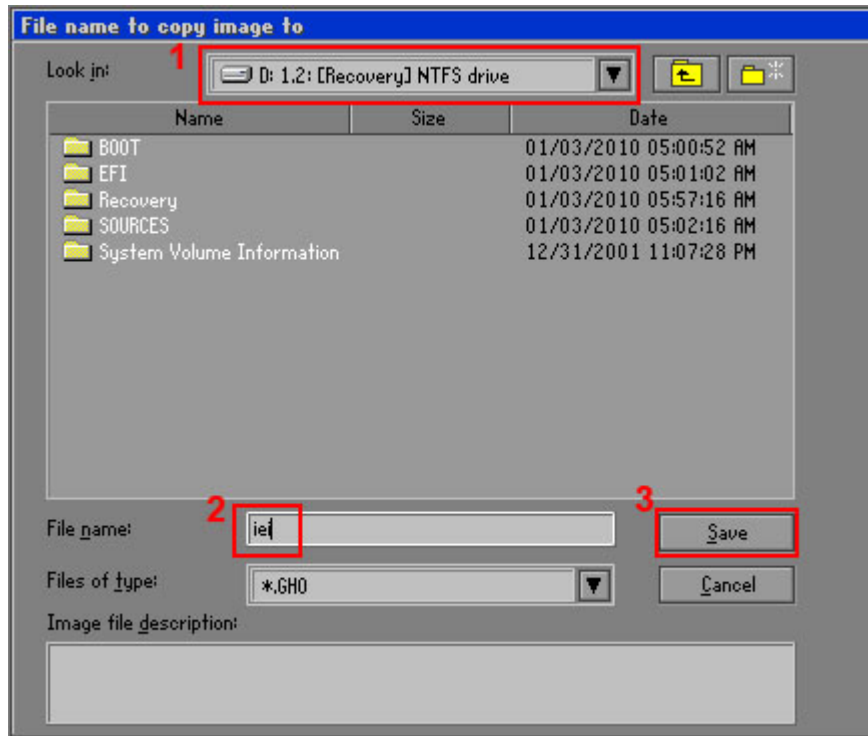


Figure B-16: File Name to Copy Image to

**Step 8:** When the Compress Image screen in **Figure B-17** prompts, click **High** to make the image file smaller.

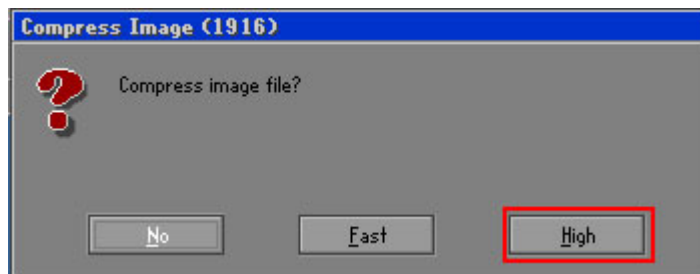
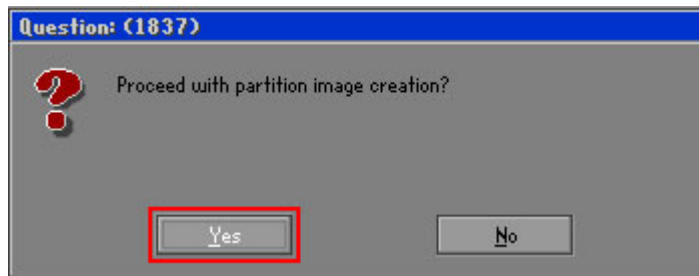


Figure B-17: Compress Image

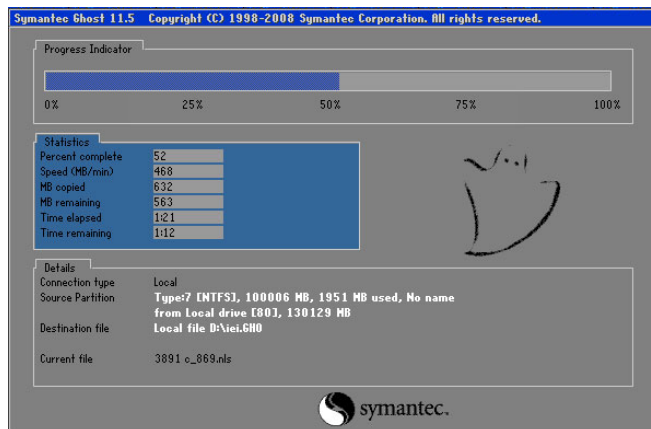
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**Step 9:** The Proceed with partition image creation window appears, click **Yes** to continue.



**Figure B-18: Image Creation Confirmation**

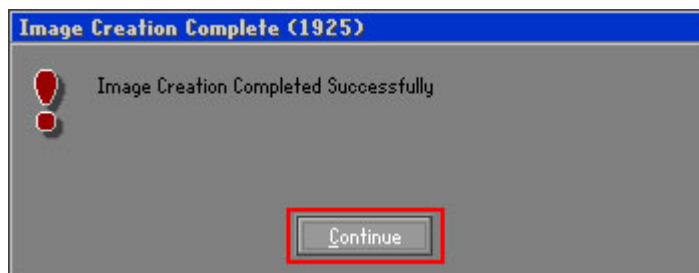
**Step 10:** The Symantec Ghost starts to create the factory default image (**Figure B-19**).



**Figure B-19: Image Creation Process**

**Step 11:** When the image creation completes, a screen prompts as shown in **Figure B-20**.

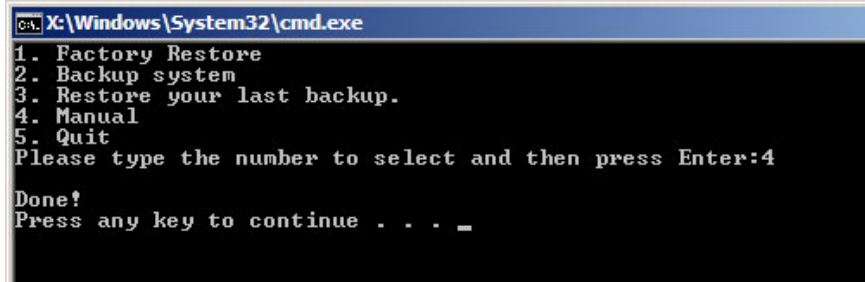
Click **Continue** and close the Ghost window to exit the program.



**Figure B-20: Image Creation Complete**



**Step 12:** The recovery tool main menu window is shown as below. Press any key to reboot the system.



```

C:\Windows\System32\cmd.exe
1. Factory Restore
2. Backup system
3. Restore your last backup.
4. Manual
5. Quit
Please type the number to select and then press Enter:4
Done!
Press any key to continue . . . _
  
```

Figure B-21: Press Any Key to Continue

## B.3 Setup Procedure for Linux

The initial setup procedures for Linux system are mostly the same with the procedure for Microsoft Windows. Please follow the steps below to setup recovery tool for Linux OS.

**Step 1: Hardware and BIOS setup.** Refer to **Section B.2.1**.

**Step 2: Install Linux operating system.** Make sure to install GRUB (v0.97 or earlier) MBR type and Ext3 partition type. Leave enough space on the hard drive to create the recover partition later.



### NOTE:

If the Linux OS is not installed with GRUB (v0.97 or earlier) and Ext3, the Symantec Ghost may not function properly.

---

While installing Linux OS, please create two partitions:

- Partition 1: /
- Partition 2: **SWAP**



**NOTE:**

Please reserve enough space for partition 3 for saving recovery images.

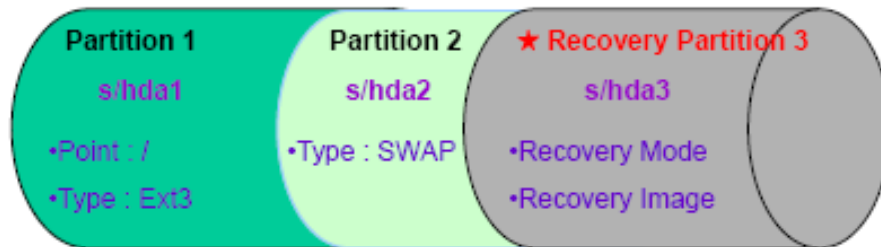


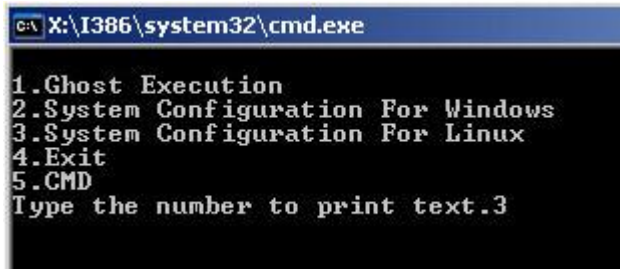
Figure B-22: Partitions for Linux

**Step 3:** Create a recovery partition. Insert the recovery CD into the optical disk drive.

Follow **Step 1 ~ Step 3** described in **Section B.2.2**. Then type the following commands (marked in red) to create a partition for recovery images.

```
system32>diskpart
DISKPART>list vol
DISKPART>sel disk 0
DISKPART>create part pri size= ____
DISKPART>assign letter=N
DISKPART>exit
system32>format N: /fs:ntfs /q /v:Recovery /y
system32>exit
```

**Step 4:** Build-up recovery partition. Press any key to boot from the recovery CD. It will take a while to launch the recovery tool. Please be patient. When the recovery tool setup menu appears, type <3> and press <Enter> (**Figure B-23**). The Symantec Ghost window appears and starts configuring the system to build-up a recovery partition. After completing the system configuration, press any key to reboot the system. Eject the recovery CD.



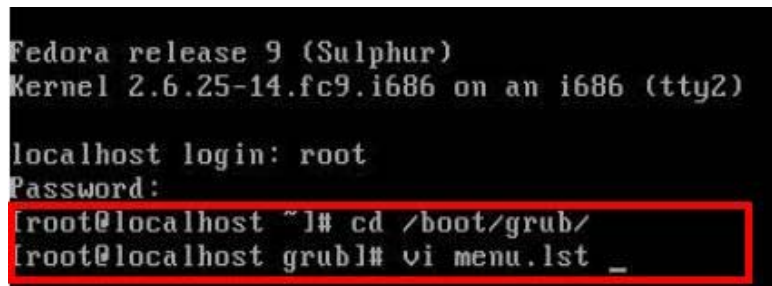
```
C:\X:\I386\system32\cmd.exe
1.Ghost Execution
2.System Configuration For Windows
3.System Configuration For Linux
4.Exit
5.CMD
Type the number to print text.3
```

**Figure B-23: System Configuration for Linux**

**Step 5:** Access the recovery tool main menu by modifying the “menu.lst”. To first access the recovery tool main menu, the menu.lst must be modified. In Linux system, enter Administrator (root). When prompt appears, type:

**cd /boot/grub**

**vi menu.lst**



```
Fedora release 9 (Sulphur)
Kernel 2.6.25-14.fc9.i686 on an i686 (tty2)

localhost login: root
Password:
[root@localhost ~]# cd /boot/grub/
[root@localhost grub]# vi menu.lst _
```

**Figure B-24: Access menu.lst in Linux (Text Mode)**

**Step 6:** Modify the menu.lst as shown below.

```
#boot=/dev/sda
default=0
timeout=10 ← Modify timeout=10
splashimage=(hd0,0)/grub/splash.xpm.gz
hiddenmenu
title Fedora (2.6.25-14.fc9.i686)
    root (hd0,0)
    kernel /vmlinuz-2.6.25-14.fc9.i686 ro root=UUID=10f1acd
ac38b5c78910 rhgb quiet
    initrd /initrd-2.6.25-14.fc9.i686.img

title Recovery Partition
root (hd0,2)
makeactive ← Type command
chainloader +1
```

- Type command:  
**title Recovery Partition**  
**root (hd0,2)**  
**makeactive**  
**chainloader +1**

**Step 7:** The recovery tool menu appears. (Figure B-25)

```
1. Factory Restore
2. Backup system
3. Restore your last backup.
4. Manual
5. Quit
Please type the number to select and then press Enter:
```

Figure B-25: Recovery Tool Menu

**Step 8:** Create a factory default image. Follow **Step 2 ~ Step 12** described in **Section B.2.5** to create a factory default image.

## B.4 Recovery Tool Functions

After completing the initial setup procedures as described above, users can access the recovery tool by pressing **<F3>** while booting up the system. The main menu of the recovery tool is shown below.

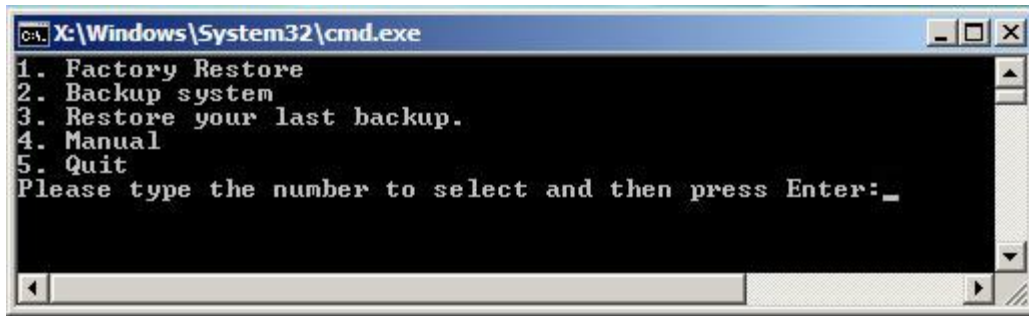


Figure B-26: Recovery Tool Main Menu

The recovery tool has several functions including:

1. **Factory Restore:** Restore the factory default image (iei.GHO) created in Section B.2.5.
2. **Backup system:** Create a system backup image (iei\_user.GHO) which will be saved in the hidden partition.
3. **Restore your last backup:** Restore the last system backup image
4. **Manual:** Enter the Symantec Ghost window to configure manually.
5. **Quit:** Exit the recovery tool and restart the system.



**WARNING:**

Please do not turn off the system power during the process of system recovery or backup.



**WARNING:**

All data in the system will be deleted during the system recovery. Please backup the system files before restoring the system (either Factory Restore or Restore Backup).



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### B.4.1 Factory Restore

To restore the factory default image, please follow the steps below.

**Step 1:** Type <1> and press <Enter> in the main menu.

**Step 2:** The Symantec Ghost window appears and starts to restore the factory default. A factory default image called **iei.GHO** is created in the hidden Recovery partition.

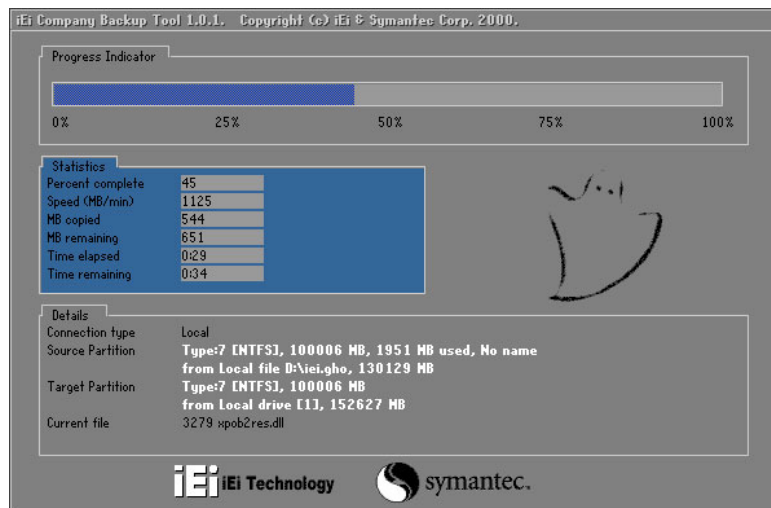


Figure B-27: Restore Factory Default

**Step 3:** The screen is shown as in **Figure B-28** when completed. Press any key to reboot the system.

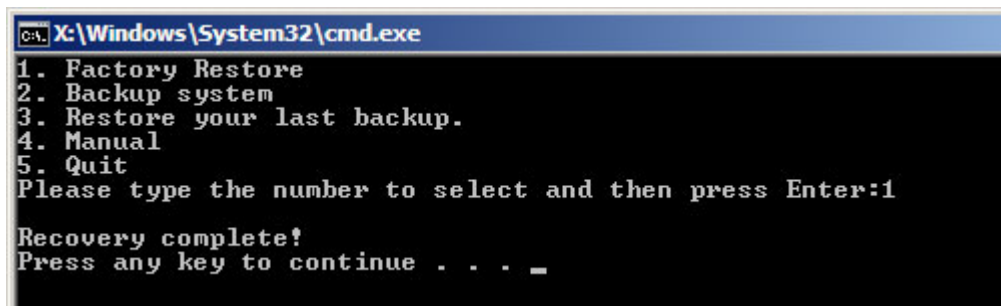


Figure B-28: Recovery Complete Window

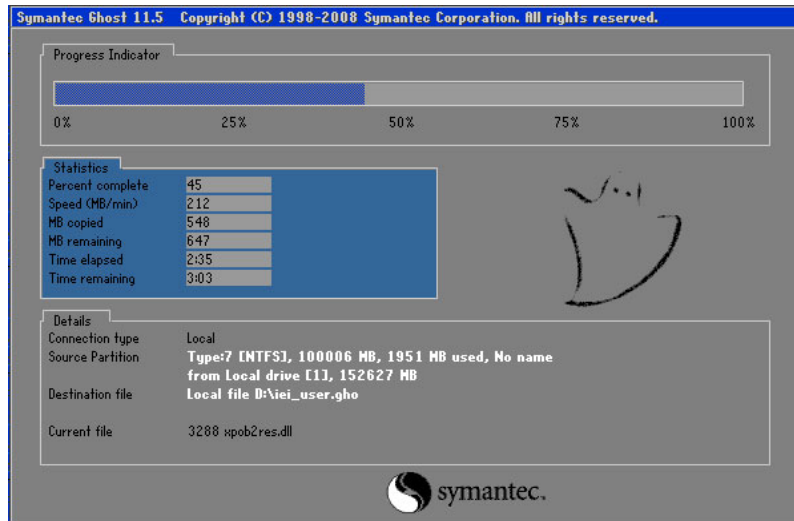


## B.4.2 Backup System

To backup the system, please follow the steps below.

**Step 1:** Type <2> and press <Enter> in the main menu.

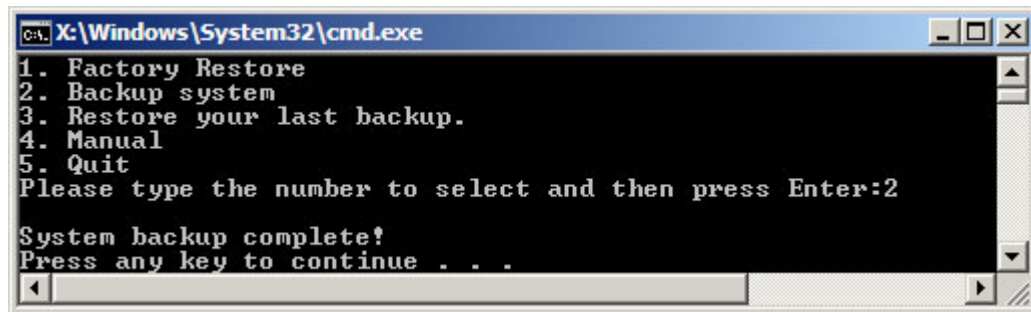
**Step 2:** The Symantec Ghost window appears and starts to backup the system. A backup image called **iei\_user.GHO** is created in the hidden Recovery partition.



**Figure B-29: Backup System**

**Step 3:** The screen is shown as in **Figure B-30** when system backup is completed.

Press any key to reboot the system.



**Figure B-30: System Backup Complete Window**

### B.4.3 Restore Your Last Backup

To restore the last system backup, please follow the steps below.

**Step 1:** Type <3> and press <Enter> in the main menu.

**Step 2:** The Symantec Ghost window appears and starts to restore the last backup image (iei\_user.GHO).

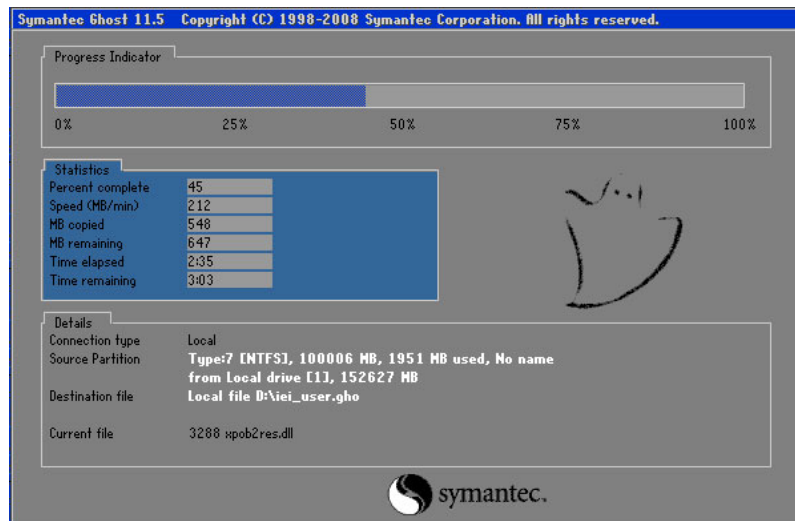


Figure B-31: Restore Backup

**Step 3:** The screen is shown as in **Figure B-32** when backup recovery is completed. Press any key to reboot the system.

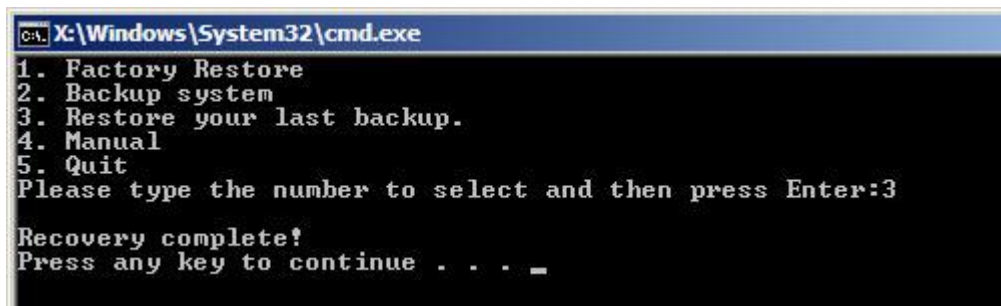


Figure B-32: Restore System Backup Complete Window

## B.4.4 Manual

To restore the last system backup, please follow the steps below.

**Step 1:** Type <4> and press <Enter> in the main menu.

**Step 2:** The Symantec Ghost window appears. Use the Ghost program to backup or recover the system manually.

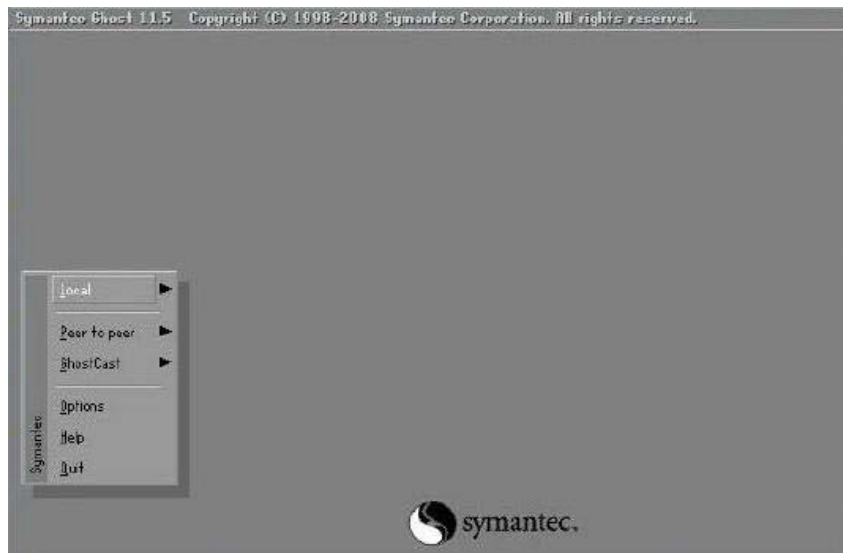


Figure B-33: Symantec Ghost Window

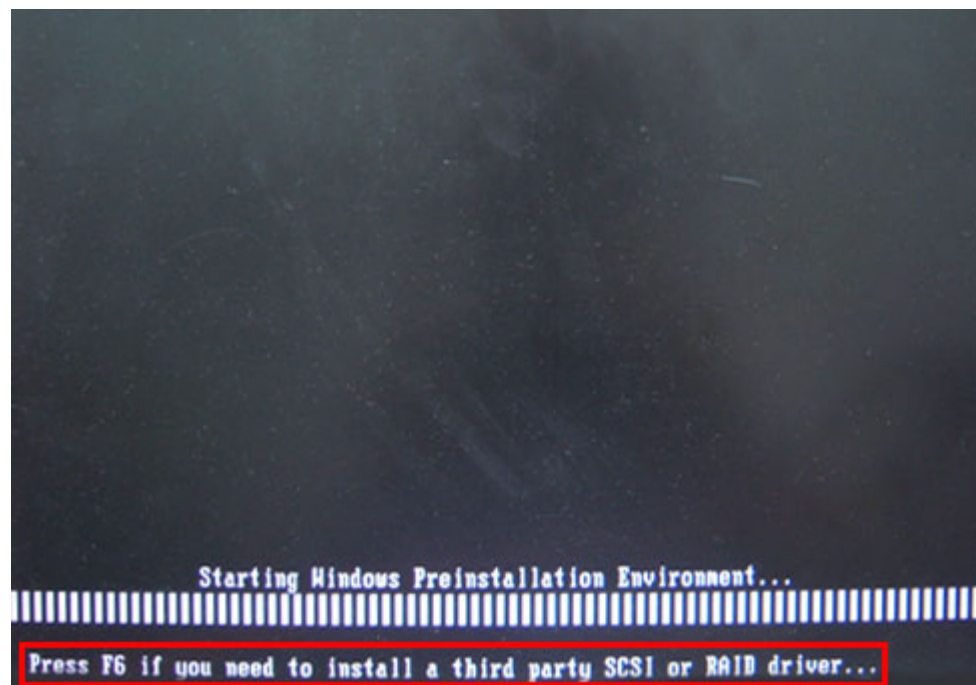
**Step 3:** When backup or recovery is completed, press any key to reboot the system.

## B.5 Other Information

### B.5.1 Using AHCI Mode or ALi M5283 / VIA VT6421A Controller

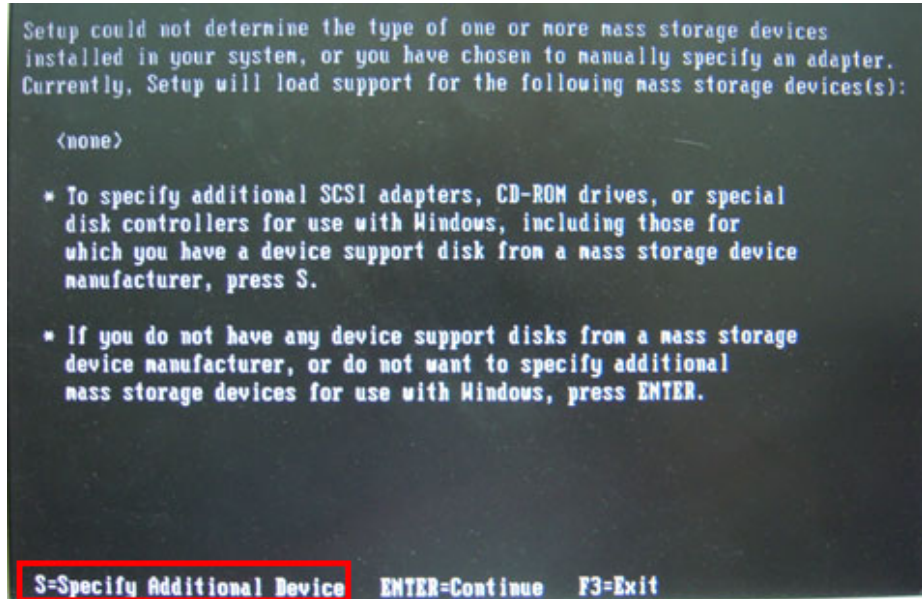
When the system uses AHCI mode or some specific SATA controllers such as ALi M5283 or VIA VT6421A, the SATA RAID/AHCI driver must be installed before using one key recovery. Please follow the steps below to install the SATA RAID/AHCI driver.

- Step 1:** Copy the SATA RAID/AHCI driver to a floppy disk and insert the floppy disk into a USB floppy disk drive. The SATA RAID/AHCI driver must be especially designed for the on-board SATA controller.
- Step 2:** Connect the USB floppy disk drive to the system.
- Step 3:** Insert the One Key Recovery CD into the system and boot the system from the CD.
- Step 4:** When launching the recovery tool, press <F6>.

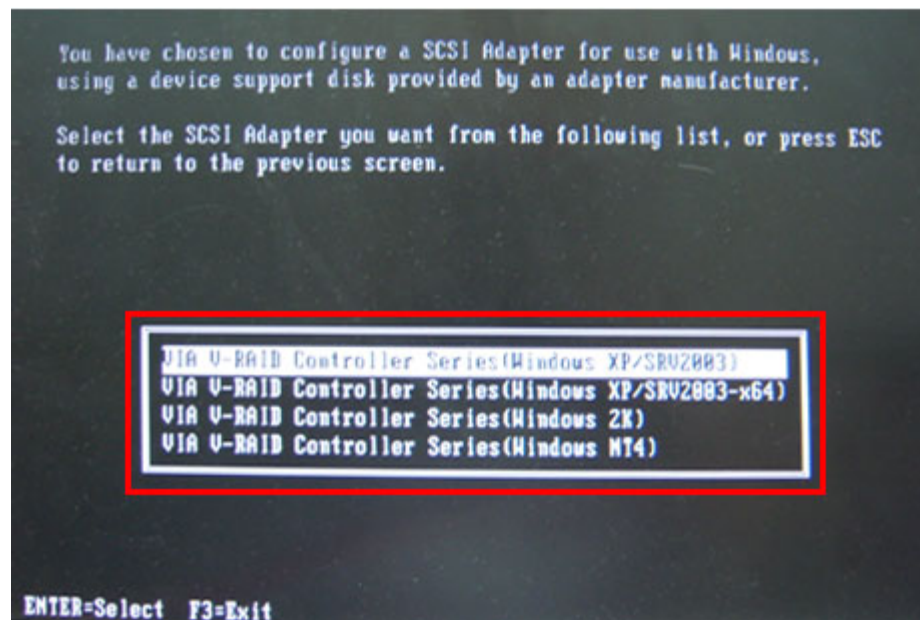




**Step 5:** When the following window appears, press <S> to select “Specify Additional Device”.



**Step 6:** In the following window, select a SATA controller mode used in the system. Then press <Enter>. The user can now start using the SATA HDD.





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**Step 7:** After pressing <Enter>, the system will get into the recovery tool setup menu.

Continue to follow the setup procedure from **Step 4** in **Section B.2.2 Create**

**Partitions** to finish the whole setup process.

### B.5.2 System Memory Requirement

To be able to access the recovery tool by pressing <F3> while booting up the system, please make sure to have enough system memory. The minimum memory requirement is listed below.

- **Using Award BIOS:** 128 MB system memory
- **Using AMI BIOS:** 512 MB system memory.

Appendix

C

# Terminology

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## WAFER-PV-D5252/D4252/N4552 SBC

<b>AC '97</b>	Audio Codec 97 (AC'97) refers to a codec standard developed by Intel® in 1997.
<b>ACPI</b>	Advanced Configuration and Power Interface (ACPI) is an OS-directed configuration, power management, and thermal management interface.
<b>AHCI</b>	Advanced Host Controller Interface (AHCI) is a SATA Host controller register-level interface.
<b>ATA</b>	The Advanced Technology Attachment (ATA) interface connects storage devices including hard disks and CD-ROM drives to a computer.
<b>ARMD</b>	An ATAPI Removable Media Device (ARMD) is any ATAPI device that supports removable media, besides CD and DVD drives.
<b>ASKIR</b>	Amplitude Shift Keyed Infrared (ASKIR) is a form of modulation that represents a digital signal by varying the amplitude (“volume”) of the signal. A low amplitude signal represents a binary 0, while a high amplitude signal represents a binary 1.
<b>BIOS</b>	The Basic Input/Output System (BIOS) is firmware that is first run when the computer is turned on and can be configured by the end user
<b>CODEC</b>	The Compressor-Decompressor (CODEC) encodes and decodes digital audio data on the system.
<b>CompactFlash®</b>	CompactFlash® is a solid-state storage device. CompactFlash® devices use flash memory in a standard size enclosure. Type II is thicker than Type I, but a Type II slot can support both types.
<b>CMOS</b>	Complimentary metal-oxide-conductor is an integrated circuit used in chips like static RAM and microprocessors.
<b>COM</b>	COM refers to serial ports. Serial ports offer serial communication to expansion devices. The serial port on a personal computer is usually a male DB-9 connector.
<b>DAC</b>	The Digital-to-Analog Converter (DAC) converts digital signals to analog signals.
<b>DDR</b>	Double Data Rate refers to a data bus transferring data on both the rising and falling edges of the clock signal.

<b>DMA</b>	Direct Memory Access (DMA) enables some peripheral devices to bypass the system processor and communicate directly with the system memory.
<b>DIMM</b>	Dual Inline Memory Modules are a type of RAM that offer a 64-bit data bus and have separate electrical contacts on each side of the module.
<b>DIO</b>	The digital inputs and digital outputs are general control signals that control the on/off circuit of external devices or TTL devices. Data can be read or written to the selected address to enable the DIO functions.
<b>EHCI</b>	The Enhanced Host Controller Interface (EHCI) specification is a register-level interface description for USB 2.0 Host Controllers.
<b>EIDE</b>	Enhanced IDE (EIDE) is a newer IDE interface standard that has data transfer rates between 4.0 MBps and 16.6 MBps.
<b>EIST</b>	Enhanced Intel® SpeedStep Technology (EIST) allows users to modify the power consumption levels and processor performance through application software. The application software changes the bus-to-core frequency ratio and the processor core voltage.
<b>FSB</b>	The Front Side Bus (FSB) is the bi-directional communication channel between the processor and the Northbridge chipset.
<b>GbE</b>	Gigabit Ethernet (GbE) is an Ethernet version that transfers data at 1.0 Gbps and complies with the IEEE 802.3-2005 standard.
<b>GPIO</b>	General purpose input
<b>HDD</b>	Hard disk drive (HDD) is a type of magnetic, non-volatile computer storage device that stores digitally encoded data.
<b>ICH</b>	The Input/Output Control Hub (ICH) is an Intel® Southbridge chipset.
<b>IrDA</b>	Infrared Data Association (IrDA) specify infrared data transmission protocols used to enable electronic devices to wirelessly communicate with each other.
<b>L1 Cache</b>	The Level 1 Cache (L1 Cache) is a small memory cache built into the system processor.
<b>L2 Cache</b>	The Level 2 Cache (L2 Cache) is an external processor memory cache.

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<b>LCD</b>	Liquid crystal display (LCD) is a flat, low-power display device that consists of two polarizing plates with a liquid crystal panel in between.
<b>LVDS</b>	Low-voltage differential signaling (LVDS) is a dual-wire, high-speed differential electrical signaling system commonly used to connect LCD displays to a computer.
<b>POST</b>	The Power-on Self Test (POST) is the pre-boot actions the system performs when the system is turned-on.
<b>RAM</b>	Random Access Memory (RAM) is volatile memory that loses data when power is lost. RAM has very fast data transfer rates compared to other storage like hard drives.
<b>SATA</b>	Serial ATA (SATA) is a serial communications bus designed for data transfers between storage devices and the computer chipsets. The SATA bus has transfer speeds up to 1.5 Gbps and the SATA II bus has data transfer speeds of up to 3.0 Gbps.
<b>S.M.A.R.T</b>	Self Monitoring Analysis and Reporting Technology (S.M.A.R.T) refers to automatic status checking technology implemented on hard disk drives.
<b>UART</b>	Universal Asynchronous Receiver-transmitter (UART) is responsible for asynchronous communications on the system and manages the system's serial communication (COM) ports.
<b>UHCI</b>	The Universal Host Controller Interface (UHCI) specification is a register-level interface description for USB 1.1 Host Controllers.
<b>USB</b>	The Universal Serial Bus (USB) is an external bus standard for interfacing devices. USB 1.1 supports 12Mbps data transfer rates and USB 2.0 supports 480Mbps data transfer rates.
<b>VGA</b>	The Video Graphics Array (VGA) is a graphics display system developed by IBM.



Appendix

**D**

# Hazardous Materials Disclosure

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## **D.1 Hazardous Materials Disclosure Table for IPB Products Certified as RoHS Compliant Under 2002/95/EC Without Mercury**

The details provided in this appendix are to ensure that the product is compliant with the Peoples Republic of China (China) RoHS standards. The table below acknowledges the presences of small quantities of certain materials in the product, and is applicable to China RoHS only.

A label will be placed on each product to indicate the estimated “Environmentally Friendly Use Period” (EFUP). This is an estimate of the number of years that these substances would “not leak out or undergo abrupt change.” This product may contain replaceable sub-assemblies/components which have a shorter EFUP such as batteries and lamps. These components will be separately marked.

Please refer to the table on the next page.

Part Name	Toxic or Hazardous Substances and Elements					
	Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (CR(VI))	Polybrominated Biphenyls (PBB)	Polybrominated Diphenyl Ethers (PBDE)
<b>Housing</b>	X	O	O	O	O	X
<b>Display</b>	X	O	O	O	O	X
<b>Printed Circuit Board</b>	X	O	O	O	O	X
<b>Metal Fasteners</b>	X	O	O	O	O	O
<b>Cable Assembly</b>	X	O	O	O	O	X
<b>Fan Assembly</b>	X	O	O	O	O	X
<b>Power Supply Assemblies</b>	X	O	O	O	O	X
<b>Battery</b>	O	O	O	O	O	O

O: This toxic or hazardous substance is contained in all of the homogeneous materials for the part is below the limit requirement in SJ/T11363-2006

X: This toxic or hazardous substance is contained in at least one of the homogeneous materials for this part is above the limit requirement in SJ/T11363-2006

## WAFER-PV-D5252/D4252/N4552 SBC

此附件旨在确保本产品符合中国 RoHS 标准。以下表格标示此产品中某有毒物质的含量符合中国 RoHS 标准规定的限量要求。

本产品上会附有“环境友好使用期限”的标签，此期限是估算这些物质“不会有泄漏或突变”的年限。本产品可能包含有较短的环境友好使用期限的可替换元件，像是电池或灯管，这些元件将会单独标示出来。

部件名称	有毒有害物质或元素					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (CR(VI))	多溴联苯 (PBB)	多溴二苯 醚 (PBDE)
壳体	X	O	O	O	O	X
显示	X	O	O	O	O	X
印刷电路板	X	O	O	O	O	X
金属螺帽	X	O	O	O	O	O
电缆组装	X	O	O	O	O	X
风扇组装	X	O	O	O	O	X
电力供应组装	X	O	O	O	O	X
电池	O	O	O	O	O	O

O: 表示该有毒有害物质在该部件所有物质材料中的含量均在 SJ/T11363-2006 标准规定的限量要求以下。  
X: 表示该有毒有害物质至少在该部件的某一均质材料中的含量超出 SJ/T11363-2006 标准规定的限量要求。