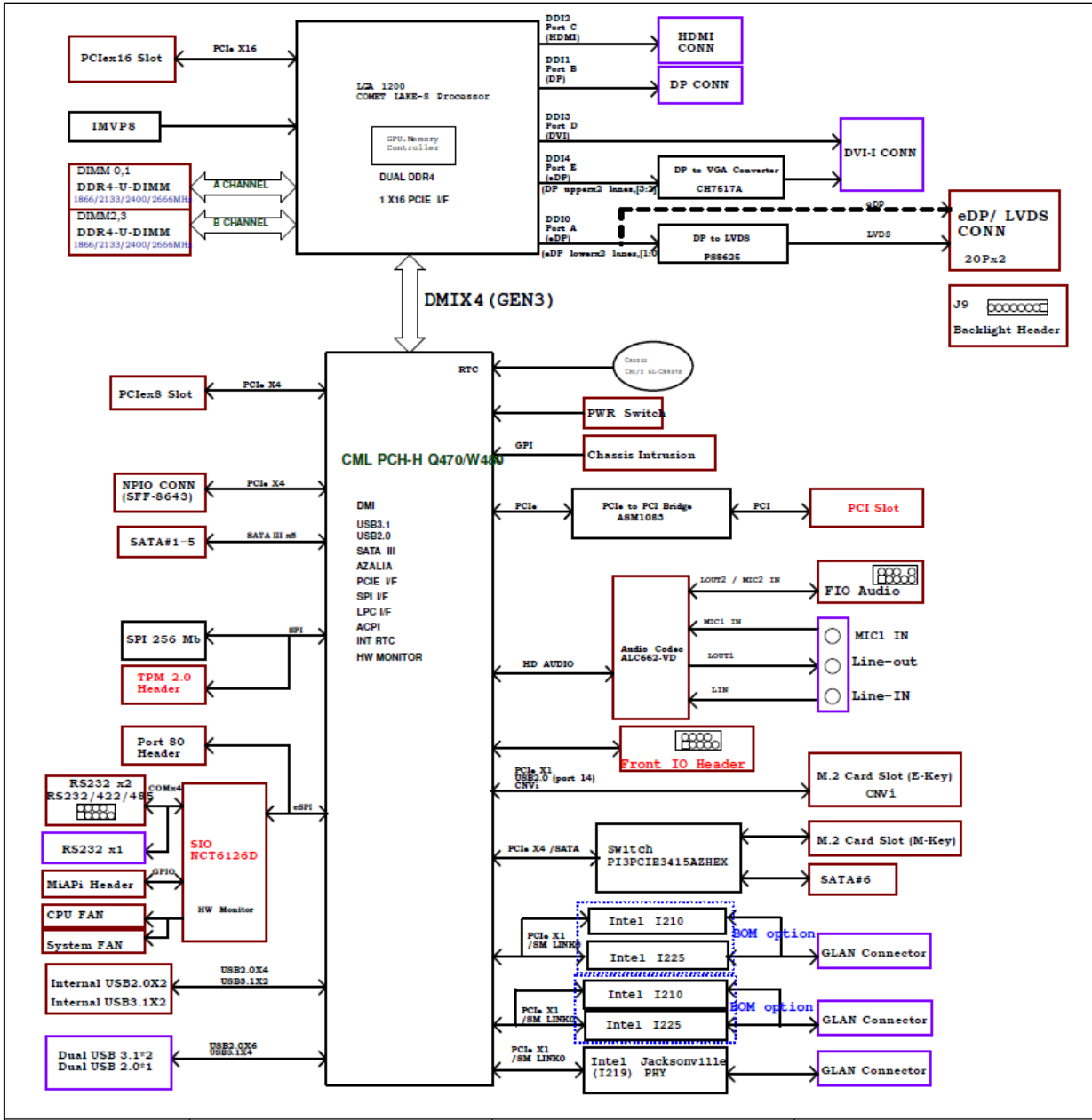


# MITAC Industrial MB PH10CMU Product Guide

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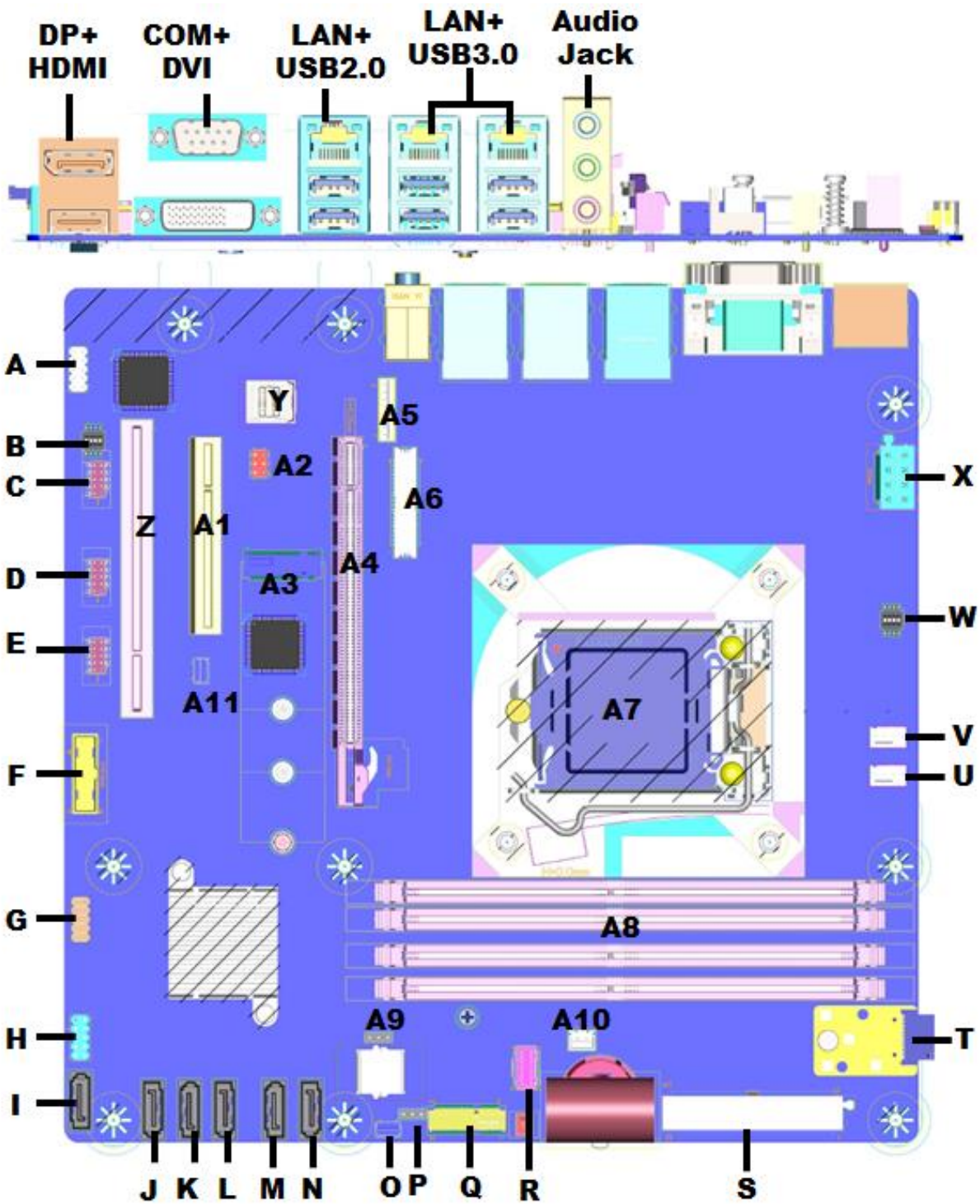
# 1. Block Diagram



\*Remark:

The M.2 2280 M-key slot s with PCIe x4 lanes. If for using Intel Optane Memory H-series module (by 2 split PCIe x2), additional ME update is needed so as to support it.

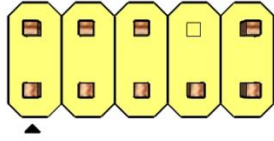
## 2. Board placement



| Conn | Description                         | Conn | Description                     |
|------|-------------------------------------|------|---------------------------------|
| A    | Front Audio Header                  | T    | TPM Header                      |
| B    | COM3 Termination Resistor Switch    | U    | System FAN Header               |
| C    | RS232/RS422/RS485 Header            | V    | CPU FAN Header                  |
| D    | RS232 Header                        | W    | PCI Express Bifurcation Switch  |
| E    | RS232 Header                        | X    | 8pin ATX Power Source Connector |
| F    | Dual USB3.0 Header                  | Y    | NPIO Header(PCIEX4)             |
| G    | Dual USB2.0 Header                  | Z    | PCI Slot                        |
| H    | Front I/O Header                    | A1   | PCIEX8 slot (PCIEX4)            |
| I    | SATA Header                         | A2   | Panel Power Option              |
| J    | SATA Header                         | A3   | M.2 KEY M Connector             |
| K    | SATA Header                         | A4   | PCIe X16 Slot                   |
| L    | SATA Header                         | A5   | LVDS Backlight Header           |
| M    | SATA Header                         | A6   | LVDS / eDP Connector            |
| N    | SATA Header(optional with M.2 SATA) | A7   | CPU Socket                      |
| O    | SPI Programing Header(Debug)        | A8   | DDR4 Memory                     |
| P    | AT/ATX Mode Selection Header        | A9   | CMOS Reset Header               |
| Q    | M.2 KEY E Connector                 | A10  | Intrusion Header                |
| R    | MiAPI Header                        | A11  | Debug Header                    |
| S    | 24pin ATX Power Source Connector    |      |                                 |

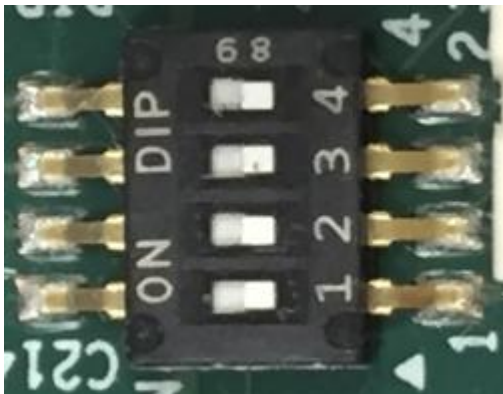
### 3. Connector & Header Pin Definition

|          |                |                           |
|----------|----------------|---------------------------|
| <b>A</b> | <b>J_HDA_1</b> | <b>Front Audio Header</b> |
|----------|----------------|---------------------------|



| Pin | Signal name | Description  |
|-----|-------------|--|
| 1   | MIC         | Front panel microphone input signal (biased when supporting stereo microphone)   |
| 2   | AUD_GND     | Ground used by analog audio circuits   |
| 3   | MIC_BIAS    | Microphone power / additional MIC input for stereo microphone support  |
| 4   | PRESENCE#   | Active low signal that signals BIOS that an Intel® HD Audio dongle is connected to the analog header. PRESENCE# = 0 when an Intel® HD Audio dongle is connected. |
| 5   | FP_OUT_R    | Right channel audio signal to front panel (headphone drive capable)  |
| 6   | AUD_GND     | Ground used by analog audio circuits   |
| 7   | RESERVED    | Reserved   |
| 8   | KEY         | No pin   |
| 9   | FP_OUT_L    | Left channel audio signal to front panel (headphone drive capable)   |
| 10  | AUD_GND     | Ground used by analog audio circuits   |

|          |            |   |
|----------|------------|---|
| <b>B</b> | <b>SW1</b> | <b>COM3 Termination Resistor Switch</b> |
|----------|------------|---|



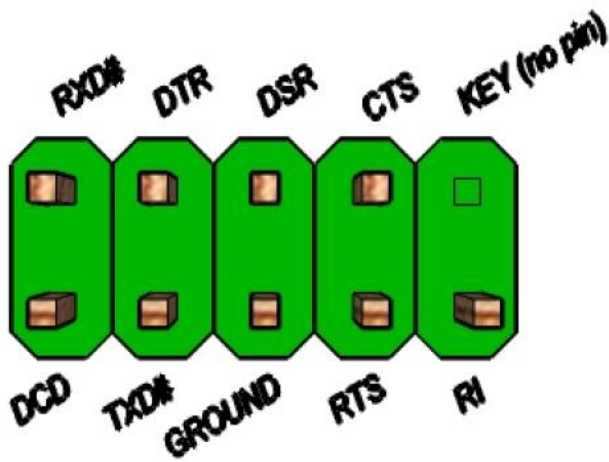
| Pin | Net name |
|-----|----------|
| 1   | NRX3     |
| 2   | NDCD3    |
| 3   | NTX3     |
| 4   | NDTR3    |

ON:PULL HIGH, OFF:NC

Termination mode,

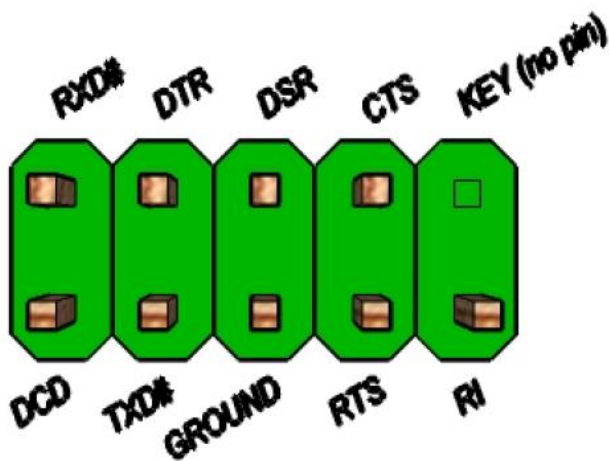
1. RS422 PULL H/L in RX/DCD
2. RS485 PULL H/L in RX/DCD and TX/DTR

|   |      |                          |
|---|------|--------------------------|
| C | COM3 | RS232/RS422/RS485 Header |
|---|------|--------------------------|



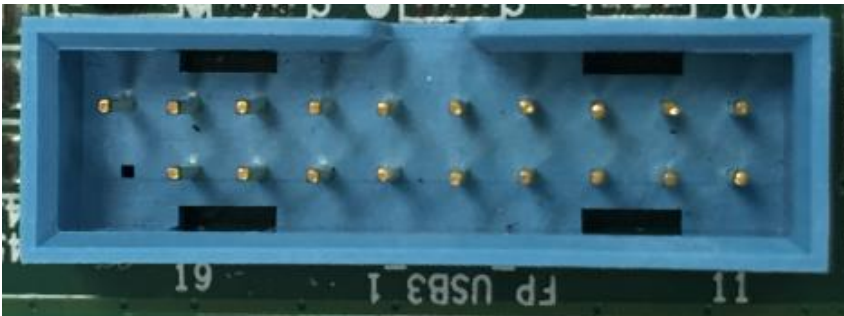
| Pin | Signal                    | Pin | Signal                    |
|-----|---------------------------|-----|---------------------------|
| 1   | DCD (Data Carrier Detect) | 2   | RXD# (Receive Data)       |
| 3   | TXD# (Transmit Data)      | 4   | DTR (Data Terminal Ready) |
| 5   | Ground                    | 6   | DSR (Data Set Ready)      |
| 7   | RTS (Request To Send)     | 8   | CTS (Clear To Send)       |
| 9   | RI (Ring Indicator)       | 10  | Key (no pin)              |

|     |           |              |
|-----|-----------|--------------|
| D,E | COM1,COM2 | RS232 Header |
|-----|-----------|--------------|



| Pin | Signal                    | Pin | Signal                    |
|-----|---------------------------|-----|---------------------------|
| 1   | DCD (Data Carrier Detect) | 2   | RXD# (Receive Data)       |
| 3   | TXD# (Transmit Data)      | 4   | DTR (Data Terminal Ready) |
| 5   | Ground                    | 6   | DSR (Data Set Ready)      |
| 7   | RTS (Request To Send)     | 8   | CTS (Clear To Send)       |
| 9   | RI (Ring Indicator)       | 10  | Key (no pin)              |

**F**    *FP\_USB3\_1*    *Dual USB3.0 Header*



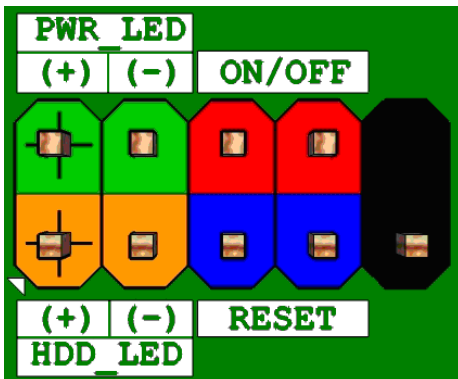
| Pin | Signal              | Pin | Signal              |
|-----|---------------------|-----|---------------------|
| 1   | +5V DC              |     | Key (no pin)        |
| 2   | 2.0 Data (negative) | 19  | +5V DC              |
| 3   | 2.0 Data (positive) | 18  | 2.0 Data (negative) |
| 4   | Ground              | 17  | 2.0 Data (positive) |
| 5   | 3.0 Data (negative) | 16  | Ground              |
| 6   | 3.0 Data (positive) | 15  | 3.0 Data (negative) |
| 7   | Ground              | 14  | 3.0 Data (positive) |
| 8   | 3.0 Data (negative) | 13  | Ground              |
| 9   | 3.0 Data (positive) | 12  | 3.0 Data (negative) |
| 10  | No Connect          | 11  | 3.0 Data (positive) |

**G**    *FP\_USB2\_1*    *Dual USB2.0 Header*



| Pin | Signal          | Pin | Signal          |
|-----|-----------------|-----|-----------------|
| 1   | 5V_USB          | 2   | 5V_USB          |
| 3   | Data (negative) | 4   | Data (negative) |
| 5   | Data (positive) | 6   | Data (positive) |
| 7   | Ground          | 8   | Ground          |
| 9   | Key (no pin)    | 10  | No Connect      |

|          |              |                         |
|----------|--------------|-------------------------|
| <i>H</i> | <i>J_FIO</i> | <i>Front I/O Header</i> |
|----------|--------------|-------------------------|



| Pin | Signal Name   | Description                      | Pin | Signal Name    | Description                        |
|-----|---------------|----------------------------------|-----|----------------|------------------------------------|
| 1   | HDD_POWER_LED | Pull-up resistor (750ohm) to +5V | 2   | POWER_LED_MAIN | [Out] Front panel LED (main color) |
| 3   | HDD_LED#      | [Out] Hard disk activity LED     | 4   | POWER_LED_ALT  | [Out] Front panel LED (alt color)  |
| 5   | GROUND        | Ground                           | 6   | POWER_SWITCH#  | [In] Power switch                  |
| 7   | RESET_SWITCH# | [In] Reset switch                | 8   | GROUND         | Ground                             |
| 9   | +5V_DC        | Power                            | 10  | KEY            | No pin                             |

|            |                |                    |
|------------|----------------|--------------------|
| <i>I~N</i> | <i>SATA1~6</i> | <i>SATA Header</i> |
|------------|----------------|--------------------|





| Pin  | Signal Name    | Description                   |
|------|----------------|-------------------------------|
| 1    | GND            | Ground                        |
| 2    | SATAHDR_TXP0_C | SATA DATA Transmit (positive) |
| 3    | SATAHDR_TXN0_C | SATA DATA Transmit (negative) |
| 4    | GND            | Ground                        |
| 5    | SATAHDR_RXN0_C | SATA DATA Receive (negative)  |
| 6    | SATAHDR_RXP0_C | SATA DATA Receive (positive)  |
| 7    | GND            | Ground                        |
| GND1 | GND            | Ground                        |
| GND2 | GND            | Ground                        |

|          |                 |                                     |
|----------|-----------------|-------------------------------------|
| <i>O</i> | <i>SPI_HDR1</i> | <i>SPI Programing Header(Debug)</i> |
|----------|-----------------|-------------------------------------|



| Pin | Signal Name     |
|-----|-----------------|
| 1   | GND             |
| 2   | P_Flash(3.3V)   |
| 3   | NC              |
| 4   | ROMWREN0_N      |
| 5   | SPI_MOSI_FLASH  |
| 6   | SPI_MISO_FLASH  |
| 7   | SPI_CLK_FLSH    |
| 8   | SPI_CS0_N_FLASH |
| 9   | NC              |
| 10  | NC              |

|          |               |                                     |
|----------|---------------|-------------------------------------|
| <i>P</i> | <i>JPSON1</i> | <i>AT/ATX Mode Selection Header</i> |
|----------|---------------|-------------------------------------|



**Pins 1&2: jumper position for AT Mode**

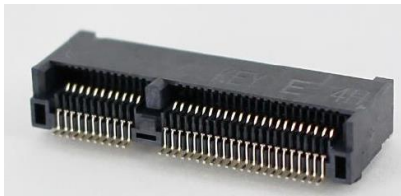


Pins 2&3: NON-AT Mode

AT/ATX Mode selection

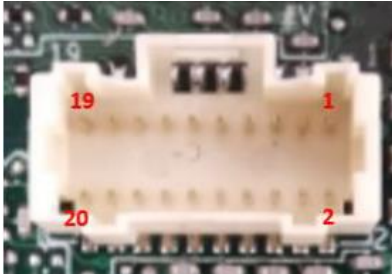
|     |             |
|-----|-------------|
| 1-2 | AT Mode     |
| 2-3 | NON-AT Mode |

|          |              |                            |
|----------|--------------|----------------------------|
| <b>Q</b> | <b>M2E_1</b> | <b>M.2 KEY E Connector</b> |
|----------|--------------|----------------------------|



|          | Standard M.2 Key E                                     | LcP Signals                 | LcP Signals           | Standard M.2 Key E     |          |
|----------|--|-----------------------------|-----------------------|------------------------|----------|
| 74       |  | +V3P3A                      | GND                   |                        | 75       |
| 72       |  | +V3P3A                      | WT_CLKP               | REFCLKN1               | 73       |
| 70       |  | PEWake1# (I/O)(0/3.3V)      | WT_CLKN               | REFCLKP1               | 71       |
| 68       |  | CLKREQ1# (IO)(0/3.3V)       | GND                   |                        | 69       |
| 66       |  | PERST1# (O)(0/3.3V)         | WT_D0P                | PERn1                  | 67       |
| 64       | RESERVED   | REFCLK0 (I)(1V @33.4MHz)    | WT_D0N                | PERp1                  | 65       |
| 62       | ALERT# (I)(0/1.8)                                      | A4WP_IRQ#                   | GND                   |                        | 63       |
| 60       | I2C_CLK (O)(0/1.8V)                                    | A4WP_I2C_CLK                | WT_D1P                | PETn1                  | 61       |
| 58       | I2C_DATA (IO)(0/1.8)                                   | A4WP_I2C_DATA               | WT_D1N                | PETp1                  | 59       |
| 56       | W_DISABLE1# (O)(0/3.3V)                                |                             | GND                   |                        | 57       |
| 54       | W_DISABLE2# (O)(0/3.3V)                                |                             | PEWake0# (IO)(0/3.3V) |                        | 55       |
| 52       | PERST0# (O)(0/3.3V)                                    |                             | CLKREQ0# (IO)(0/3.3V) |                        | 53       |
| 50       | SUSCLK(32kHz) (O)(0/3.3V)                              | C_P32K (3.3V Tolerant)      | GND                   |                        | 51       |
| 48       | COEX_TXD (O)(0/1.8V)                                   |                             | REFCLKN0              |                        | 49       |
| 46       | COEX_RXD (O)(0/1.8V)                                   |                             | REFCLKP0              |                        | 47       |
| 44       | COEX3 (IO)(0/1.8V)                                     |                             | GND                   |                        | 45       |
| 42       | CLink CLK  |                             | PERn0                 |                        | 43       |
| 40       | CLink DATA   |                             | PERp0                 |                        | 41       |
| 38       | CLink RESET (O)(0/3.3V)                                |                             | GND                   |                        | 39       |
| 36       | LPSS UART RTS (O)(0/1.8V) / BRI_DT (MUX'd in PCH/SoC)  |                             | PETn0                 |                        | 37       |
| 34       | LPSS UART CTS (I)(0/1.8V) / RGI_RSP (MUX'd in PCH/SoC) |                             | PETp0                 |                        | 35       |
| 32       | LPSS UART Tx (O)(0/1.8V) / RGI_DT (MUX'd in PCH/SoC)   |                             | GND                   |                        | 33       |
| <b>E</b> | Connector Key  |                             | Connector Key         |                        | <b>E</b> |
|          | Connector Key  |                             | Connector Key         |                        |          |
|          | Connector Key  |                             | Connector Key         |                        |          |
|          | Connector Key  |                             | Connector Key         |                        |          |
| 22       | LPSS UART Rx (I)(0/1.8V) / BRI_RSP (MUX'd in PCH/SoC)  |                             | WGR_CLKP              | SDIO Reset#(O)(0/1.8V) | 23       |
| 20       | UART Wake# (I)(0/3.3V)                                 |                             | WGR_CLKN              | SDIO Wake#(I)(0/1.8V)  | 21       |
| 18       | GND  | GND/LNA_EN (LcP Production) | GND                   | SDIO DAT3(IO)(0/1.8V)  | 19       |
| 16       | LED2# (I)(OD)  |                             | WGR_D0P               | SDIO DAT2(IO)(0/1.8V)  | 17       |
| 14       | PCM_OUT (O)(0/1.8V) / CLKREQ0 (MUX'd in PCH/SoC)       |                             | WGR_D0N               | SDIO DAT1(IO)(0/1.8V)  | 15       |
| 12       | PCM_IN (I)(0/1.8V)                                     |                             | GND                   | SDIO DAT0(IO)(0/1.8V)  | 13       |
| 10       | PCM_SYNC (O)(0/1.8V) / RF_RESET_B (MUX'd in PCH/SoC)   |                             | WGR_D1P               | SDIO CMD0(IO)(0/1.8V)  | 11       |
| 8        | PCM_CLK (O)(0/1.8V)                                    |                             | WGR_D1N               | SDIO CLK(O)(0/1.8V)    | 9        |
| 6        | LED1# (I)(OD)  |                             | GND                   |                        | 7        |
| 4        | +V3P3A   |                             | USB_D-                |                        | 5        |
| 2        | +V3P3A   |                             | USB_D+                |                        | 3        |
|          |  |                             | GND                   |                        | 1        |

|          |                 |                     |
|----------|-----------------|---------------------|
| <i>R</i> | <i>J_MAPI_1</i> | <i>MiAPI Header</i> |
|----------|-----------------|---------------------|



| Pin | Net name     | Pin | Net name    |
|-----|--------------|-----|-------------|
| 1   | MAPI_GPIO1   | 2   | VCC         |
| 3   | MAPI_GPIO2   | 4   | MAPI_GPIO6  |
| 5   | MAPI_GPIO3   | 6   | MAPI_GPIO7  |
| 7   | MAPI_GPIO4   | 8   | MAPI_GPIO8  |
| 9   | MAPI_GPIO5   | 10  | MAPI_GPIO9  |
| 11  | WD_Time      | 12  | MAPI_GPIO10 |
| 13  | Power Button | 14  | SMBUS_DATA  |
| 15  | UART_TX      | 16  | SMBUS_CLK   |
| 17  | UART_RX      | 18  | 5VSB        |
| 19  | GND          | 20  | NA          |

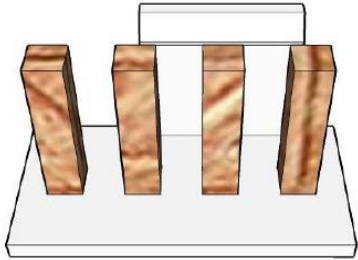
|          |            |                   |
|----------|------------|-------------------|
| <i>T</i> | <i>J48</i> | <i>TPM Header</i> |
|----------|------------|-------------------|



| Pin | Net name | Pin | Net name     |
|-----|----------|-----|--------------|
| 1   | VCC3_TPM | 2   | TPM_CS2      |
| 3   | TPM_MISO | 4   | Key (no pin) |
| 5   | TPM_MOSI | 6   | PLTRST_N     |
| 7   | PRIQ_N   | 8   | GND          |
| 9   | NC       | 10  | SPI_CLK      |
| 11  | NC       | 12  | TPM_DET      |

|    |    |    |         |
|----|----|----|---------|
| 13 | NC | 14 | VSB_3V3 |
|----|----|----|---------|

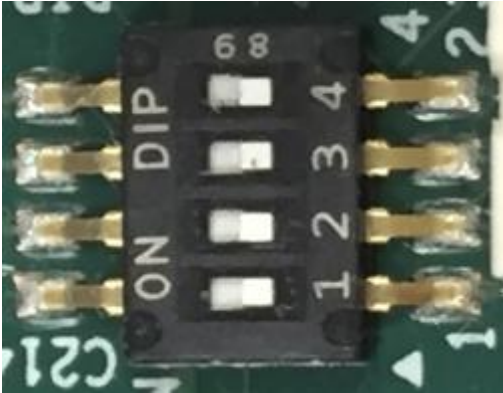
|            |                               |                   |
|------------|-------------------------------|-------------------|
| <i>U~V</i> | <i>J_CPU_FAN1, J_FIO_FAN1</i> | <i>FAN Header</i> |
|------------|-------------------------------|-------------------|



| Pin | Signal       |
|-----|--------------|
| 1   | Ground       |
| 2   | +12V         |
| 3   | CPU_FAN_TACH |
| 4   | CPU_FAN_CTRL |

|          |            |                                       |
|----------|------------|---------------------------------------|
| <i>W</i> | <i>SW2</i> | <i>PCI Express Bifurcation Switch</i> |
|----------|------------|---------------------------------------|

| Pin | Net name |
|-----|----------|
| 1   | CFG5     |
| 2   | CFG6     |
| 3   | NC       |
| 4   | NC       |

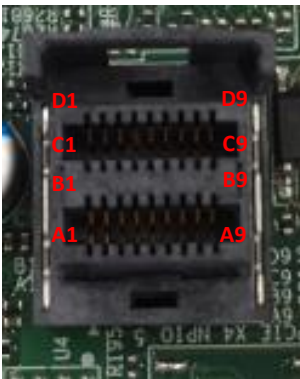


ON:PULL LOW, OFF:NC

CFG[6:5]: PCI Express Bifurcation

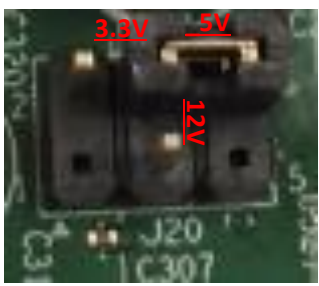
- 00 = 1 x8, 2 x4 PCI Express
- 01 = reserved
- 10 = 2 x8 PCI Express
- 11 = 1 x16 PCI Express (Default)

|          |                     |                            |
|----------|---------------------|----------------------------|
| <i>Y</i> | <i>PCIE_X4_NPIO</i> | <i>NPIO Header(PCIEX4)</i> |
|----------|---------------------|----------------------------|



| Pin | Signal  | Pin | Signal  | Pin | Signal   | Pin | Signal |
|-----|---------|-----|---------|-----|----------|-----|--------|
| A1  | REFCLK+ | B1  | PERST#  | C1  | VCC 3.3V | D1  | SMDAT  |
| A2  | REFCLK+ | B2  | CLKREQ# | C2  | N.C      | D2  | SMCLK  |
| A3  | GND     | B3  | GND     | C3  | GND      | D3  | GND    |
| A4  | PERp1   | B4  | PERp0   | C4  | PETp1    | D4  | PETp0  |
| A5  | PERn1   | B5  | PERn0   | C5  | PETn1    | D5  | PETn0  |
| A6  | GND     | B6  | GND     | C6  | GND      | D6  | GND    |
| A7  | PERp3   | B7  | PERp2   | C7  | PETp3    | D7  | PETp2  |
| A8  | PERn3   | B8  | PERn2   | C8  | PETn3    | D8  | PETn2  |
| A9  | GND     | B9  | GND     | C9  | GND      | D9  | GND    |

|           |            |                           |
|-----------|------------|---------------------------|
| <b>A2</b> | <b>J20</b> | <i>Panel Power Option</i> |
|-----------|------------|---------------------------|



| Pin | Net name     | Pin | Net name    |
|-----|--------------|-----|-------------|
| 1   | Key (no pin) | 2   | VCC3        |
| 3   | +12V         | 4   | LCD_VCC_SEL |
| 5   | Key (no pin) | 6   | VCC         |

|           |              |                            |
|-----------|--------------|----------------------------|
| <b>A3</b> | <b>M2M_1</b> | <i>M.2 KEY M Connector</i> |
|-----------|--------------|----------------------------|



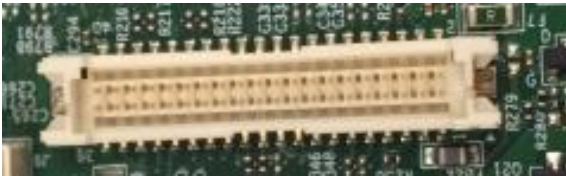
|    |                             |                          |    |
|----|-----------------------------|--------------------------|----|
|    |                             | GND                      | 75 |
| 74 | 3.3Vaux                     | GND                      | 73 |
| 72 | 3.3Vaux                     | GND                      | 71 |
| 70 | 3.3Vaux                     | PEDET (OC-PCIe/GND-SATA) | 69 |
| 68 | SUSCLK(32kHz) (O)(0/3.3V)   | N/C                      | 67 |
|    | Key                         | Key                      |    |
|    | Key                         | Key                      |    |
|    | Key                         | Key                      |    |
|    | Key                         | Key                      |    |
| 58 | N/C                         | GND                      | 57 |
| 56 | N/C                         | REFCLKP                  | 55 |
| 54 | PEWake# (IO)(0/3.3V) or N/C | REFCLKN                  | 53 |
| 52 | CLKREQ# (IO)(0/3.3V) or N/C | GND                      | 51 |
| 50 | PERST# (O)(0/3.3V) or N/C   | PERp0/SATA-A+            | 49 |
| 48 | N/C                         | PERn0/SATA-A-            | 47 |
| 46 | N/C                         | GND                      | 45 |
| 44 | N/C                         | PETp0/SATA-B-            | 43 |
| 42 | N/C                         | PETn0/SATA-B+            | 41 |
| 40 | N/C                         | GND                      | 39 |
| 38 | DEVSLP (O)(0/3.3V)          | PERp1                    | 37 |
| 36 | N/C                         | PERn1                    | 35 |
| 34 | N/C                         | GND                      | 33 |
| 32 | N/C                         | PETp1                    | 31 |
| 30 | N/C                         | PETn1                    | 29 |
| 28 | N/C                         | GND                      | 27 |
| 26 | N/C                         | N/C                      | 25 |
| 24 | N/C                         | N/C                      | 23 |
| 22 | N/C                         | GND                      | 21 |
| 20 | N/C                         | N/C                      | 19 |
| 18 | 3.3Vaux                     | N/C                      | 17 |
| 16 | 3.3Vaux                     | GND                      | 15 |
| 14 | 3.3Vaux                     | N/C                      | 13 |
| 12 | 3.3Vaux                     | N/C                      | 11 |
| 10 | DAS/DSS# (I)(OD)            | GND                      | 9  |
| 8  | N/C                         | N/C                      | 7  |
| 6  | N/C                         | N/C                      | 5  |
| 4  | 3.3Vaux                     | GND                      | 3  |
| 2  | 3.3Vaux                     | GND                      | 1  |

|    |    |                       |
|----|----|-----------------------|
| A5 | J9 | LVDS Backlight Header |
|----|----|-----------------------|



| Pin | Net name    |
|-----|-------------|
| 1   | BKLT_EN     |
| 2   | BKLT_CTRL   |
| 3   | BKLT_PWR    |
| 4   | BKLT_PWR    |
| 5   | GND         |
| 6   | GND         |
| 7   | BRIGHT_UP   |
| 8   | BRIGHT_DOWN |

|    |    |                     |
|----|----|---------------------|
| A6 | J5 | LVDS /eDP Connector |
|----|----|---------------------|



The connector is used for LVDS/eDP, it could select by BOM option.

#### 40-pin LVDS connector pin-out

| Pin | Signal       | Description                                       |
|-----|--------------|---|
| 1   | VCC3         | VCC   |
| 2   | 3.3V/5V/12V  | Selectable LCD power output                       |
| 3   | VCC3         | VCC   |
| 4   | 3.3V/5V/12V  | Selectable LCD power output                       |
| 5   | LVDS_DDC_SCL | LVDS_DDC_SCL                                      |
| 6   | LVDS_DDC_SDA | LVDS_DDC_SDA                                      |
| 7   | CABLE_ID1    | VCC (reserve for MiTAC AIO CABLE_ID1, low enable) |
| 8   | GND          | Ground  |
| 9   | TB0P         | LVDS Channel A diff data output - positive        |
| 10  | TA0P         | LVDS Channel A diff data output - positive        |
| 11  | TB0N         | LVDS Channel A diff data output - negative        |
| 12  | TA0N         | LVDS Channel A diff data output - negative        |
| 13  | GND          | Ground  |
| 14  | GND          | Ground  |
| 15  | TD0P         | LVDS Channel A diff data output - positive        |
| 16  | TC0P         | LVDS Channel A diff data output - positive        |
| 17  | TD0N         | LVDS Channel A diff data output - negative        |



|    |       |  |
|----|-------|--|
| 18 | TC0N  | LVDS Channel A diff data output - negative           |
| 19 | GND   | Ground   |
| 20 | GND   | Ground   |
| 21 | TB1P  | LVDS Channel B diff data output-positive             |
| 22 | TA1P  | LVDS Channel B diff data output-positive             |
| 23 | TB1N  | LVDS Channel B diff data output-negative             |
| 24 | TA1N  | LVDS Channel B diff data output-negative             |
| 25 | GND   | Ground   |
| 26 | GND   | Ground   |
| 27 | TD1P  | LVDS Channel B diff data output-positive             |
| 28 | TC1P  | LVDS Channel B diff data output-positive             |
| 29 | TD1N  | LVDS Channel B diff data output-negative             |
| 30 | TC1N  | LVDS Channel B diff data output-negative             |
| 31 | GND   | Ground   |
| 32 | GND   | Ground   |
| 33 | TCK1P | LVDS Channel B diff data output - positive           |
| 34 | TCK0P | LVDS Channel A diff data output - positive           |
| 35 | TCK1N | LVDS Channel B diff data output - negative           |
| 36 | TCK0N | LVDS Channel A diff data output - negative           |
| 37 | GND   | Ground   |
| 38 | GND   | Ground   |
| 39 | NC    | NC (reserve for MiTAC selectable BKLT power output ) |
| 40 | NC    | NC (reserve for MiTAC selectable BKLT power output ) |

#### 40-pin eDP connector pin-out

| Pin | Signal       | Description                                       |
|-----|--------------|---|
| 1   | VCC3         | VCC   |
| 2   | 3.3V/5V/12V  | Selectable LCD power output                       |
| 3   | VCC3         | VCC   |
| 4   | 3.3V/5V/12V  | Selectable LCD power output                       |
| 5   | EDP_CPU_AUX- | Aux channel                                       |
| 6   | EDP_CPU_AUX+ | Aux channel                                       |
| 7   | CABLE_ID1    | VCC (reserve for MiTAC AIO CABLE_ID1, low enable) |
| 8   | HPDET        | Hot plug detection                                |

|    |                   |                   |
|----|-------------------|-------------------|
| 9  | EDP_CPU_1+        | MAIN LINK         |
| 10 | EDP_CPU_0+        | MAIN LINK         |
| 11 | EDP_CPU_1-        | MAIN LINK         |
| 12 | EDP_CPU_0-        | MAIN LINK         |
| 13 | GND               | Ground            |
| 14 | GND               | Ground            |
| 15 | NC                | NC                |
| 16 | NC                | NC                |
| 17 | NC                | NC                |
| 18 | NC                | NC                |
| 19 | GND               | Ground            |
| 20 | GND               | Ground            |
| 21 | BKLT_EN           | BKLT_EN           |
| 22 | PCH_BACKLIGHT_PWM | PCH_BACKLIGHT_PWM |
| 23 | NC                | NC                |
| 24 | NC                | NC                |
| 25 | GND               | Ground            |
| 26 | GND               | Ground            |
| 27 | NC                | NC                |
| 28 | NC                | NC                |
| 29 | NC                | NC                |
| 30 | NC                | NC                |
| 31 | GND               | Ground            |
| 32 | GND               | Ground            |
| 33 | NC                | NC                |
| 34 | NC                | NC                |
| 35 | NC                | NC                |
| 36 | NC                | NC                |

|    |          |                                    |
|----|----------|------------------------------------|
| 37 | GND      | Ground                             |
| 38 | GND      | Ground                             |
| 39 | BKLT_PWR | Selectable BKLT power output (12V) |
| 40 | BKLT_PWR | Selectable BKLT power output (12V) |

|           |                |                          |
|-----------|----------------|--------------------------|
| <b>A9</b> | <b>CLCMOS1</b> | <b>CMOS Reset Header</b> |
|-----------|----------------|--------------------------|



Pins 1&2: jumper position for CMOS Reset

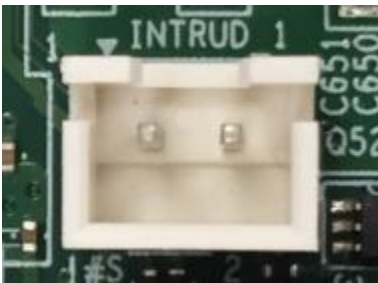


Pins 2&3: Normal

**CMOS Clear**

|     |            |
|-----|------------|
| 1-2 | Clear CMOS |
| 2-3 | Normal     |

|            |                 |                                |
|------------|-----------------|--------------------------------|
| <b>A10</b> | <b>INTRUD_1</b> | <b>Intrusion Switch Header</b> |
|------------|-----------------|--------------------------------|



| Pin | Net name   |
|-----|------------|
| 1   | INTRUDER_N |
| 2   | GND        |

|            |                  |                     |
|------------|------------------|---------------------|
| <b>A11</b> | <b>eSPI_HDR1</b> | <b>Debug Header</b> |
|------------|------------------|---------------------|



| Pin | Signal Name   |
|-----|---------------|
| 1   | GND           |
| 2   | ESPI_RST_N    |
| 3   | ESPI_CLK      |
| 4   | ESPI_CS0_N    |
| 5   | LAD_ESPI_IO_3 |
| 6   | LAD_ESPI_IO_2 |
| 7   | LAD_ESPI_IO_1 |
| 8   | LAD_ESPI_IO_0 |
| 9   | VCC3          |
| 10  | 3VSB          |

# MITAC Industrial MB PH10CMU BIOS Set-up Menu

---

## 1. Main Page

Aptio Setup - AMI

Main Advanced Event Logs Security Boot Save & Exit

BIOS Information

|              |                     |
|--------------|---------------------|
| BIOS Vendor  | American Megatrends |
| Core Version | 5.17                |
| Compliancy   | UEFI 2.7; PI 1.6    |
| BIOS Version | D8210X10            |
| Build Date   | 09/11/2020          |

ME FW Version 14.0.39.1339

Processor Information

|   |
|---|
| Intel(R) Core(TM) i7-10700E CPU @ 2.90GHz |
| Microcode Revision CA                     |

Memory Information

|                  |                 |
|------------------|-----------------|
| Total Memory     | 16384 MB        |
| Memory Slot1     | 16384 MB (DDR4) |
| Memory Slot2     | 0 MB (DDR4)     |
| Memory Slot3     | 0 MB (DDR4)     |
| Memory Slot4     | 0 MB (DDR4)     |
| Memory Frequency | 2667 MHz        |

Serial ATA Port 1 Empty

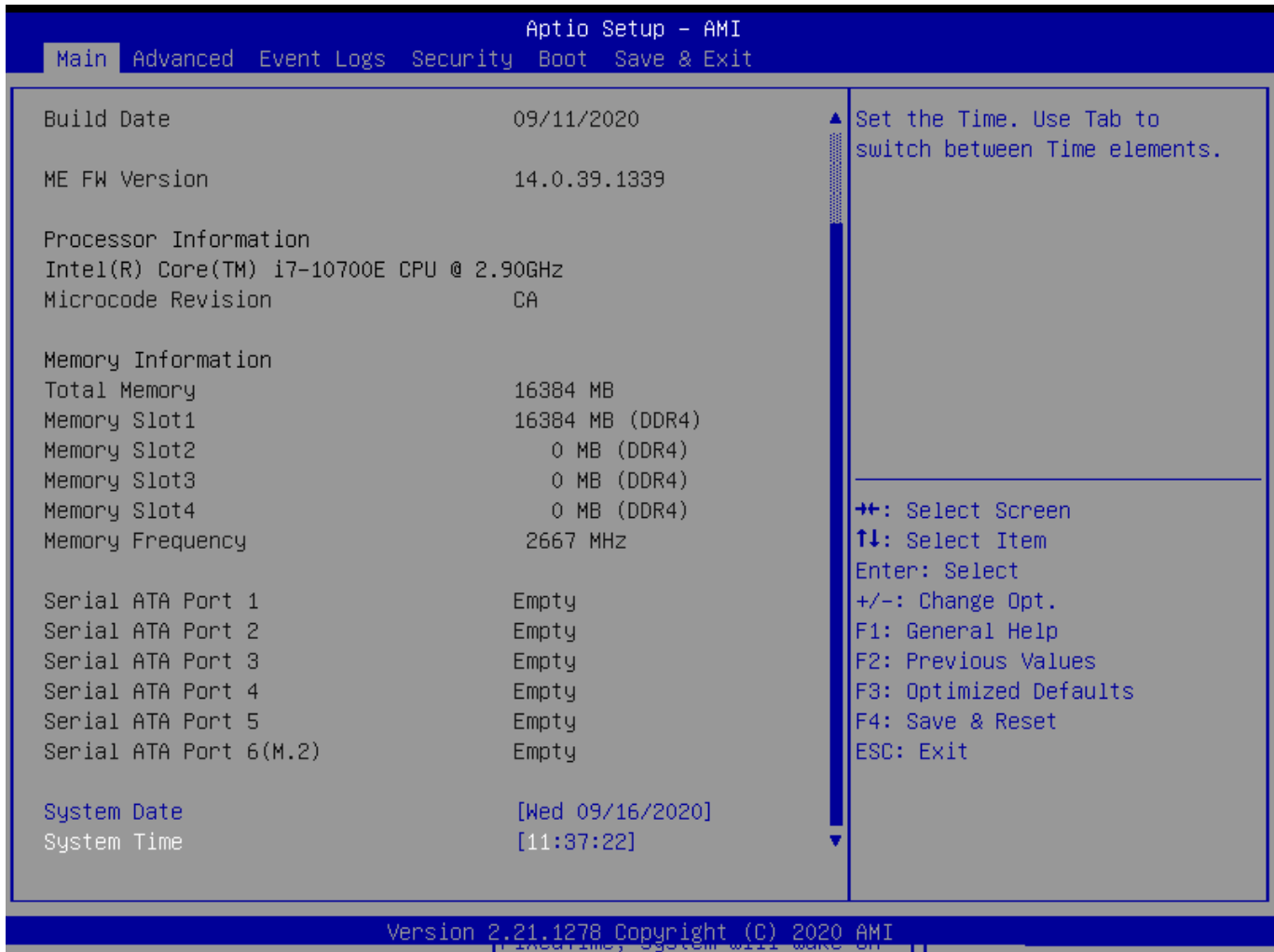
Serial ATA Port 2 Empty

Serial ATA Port 3 Empty

Serial ATA Port 4 Empty

++: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F3: Optimized Defaults  
F4: Save & Reset  
ESC: Exit

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|               |   |
|---------------|---|
| Field Name    | <b>BIOS Vender</b>  |
| Default Value | American Megatrends   |
| Comment       | This field is not selectable. There is no help text associated with it. |

|               |   |
|---------------|---|
| Field Name    | <b>Core Version</b>   |
| Default Value | 5.17  |
| Comment       | This field is not selectable. There is no help text associated with it. |

|               |   |
|---------------|---|
| Field Name    | <b>Compliance</b>   |
| Default Value | UEFI 2.7 ; PI 1.6   |
| Comment       | This field is not selectable. There is no help text associated with it. |

|               |   |
|---------------|---|
| Field Name    | <b>BIOS Version</b>   |
| Default Value | Display the version of the BIOS   |
| Comment       | This field is not selectable. There is no help text associated with it. |

|            |                   |
|------------|-------------------|
| Field Name | <b>Build Date</b> |
|------------|-------------------|

|               |   |
|---------------|---|
| Default Value | Display build date of the BIOS  |
| Comment       | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>ME FW Version</b>  |
| Value      | ME Firmware Version.  |
| Comment    | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>Processor Information</b>  |
| Value      | Display the installed CPU brand.  |
| Comment    | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>Microcode Version</b>  |
| Value      | Display the CPU microcode revision.                                     |
| Comment    | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>Total Memory</b>   |
| Value      | Display the installed memory size.                                      |
| Comment    | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>Memory Slot1</b>   |
| Value      | Display the installed memory size of slot1.                             |
| Comment    | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>Memory Slot2</b>   |
| Value      | Display the installed memory size of slot2.                             |
| Comment    | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>Memory Slot3</b>   |
| Value      | Display the installed memory size of slot3.                             |
| Comment    | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>Memory Slot4</b>   |
| Value      | Display the installed memory size of slot4.                             |
| Comment    | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>Memory Frequency</b>   |
| Value      | Display the installed memory frequency.                                 |
| Comment    | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>Serial ATA Port 1</b>  |
| Value      | Display the installed SATA device model/size of port 1.                 |
| Comment    | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>Serial ATA Port 2</b>  |
| Value      | Display the installed SATA device model/size of port 2.                 |
| Comment    | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>Serial ATA Port 3</b>  |
| Value      | Display the installed SATA device model/size of port 3.                 |
| Comment    | This field is not selectable. There is no help text associated with it. |



|            |   |
|------------|---|
| Field Name | <b>Serial ATA Port 4</b>  |
| Value      | Display the installed SATA device model/size of port 4.                 |
| Comment    | This field is not selectable. There is no help text associated with it. |

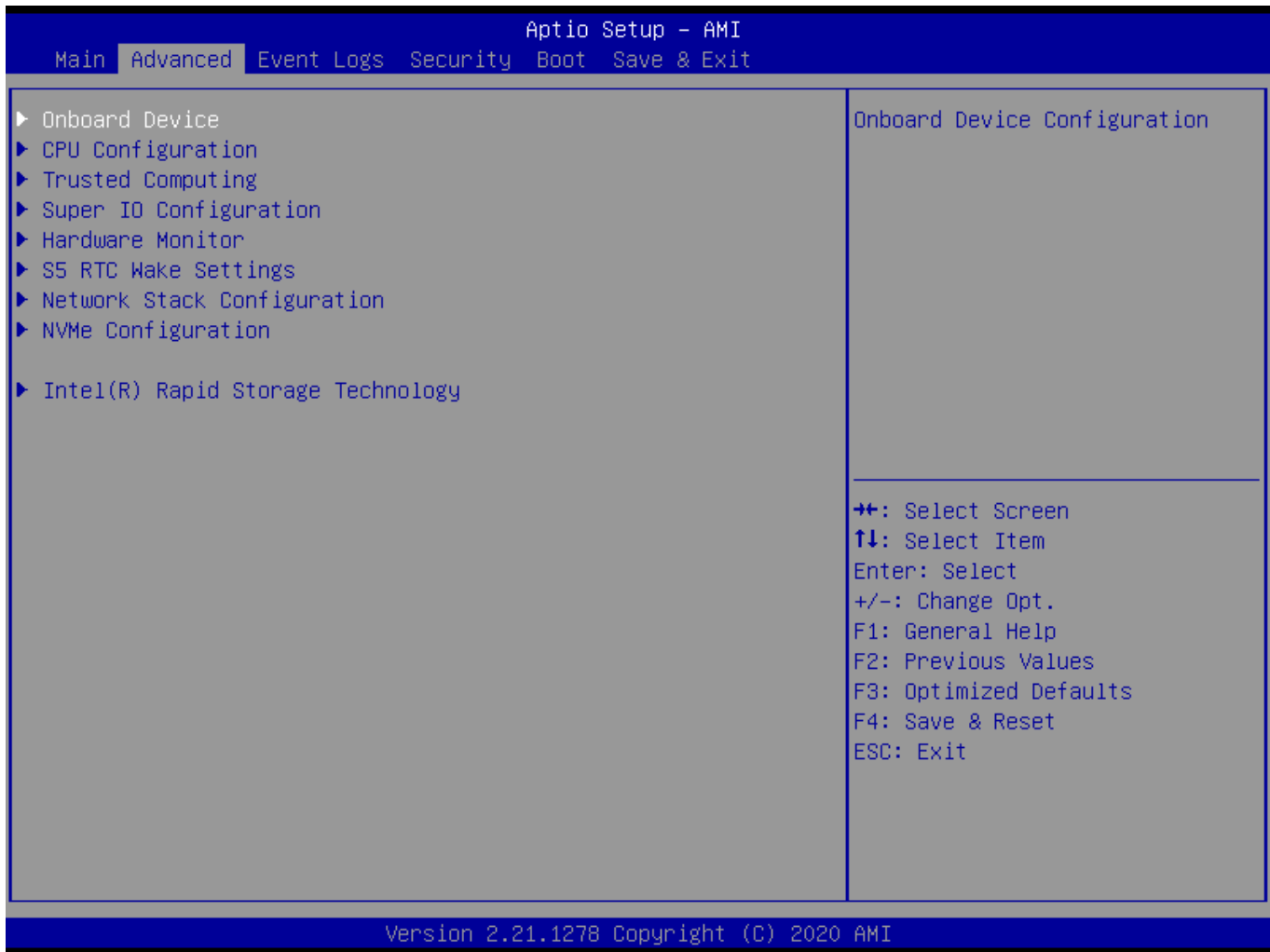
|            |   |
|------------|---|
| Field Name | <b>Serial ATA Port 5</b>  |
| Value      | Display the installed SATA device model/size of port 5.                 |
| Comment    | This field is not selectable. There is no help text associated with it. |

|            |   |
|------------|---|
| Field Name | <b>Serial ATA Port 6 (M.2)</b>  |
| Value      | Display the installed SATA device model/size of port 6.                 |
| Comment    | This field is not selectable. There is no help text associated with it. |

|                |  |
|----------------|--|
| Field Name     | <b>System Date</b>   |
| Default Value  | [Www mm/dd/yyyy]   |
| Possible Value | Www : Mon/Tue/Wed/Thu/Fri/Sat/Sun<br>mm : 1-12<br>dd : 1-31<br>yyyy : 1998-9999  |
| Help           | Set the Date. Use Tab to switch between Date elements. Default Rangers:<br>Year : 1998-9999<br>Months : 1-12<br>Days : Dependent on month Range of Years may vary. |

|                |  |
|----------------|--|
| Field Name     | <b>System Time</b>                                     |
| Default Value  | [hh :mm :ss]   |
| Possible Value | hh : 0-23<br>mm : 0-59<br>ss : 0-59                    |
| Help           | Set the Time. Use Tab to switch between Time elements. |

## 2. Advanced Page



|            |   |
|------------|---|
| Field Name | <b>Onboard Device</b>   |
| Help       | Onboard Device Configuration.                                 |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

|            |   |
|------------|---|
| Field Name | <b>CPU Configuration</b>                                      |
| Help       | CPU Configuration Parameters.                                 |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

|            |   |
|------------|---|
| Field Name | <b>Trusted Computing</b>                                      |
| Help       | Trusted Computing Settings                                    |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

|            |   |
|------------|---|
| Field Name | <b>Super IO Configuration</b>                                 |
| Help       | System Super IO Chip Parameters.                              |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

|            |                            |
|------------|----------------------------|
| Field Name | <b>NCT6116D HW Monitor</b> |
|------------|----------------------------|

|         |   |
|---------|---|
| Help    | Monitor hardware status                                       |
| Comment | Press Enter when selected to go into the associated Sub-Menu. |

|            |   |
|------------|---|
| Field Name | <b>S5 RTC Wake Settings</b>                                   |
| Help       | Enable system to wake from S5 using RTC alarm                 |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

|            |   |
|------------|---|
| Field Name | <b>Network Stack Configuration</b>                            |
| Help       | Network Stack Settings.                                       |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

|            |   |
|------------|---|
| Field Name | <b>NVMe Configuration</b>                                     |
| Help       | NVMe Device Options Settings                                  |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

|            |   |
|------------|---|
| Field Name | <b>Intel (R) Rapid Storage Technology (Suppressed if SATA Mode Selection is AHCI)</b> |
| Help       | This formset allow the user to manage RAID volumes on the Intel(R) RAID Controller.   |
| Comment    | Press Enter when selected to go into the associated Sub-Menu.                         |

## Onboard Device

Aptio Setup - AMI

Advanced

|                       |               |   |
|-----------------------|---------------|---|
| Turbo Mode            | [Enabled]     | Enable/Disable processor Turbo Mode (requires Intel Speed Step or Intel Speed Shift to be available and enabled). |
| State After G3        | [S5 State]    |   |
| DVMT Pre-Allocated    | [64M]         |   |
| DVMT Total Gfx Mem    | [256M]        |   |
| SATA Mode Selection   | [AHCI]        |   |
| Wake on LAN Enable    | [Enabled]     |   |
| HD Audio              | [Enabled]     |   |
| ME Update             | [Disabled]    |   |
| DeepSx Power Policies | [Disabled]    |   |
| Chassis Intrusion     | [Disabled]    |   |
| LCD Panel Type        | [Auto Detect] |   |

⇐⇐: Select Screen  
 ↑↓: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F2: Previous Values  
 F3: Optimized Defaults  
 F4: Save & Reset  
 ESC: Exit

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|                |   |
|----------------|---|
| Field Name     | <b>Turbo Mode</b>   |
| Default Value  | [Enabled]   |
| Possible Value | Enabled<br>Disabled   |
| Help           | Enable/Disable processor Turbo Mode (requires Intel Speed Step or Intel Speed Shift to be available and enabled). |

|                |  |
|----------------|--|
| Field Name     | <b>State After G3</b>  |
| Default Value  | [S5 State]   |
| Possible Value | S0 State<br>S5 State   |
| Help           | Specify what state to go to when power is re-applied after a power failure (G3 state). |

|                |                          |
|----------------|--------------------------|
| Field Name     | <b>DVT Pre-Allocated</b> |
| Default Value  | [64M]                    |
| Possible Value | 64M                      |

|      |  |
|------|--|
|      | 32M/F7<br>36M<br>40M<br>44M<br>48M<br>52M<br>56M<br>60M  |
| Help | Select DVMT 5.0 Pre-Allocated (Fixed) Graphics Memory size used by the Internal Graphics Device. |

|                |  |
|----------------|--|
| Field Name     | <b>DVT Total Gfx Mem</b>   |
| Default Value  | [256M]   |
| Possible Value | 128M<br>256M<br>MAX  |
| Help           | Select DVMT5.0 Total Graphic Memory size used by the Internal Graphics Device. |

|                |  |
|----------------|--|
| Field Name     | <b>SATA Mode Selection</b>                             |
| Value          | [AHCI]   |
| Possible Value | AHCI / Intel RST With Intel Optane System Acceleration |
| Help           | Determines how SATA controller(s) operate.             |

|                |   |
|----------------|---|
| Field Name     | <b>PCIe Storage Dev On Port 9 (Available when SATA Mode Selection set to "Intel RST Premium With Intel Optane System Acceleration")</b> |
| Value          | [Not RST Controlled]  |
| Possible Value | Not RST Controlled / RST Controlled   |
| Help           | Enable/Disable RST PCIe Storage Remapping.  |

|                |   |
|----------------|---|
| Field Name     | <b>Wake on LAN Enable</b>                         |
| Default Value  | [Enabled]   |
| Possible Value | Enabled<br>Disabled                               |
| Help           | Enable/Disable integrated LAN to wake the system. |

|                |  |
|----------------|--|
| Field Name     | <b>HD Audio</b>  |
| Default Value  | [Enabled]  |
| Possible Value | Enabled<br>Disabled  |
| Help           | Control Detection of the HD-Audio device.<br>Disabled = HDA will be unconditionally disabled<br>Enabled = HDA will be unconditionally enabled. |

|                |  |
|----------------|--|
| Field Name     | <b>ME Update</b>   |
| Default Value  | [Disabled]   |
| Possible Value | Enabled<br>Disabled  |
| Help           | Temporary disable Intel CSME for ME FW Update. Enabled = Intel CSME disabled after first time reboot only. |

|                |  |
|----------------|--|
| Field Name     | <b>DeepSx Power Policies</b>             |
| Default Value  | [Disabled]                               |
| Possible Value | Enabled in S4-S5<br>Disabled             |
| Help           | Configure the DeepSx Mode configuration. |

|                |                              |
|----------------|------------------------------|
| Field Name     | <b>Chassis Intrusion</b>     |
| Default Value  | [Disabled]                   |
| Possible Value | Disabled<br>Enabled<br>Reset |
| Help           | Configure Chassis Intrusion. |

|                |  |
|----------------|--|
| Field Name     | <b>LCD Panel Type</b>  |
| Default Value  | [Auto Detect]  |
| Possible Value | Auto Detect<br>800x480 eDP   |
| Help           | Select LCD panel used by Internal Graphics Device by selecting the appropriate setup item. |

## CPU Configuration

Aptio Setup - AMI

Advanced

|   |  |   |
|---|--|---|
| CPU Configuration                             |  | Enables utilization of additional hardware capabilities provided by Intel (R) Trusted Execution Technology.<br>Changes require a full power cycle to take effect.               |
| Type  | Intel(R) Core(TM) i5-10500 CPU @ 3.10GHz |   |
| ID  | 0xA0650                                  |   |
| Speed   | 3100 MHz                                 |   |
| L1 Data Cache                                 | 32 KB x 6                                |   |
| L1 Instruction Cache                          | 32 KB x 6                                |   |
| L2 Cache                                      | 256 KB x 6                               |   |
| L3 Cache                                      | 12 MB                                    |   |
| L4 Cache                                      | N/A                                      |   |
| VMX   | Supported                                |   |
| SMX/TXT                                       | Supported                                |   |
| Intel Trusted Execution Technology [Disabled] |  | ++: Select Screen<br>↑↓: Select Item<br>Enter: Select<br>+/-: Change Opt.<br>F1: General Help<br>F2: Previous Values<br>F3: Optimized Defaults<br>F4: Save & Reset<br>ESC: Exit |

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|               |   |
|---------------|---|
| Field Name    | <b>Type</b>   |
| Default Value | [Intel CPU Brand String]  |
| Comment       | This field is not selectable. There is no help text associated with it. |

|               |   |
|---------------|---|
| Field Name    | <b>ID</b>   |
| Default Value | Displays CPU Signature  |
| Comment       | This field is not selectable. There is no help text associated with it. |

|               |   |
|---------------|---|
| Field Name    | <b>Speed</b>  |
| Default Value | Displays the CPU Speed  |
| Comment       | This field is not selectable. There is no help text associated with it. |

|               |                      |
|---------------|----------------------|
| Field Name    | <b>L1 Data Cache</b> |
| Default Value | L1 Data Cache Size   |

|         |   |
|---------|---|
| Comment | This field is not selectable. There is no help text associated with it. |
|---------|---|

|               |   |
|---------------|---|
| Field Name    | <b>L1 Instruction Cache</b>   |
| Default Value | L1 Instruction Cache Size   |
| Comment       | This field is not selectable. There is no help text associated with it. |

|               |   |
|---------------|---|
| Field Name    | <b>L2 Cache</b>   |
| Default Value | L2 Cache Size   |
| Comment       | This field is not selectable. There is no help text associated with it. |

|               |   |
|---------------|---|
| Field Name    | <b>L3 Cache</b>   |
| Default Value | L3 Cache Size   |
| Comment       | This field is not selectable. There is no help text associated with it. |

|               |   |
|---------------|---|
| Field Name    | <b>L4 Cache</b>   |
| Default Value | L4 Cache Size   |
| Comment       | This field is not selectable. There is no help text associated with it. |

|               |   |
|---------------|---|
| Field Name    | <b>VMX</b>  |
| Default Value | VMX Supported or Not  |
| Comment       | This field is not selectable. There is no help text associated with it. |

|               |   |
|---------------|---|
| Field Name    | <b>SMX/TXT</b>  |
| Default Value | SMX/TXT Supported or Not  |
| Comment       | This field is not selectable. There is no help text associated with it. |

|                |   |
|----------------|---|
| Field Name     | <b>Intel Trusted Execution Technology</b>   |
| Default Value  | [Disabled]  |
| Possible Value | Enabled<br>Disabled   |
| Help           | Enables utilization of additional hardware capabilities provided by Intel (R) Trusted Execution Technology.<br>Changes require a full power cycle to take effect. |



## 2.3 Trusted Computing

Aptio Setup - AMI

Advanced

|   |   |
|---|---|
| <pre> TPM 2.0 Device Found Firmware Version:      500.14 Vendor:                INTC  Security Device Support [Enable] Pending operation      [None]                     </pre> | <p>Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.</p> <hr/> <p>                     ⇐⇐: Select Screen<br/>                     ↑↓: Select Item<br/>                     Enter: Select<br/>                     +/-: Change Opt.<br/>                     F1: General Help<br/>                     F2: Previous Values<br/>                     F3: Optimized Defaults<br/>                     F4: Save &amp; Reset<br/>                     ESC: Exit                 </p> |
|---|---|

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|               |   |
|---------------|---|
| Field Name    | <b>Firmware Version</b>   |
| Default Value | TPM module version.   |
| Comment       | This field is not selectable. There is no help text associated with it. |

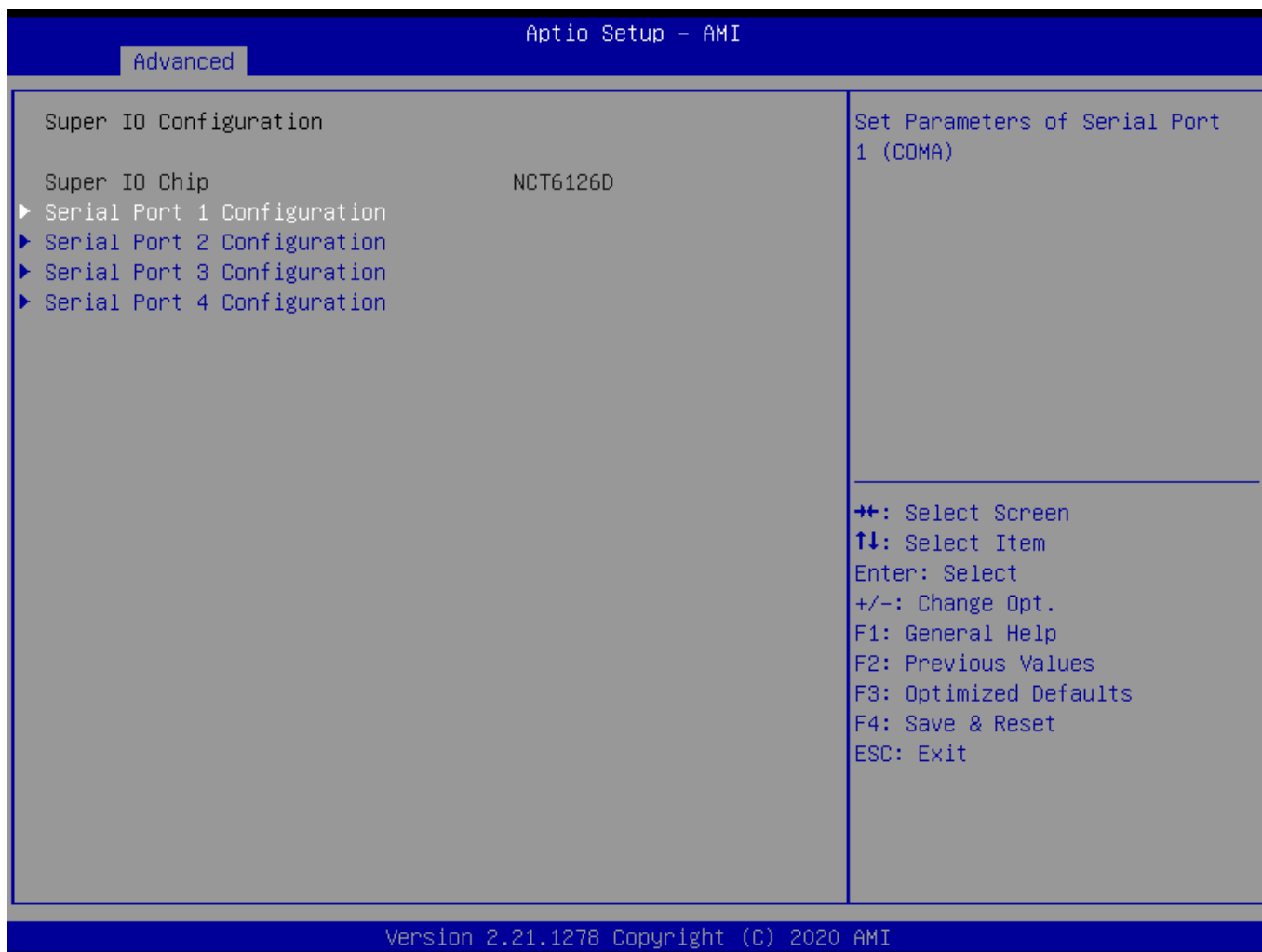
|               |   |
|---------------|---|
| Field Name    | <b>Vender</b>   |
| Default Value | TPM module vender name.   |
| Comment       | This field is not selectable. There is no help text associated with it. |

|                |   |
|----------------|---|
| Field Name     | <b>Security Device Support</b>  |
| Default Value  | [Enable]  |
| Possible Value | Enable<br>Disable   |
| Help           | Enables or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available. |

|                |  |
|----------------|--|
| Field Name     | <b>Pending operation</b>   |
| Default Value  | [None]   |
| Possible Value | None<br>TPM Clear  |
| Help           | Schedule an Operation for the Security Device. NOTE: Your Computer will reboot during restart in order to change State of Security Device. |



## Super IO Configuration



|            |   |
|------------|---|
| Field Name | <b>Serial Port 1 Configuration</b>                            |
| Help       | Set Parameters of Serial Port 1 (COMA)                        |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

|            |   |
|------------|---|
| Field Name | <b>Serial Port 2 Configuration</b>                            |
| Help       | Set Parameters of Serial Port 2 (COMB)                        |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

|            |  |
|------------|--|
| Field Name | <b>Serial Port 3 Configuration (Gray out in Q470-Entry / H420e skus)</b> |
| Help       | Set Parameters of Serial Port 3 (COMC)                                   |
| Comment    | Press Enter when selected to go into the associated Sub-Menu.            |

|            |   |
|------------|---|
| Field Name | <b>Serial Port 4 Configuration</b>                            |
| Help       | Set Parameters of Serial Port 4 (COMD)                        |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

## Serial Port 1 Configuration

| Advanced                                 |                 | Aptio Setup - AMI   |  |
|--|-----------------|---|--|
| Serial Port 1 Configuration              |                 | Enable or Disable Serial Port (COM)   |  |
| Serial Port                              | [Enabled]       |   |  |
| Device Settings                          | ID=3F8h; IRQ=4; |   |  |
| Change Settings                          | [Auto]          |   |  |
|  |                 | ++: Select Screen<br>↑↓: Select Item<br>Enter: Select<br>+/-: Change Opt.<br>F1: General Help<br>F2: Previous Values<br>F3: Optimized Defaults<br>F4: Save & Reset<br>ESC: Exit |  |
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|                |                                    |
|----------------|------------------------------------|
| Field Name     | <b>Serial Port</b>                 |
| Default Value  | [Enabled]                          |
| Possible Value | Disabled<br>Enabled                |
| Help           | Enable or Disable Serial Port(COM) |

|               |   |
|---------------|---|
| Field Name    | <b>Device Settings</b>  |
| Default Value | Device Super IO COM1 Address and IRQ.                                   |
| Comment       | This field is not selectable. There is no help text associated with it. |

|                |   |
|----------------|---|
| Field Name     | <b>Change Settings</b>  |
| Default Value  | [Auto]  |
| Possible Value | Auto<br>IO=3F8h; IRQ=4;<br>IO=3F8h; IRQ=3,4,5,6,7,9,10,11,12;<br>IO=2F8h; IRQ=3,4,5,6,7,9,10,11,12;<br>IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12;<br>IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12; |
| Help           | Select an optimal settings for Super IO Device  |

## Serial Port 2 Configuration

| Advanced                                 |                 | Aptio Setup - AMI   |  |
|--|-----------------|---|--|
| Serial Port 2 Configuration              |                 | Enable or Disable Serial Port (COM)   |  |
| Serial Port                              | [Enabled]       |   |  |
| Device Settings                          | ID=2F8h; IRQ=3; |   |  |
| Change Settings                          | [Auto]          |   |  |
|  |                 | ++: Select Screen<br>↑↓: Select Item<br>Enter: Select<br>+/-: Change Opt.<br>F1: General Help<br>F2: Previous Values<br>F3: Optimized Defaults<br>F4: Save & Reset<br>ESC: Exit |  |
| Version 2.21.1278 Copyright (C) 2020 AMI |                 |   |  |

|                |                                    |
|----------------|------------------------------------|
| Field Name     | <b>Serial Port</b>                 |
| Default Value  | [Enabled]                          |
| Possible Value | Disabled<br>Enabled                |
| Help           | Enable or Disable Serial Port(COM) |

|               |   |
|---------------|---|
| Field Name    | <b>Device Settings</b>  |
| Default Value | Device Super IO COM2 Address and IRQ.                                   |
| Comment       | This field is not selectable. There is no help text associated with it. |

|                |   |
|----------------|---|
| Field Name     | <b>Change Settings</b>  |
| Default Value  | [Auto]  |
| Possible Value | Auto<br>IO=2F8h; IRQ=3;<br>IO=3F8h; IRQ=3,4,5,6,7,9,10,11,12;<br>IO=2F8h; IRQ=3,4,5,6,7,9,10,11,12;<br>IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12;<br>IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12; |
| Help           | Select an optimal settings for Super IO Device  |

## Serial Port 3 Configuration

| Advanced                                 |                 | Aptio Setup - AMI   |  |
|--|-----------------|---|--|
| Serial Port 3 Configuration              |                 | Enable or Disable Serial Port (COM)   |  |
| Serial Port                              | [Enabled]       |   |  |
| Device Settings                          | ID=3E8h; IRQ=7; |   |  |
| Change Settings                          | [Auto]          |   |  |
| Mode Configuration                       | [3T/5R RS232]   |   |  |
|  |                 | ++: Select Screen<br>↑↓: Select Item<br>Enter: Select<br>+/-: Change Opt.<br>F1: General Help<br>F2: Previous Values<br>F3: Optimized Defaults<br>F4: Save & Reset<br>ESC: Exit |  |
| Version 2.21.1278 Copyright (C) 2020 AMI |                 |   |  |

|                |                                    |
|----------------|------------------------------------|
| Field Name     | <b>Serial Port</b>                 |
| Default Value  | [Enabled]                          |
| Possible Value | Disabled<br>Enabled                |
| Help           | Enable or Disable Serial Port(COM) |

|               |   |
|---------------|---|
| Field Name    | <b>Device Settings</b>  |
| Default Value | Device Super IO COM3 Address and IRQ.                                   |
| Comment       | This field is not selectable. There is no help text associated with it. |



|                |   |
|----------------|---|
| Field Name     | <b>Change Settings</b>  |
| Default Value  | [Auto]  |
| Possible Value | Auto<br>IO=3E8h; IRQ=7;<br>IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12;<br>IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12;<br>IO=220h; IRQ=3,4,5,6,7,9,10,11,12;<br>IO=228h; IRQ=3,4,5,6,7,9,10,11,12; |
| Help           | Select an optimal settings for Super IO Device  |

|                |   |
|----------------|---|
| Field Name     | <b>Mode Configuration</b>   |
| Default Value  | [3T/5R RS232]   |
| Possible Value | 1T/1R RS422<br>3T/5R RS232<br>1T/1R RS485 TX ENABLE Low Active<br>1T/1R RS422 with termination resistor<br>1T/1R RS485 with termination resistor TX ENABLE Low Active<br>Disabled |
| Help           | Configure serial port as RS232/RS422/RS485.   |

## Serial Port 4 Configuration

| Advanced                                 |                 | Aptio Setup - AMI  |  |
|--|-----------------|--|--|
| Serial Port 4 Configuration              |                 | Enable or Disable Serial Port (COM)  |  |
| Serial Port                              | [Enabled]       |  |  |
| Device Settings                          | IO=228h; IRQ=7; |  |  |
| Change Settings                          | [Auto]          |  |  |
|  |                 | ⇄: Select Screen<br>↑↓: Select Item<br>Enter: Select<br>+/-: Change Opt.<br>F1: General Help<br>F2: Previous Values<br>F3: Optimized Defaults<br>F4: Save & Reset<br>ESC: Exit |  |
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|                |                                    |
|----------------|------------------------------------|
| Field Name     | <b>Serial Port</b>                 |
| Default Value  | [Enabled]                          |
| Possible Value | Disabled<br>Enabled                |
| Help           | Enable or Disable Serial Port(COM) |

|               |   |
|---------------|---|
| Field Name    | <b>Device Settings</b>  |
| Default Value | Device Super IO COM4 Address and IRQ.                                   |
| Comment       | This field is not selectable. There is no help text associated with it. |

|                |   |
|----------------|---|
| Field Name     | <b>Change Settings</b>  |
| Default Value  | [Auto]  |
| Possible Value | Auto<br>IO=220h; IRQ=7;<br>IO=3E8h; IRQ=3,4,5,6,7,9,10,11,12;<br>IO=2E8h; IRQ=3,4,5,6,7,9,10,11,12;<br>IO=220h; IRQ=3,4,5,6,7,9,10,11,12; |

|      |  |
|------|--|
|      | IO=228h; IRQ=3,4,5,6,7,9,10,11,12;             |
| Help | Select an optimal settings for Super IO Device |

## Hardware Monitor

Aptio Setup - AMI

Advanced

|                               |             |  |
|-------------------------------|-------------|--|
| PC Health Status              |             | If Enabled, POST monitors voltage, temperature, and fan status. If these values are out of range, BIOS display warning message and turn on beep sound. |
| Hardware Monitor Alert Enable | [Enabled]   |  |
| System Fan Enable             | [Disabled]  |  |
| CPU Temperature               | : +46 °C    |  |
| CPU VR Temperature            | : +32 °C    |  |
| DIMM Temperature              | : +29 °C    |  |
| CPU Fan Speed                 | : 1652 RPM  |  |
| Front Fan Speed               | : N/A       |  |
| Vcore                         | : +0.896 V  |  |
| 3VSB                          | : +3.311 V  |  |
| VBAT                          | : +3.072 V  |  |
| 12V                           | : +12.288 V |  |
| VCCST                         | : +1.048 V  |  |

⇐: Select Screen  
 ↑↓: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F2: Previous Values  
 F3: Optimized Defaults  
 F4: Save & Reset  
 ESC: Exit

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| Type               | Range  |
|--------------------|--|
| CPU Temperature    | -20 ~ (By Processor Tjmax) °C  |
| CPU VR Temperature | -20 ~ 120 °C   |
| DIMM Temperature   | -20 ~ 120 °C   |
| CPU Fan Speed      | There are many kinds of the fan could be installed into the system, so we could only set 0 RPM for the failed fan speed, and there is also no high RPM limitation. |
| Front Fan Speed    |  |
| CPU Vcore          | 0~1.52V  |
| 3VSB               | 3.135~3.465V   |
| VBAT               | 2.6~3.3V   |
| 12V                | 11.4~12.6V   |
| VCCST              | 1.029~1.071V   |

|                |  |
|----------------|--|
| Field Name     | Hardware Monitor Alert Enable  |
| Default Value  | [Disabled]   |
| Possible Value | Enabled<br>Disabled  |
| Help           | If Enabled, POST monitors voltage, temperature, and fan status. If these values are out of range, BIOS display warning message and turn on beep sound. |

|                |   |
|----------------|---|
| Field Name     | System Fan Enable <b>(Suppressed if Hardware Monitor Alert is Disabled)</b>   |
| Default Value  | [Disabled]  |
| Possible Value | Enabled<br>Disabled   |
| Help           | If Enabled, POST monitors system fan status. If this values is out of range, BIOS display warning message and turn on beep sound. |

## S5 RTC Wake Settings

Aptio Setup - AMI

Advanced

|                                |   |
|--------------------------------|---|
| Wake system from S5 [Disabled] | Enable or disable System wake on alarm event. Select FixedTime, system will wake on the hr::min::sec specified.   |
|                                | ⇐+: Select Screen<br>↑↓: Select Item<br>Enter: Select<br>+/-: Change Opt.<br>F1: General Help<br>F2: Previous Values<br>F3: Optimized Defaults<br>F4: Save & Reset<br>ESC: Exit |

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|                |   |
|----------------|---|
| Field Name     | Wake system from S5   |
| Default Value  | [Disabled]  |
| Possible Value | Disabled<br>Fixed Time  |
| Help           | Enable or disable System wake on alarm event, Select FixedTime, system will wake on the hr::min::sec specified. |

|                |   |
|----------------|---|
| Field Name     | Wake up hour(Show when Wake system from S5 set to Fixed Time) |
| Default Value  | 0   |
| Possible Value | 0-23  |
| Help           | Select 0-23 For example enter 3 for 3am and 15 for 3pm        |

|                |   |
|----------------|---|
| Field Name     | Wake up minute(Show when Wake system from S5 set to Fixed Time) |
| Default Value  | 0   |
| Possible Value | 0-59  |
| Help           | Select 0 – 59 for Minute  |

|                |   |
|----------------|---|
| Field Name     | Wake up second(Show when Wake system from S5 set to Fixed Time) |
| Default Value  | 0   |
| Possible Value | 0 - 59  |
| Help           | Select 0 – 59 for Second  |

## Network Stack Configuration



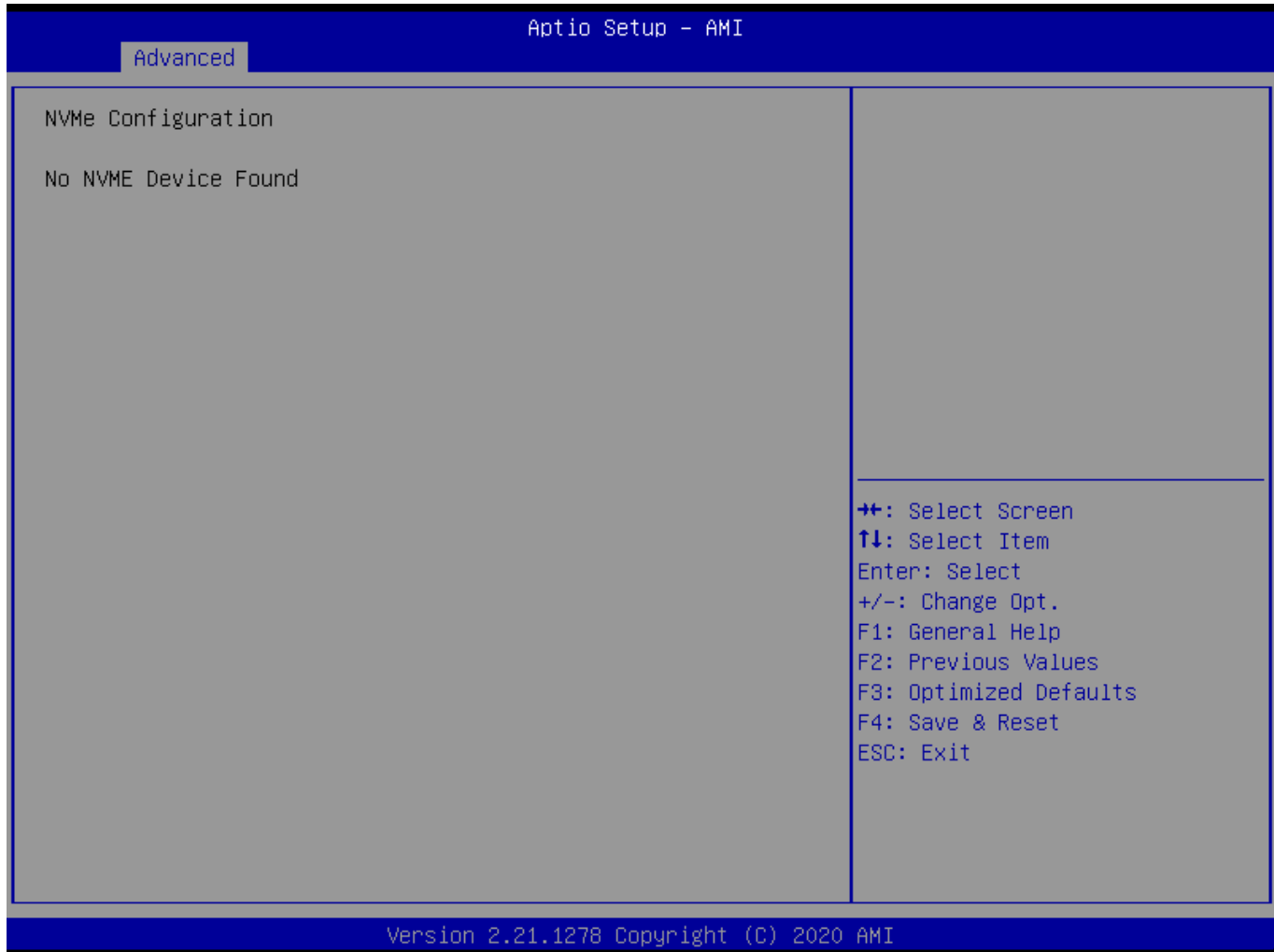
|                |                                    |
|----------------|------------------------------------|
| Field Name     | <b>Network stack</b>               |
| Default Value  | [Disabled]                         |
| Possible Value | Disabled<br>Enabled                |
| Help           | Enable/Disable UEFI Network stack. |

|                |  |
|----------------|--|
| Field Name     | <b>Ipv4 PXE Support (Available when Network stack Enabled)</b>                                 |
| Default Value  | [Disabled]   |
| Possible Value | Disabled<br>Enabled  |
| Help           | Enable/Disable Ipv4 PXE Boot Support. If disabled IPV4 PXE boot support will not be available. |



|                |  |
|----------------|--|
| Field Name     | <b>Ipv6 PXE Support (Available when Network stack Enabled)</b>                                 |
| Default Value  | [Disabled]   |
| Possible Value | Disabled<br>Enabled  |
| Help           | Enable/Disable Ipv6 PXE Boot Support. If disabled IPV6 PXE boot support will not be available. |

## NVMe Configuration



|            |   |
|------------|---|
| Field Name | <b>(Device)</b>   |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

## Intel (R) Rapid Storage Technology

Aptio Setup - AMI

Advanced

Intel(R) RST 17.8.2.4684 RAID Driver

Select to see more information about the disk

Non-RAID Physical Disks:

▶ SATA 0.0, ST2000NM0008-2F3100 ZDS07YB2, 1.8TB

---

⇐: Select Screen  
 ⇕: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F2: Previous Values  
 F3: Optimized Defaults  
 F4: Save & Reset  
 ESC: Exit

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|            |  |
|------------|--|
| Field Name | <b>Create RAID Volume</b>                    |
| Help       | This page allows you to create a RAID volume |

|            |   |
|------------|---|
| Field Name | <b>Raid Volume</b>                                    |
| Help       | Select to see more information about the RAID Volume. |

|            |  |
|------------|--|
| Field Name | <b>Non-RAID Physical Disks:</b>                |
| Help       | Select to see more information about the disk. |

---

### 3. Event Logs



|            |   |
|------------|---|
| Field Name | <b>Change Smbios Event Log Settings</b>                       |
| Help       | Press <Enter> to change the Smbios Event Log configuration.   |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

|            |   |
|------------|---|
| Field Name | <b>View Smbios Event Log</b>                                  |
| Help       | Press <Enter> to view the Smbios Event Log records.           |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

## Change Smbios Event Log Settings

Aptio Setup - AMI

Event Logs

|  |  |
|--|--|
| <p>Enabling/Disabling Options</p> <p>Smbios Event Log [Enabled]</p> <p>Erasing Settings</p> <p>Erase Event Log [No]</p> <p>When Log is Full [Do Nothing]</p> | <p>Change this to enable or disable all features of Smbios Event Logging during boot.</p> <hr/> <p>           ⇐⇐: Select Screen<br/>           ⇕⇕: Select Item<br/>           Enter: Select<br/>           +/-: Change Opt.<br/>           F1: General Help<br/>           F2: Previous Values<br/>           F3: Optimized Defaults<br/>           F4: Save &amp; Reset<br/>           ESC: Exit         </p> |
|--|--|

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|                |   |
|----------------|---|
| Field Name     | <b>Smbios Event Log</b>   |
| Default Value  | [Enabled]   |
| Possible Value | Enabled<br>Disabled   |
| Help           | Change this to enable or disable all feature of Smbios Event Logging during boot. |

|                |  |
|----------------|--|
| Field Name     | <b>Erase Event Log</b>   |
| Default Value  | [No]   |
| Possible Value | No / Yes, Next reset / Yes, Every reset  |
| Help           | Choose options for erasing Smbios Event Log. Erasing is done prior to any logging activation during reset. |

|                |                                 |
|----------------|---------------------------------|
| Field Name     | <b>Whea Log is Full</b>         |
| Default Value  | [Do Nothing]                    |
| Possible Value | Do Nothing<br>Erase Immediately |

## View Smbios Event Log

Aptio Setup - AMI

Event Logs

| DATE     | TIME     | ERROR CODE  | SEVERITY | COUNT | DESCRIPTION  |
|----------|----------|-------------|----------|-------|--|
| 06/04/20 | 06:35:10 | Smbios 0x16 | N/A      | N/A   | Log Area Reset and Count is applicable only for Multi-Events |

⇐⇐: Select Screen  
 ⇕: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F2: Previous Values  
 F3: Optimized Defaults  
 F4: Save & Reset  
 ESC: Exit

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|                |  |
|----------------|--|
| Field Name     | <b>DATE / TIME / ERROR CODE / SEVERITY / COUNT</b> |
| Default Value  | MM/DD/YY HH:MM:SS Smbios 0x16 N/A N/A              |
| Possible Value | By Events.   |
| Help           | By Events.   |

## 4. Security Page

Aptio Setup - AMI

Main Advanced Event Logs **Security** Boot Save & Exit

---

Password Description

If ONLY the Administrator's password is set, then this only limits access to Setup and is only asked for when entering Setup.

If ONLY the User's password is set, then this is a power on password and must be entered to boot or enter Setup. In Setup the User will have Administrator rights.

The password length must be in the following range:

|                |    |
|----------------|----|
| Minimum length | 3  |
| Maximum length | 20 |

Administrator Password  
User Password

HDD Security Configuration:  
P0:ST2000NM0008-2F3100

- ▶ Secure Boot
- ▶ BIOS Update

Set Administrator Password

---

⇐+: Select Screen  
 ↑↓: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F2: Previous Values  
 F3: Optimized Defaults  
 F4: Save & Reset  
 ESC: Exit

---

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|            |                               |
|------------|-------------------------------|
| Field Name | <b>Administrator Password</b> |
| Help       | Set Administrator Password    |

|            |                      |
|------------|----------------------|
| Field Name | <b>User Password</b> |
| Help       | Set User Password.   |

|            |   |
|------------|---|
| Field Name | <b>HDD Security drive</b>                                     |
| Help       | HDD Security Configuration for selected drive                 |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

|            |   |
|------------|---|
| Field Name | <b>Secure Boot</b>  |
| Help       | Secure Boot Configuration                                     |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |

|            |   |
|------------|---|
| Field Name | <b>BIOS Update</b>  |
| Help       | BIOS Update support   |
| Comment    | Press Enter when selected to go into the associated Sub-Menu. |



## HDD Security

Aptio Setup - AMI

Security

HDD Password Description :

Allows Access to Set, Modify and Clear  
Hard Disk User Password  
User Password is mandatory to Enable HDD Security.  
If the 'Set User Password' option is hidden,  
do power cycle to enable the option again.

HDD PASSWORD CONFIGURATION:

|                      |               |
|----------------------|---------------|
| Security Supported : | Yes           |
| Security Enabled :   | No            |
| Security Locked :    | No            |
| Security Frozen :    | Yes           |
| HDD User Pwd Status: | NOT INSTALLED |

⇄: Select Screen  
↑↓: Select Item  
Enter: Select  
+/-: Change Opt.  
F1: General Help  
F2: Previous Values  
F3: Optimized Defaults  
F4: Save & Reset  
ESC: Exit

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| Field Name | Set User Password   |
|------------|---|
| Help       | Set HDD User Password. *** Advisable to Power Cycle System after Setting Hard Disk Passwords ***. Discard or Save changes option in setup does not have any impact on HDD when password is set or removed. If the 'Set HDD User Password' option is hidden, do power cycle to enable the option again |

## Secure Boot

Aptio Setup - AMI

Security

|                        |                         |  |
|------------------------|-------------------------|--|
| System Mode            | Setup                   | Secure Boot feature is Active if Secure Boot is Enabled, Platform Key(PK) is enrolled and the System is in User mode. The mode change requires platform reset                  |
| Secure Boot            | [Enabled]<br>Not Active |  |
| Secure Boot Mode       | [Standard]              | ⇄: Select Screen<br>↑↓: Select Item<br>Enter: Select<br>+/-: Change Opt.<br>F1: General Help<br>F2: Previous Values<br>F3: Optimized Defaults<br>F4: Save & Reset<br>ESC: Exit |
| ▶ Restore Factory Keys |                         |  |
| ▶ Reset To Setup Mode  |                         |  |
| ▶ Key Management       |                         |  |

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|                |   |
|----------------|---|
| Field Name     | <b>Secure Boot</b>  |
| Default Value  | [Enabled]   |
| Possible Value | Enabled<br>Disabled   |
| Help           | Secure Boot feature is Active if Secure Boot is Enabled,Platform Key(PK) is enrolled and the System is in User mode.The mode change requires platform reset |

|                |   |
|----------------|---|
| Field Name     | <b>Secure Boot Mode</b>   |
| Default Value  | [Standard]  |
| Possible Value | Standard<br>Custom  |
| Help           | Secure Boot mode options:Standard or Custom.In Custom mode, Secure Boot Policy variables can be configured by a physically present user without full authentication |

|            |  |
|------------|--|
| Field Name | <b>Restore Factory Keys</b>  |
| Help       | Force System to User Mode. Install factory default Secure Boot key databases |

|            |   |
|------------|---|
| Field Name | <b>Reset to Setup Mode</b>                      |
| Help       | Delete all Secure Boot key databases from NVRAM |

|            |   |
|------------|---|
| Field Name | <b>Key Management</b>   |
| Help       | Enables expert users to modify Secure Boot Policy variables without full authentication |
| Comment    | Enables expert users to modify Secure Boot Policy variables without full authentication |

## Key Management

Aptio Setup - AMI
Security

| <p>Vendor Keys <span style="float: right;">Valid</span></p> <p>Factory Key Provision <span style="float: right;">[Disabled]</span></p> <ul style="list-style-type: none"> <li>▶ Restore Factory Keys</li> <li>▶ Reset To Setup Mode</li> <li>▶ Export Secure Boot variables</li> <li>▶ Enroll Efi Image</li> </ul> <p>Device Guard Ready</p> <ul style="list-style-type: none"> <li>▶ Remove 'UEFI CA' from DB</li> <li>▶ Restore DB defaults</li> </ul> <table style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="border-right: 1px solid black; border-bottom: 1px solid black;">Secure Boot variable</th> <th style="border-right: 1px solid black; border-bottom: 1px solid black;">Size</th> <th style="border-right: 1px solid black; border-bottom: 1px solid black;">Keys</th> <th style="border-bottom: 1px solid black;">Key Source</th> </tr> </thead> <tbody> <tr> <td style="border-right: 1px solid black;">▶ Platform Key(PK)</td> <td style="border-right: 1px solid black; text-align: center;">0</td> <td style="border-right: 1px solid black; text-align: center;">0</td> <td>No Keys</td> </tr> <tr> <td style="border-right: 1px solid black;">▶ Key Exchange Keys</td> <td style="border-right: 1px solid black; text-align: center;">0</td> <td style="border-right: 1px solid black; text-align: center;">0</td> <td>No Keys</td> </tr> <tr> <td style="border-right: 1px solid black;">▶ Authorized Signatures</td> <td style="border-right: 1px solid black; text-align: center;">0</td> <td style="border-right: 1px solid black; text-align: center;">0</td> <td>No Keys</td> </tr> <tr> <td style="border-right: 1px solid black;">▶ Forbidden Signatures</td> <td style="border-right: 1px solid black; text-align: center;">0</td> <td style="border-right: 1px solid black; text-align: center;">0</td> <td>No Keys</td> </tr> <tr> <td style="border-right: 1px solid black;">▶ Authorized TimeStamps</td> <td style="border-right: 1px solid black; text-align: center;">0</td> <td style="border-right: 1px solid black; text-align: center;">0</td> <td>No Keys</td> </tr> <tr> <td style="border-right: 1px solid black;">▶ OsRecovery Signatures</td> <td style="border-right: 1px solid black; text-align: center;">0</td> <td style="border-right: 1px solid black; text-align: center;">0</td> <td>No Keys</td> </tr> </tbody> </table> | Secure Boot variable | Size | Keys       | Key Source | ▶ Platform Key(PK) | 0 | 0 | No Keys | ▶ Key Exchange Keys | 0 | 0 | No Keys | ▶ Authorized Signatures | 0 | 0 | No Keys | ▶ Forbidden Signatures | 0 | 0 | No Keys | ▶ Authorized TimeStamps | 0 | 0 | No Keys | ▶ OsRecovery Signatures | 0 | 0 | No Keys | <p>Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode</p> <hr/> <p>             ++: Select Screen<br/>             ↑↓: Select Item<br/>             Enter: Select<br/>             +/-: Change Opt.<br/>             F1: General Help<br/>             F2: Previous Values<br/>             F3: Optimized Defaults<br/>             F4: Save &amp; Reset<br/>             ESC: Exit         </p> |
|---|----------------------|------|------------|------------|--------------------|---|---|---------|---------------------|---|---|---------|-------------------------|---|---|---------|------------------------|---|---|---------|-------------------------|---|---|---------|-------------------------|---|---|---------|---|
| Secure Boot variable  | Size                 | Keys | Key Source |            |                    |   |   |         |                     |   |   |         |                         |   |   |         |                        |   |   |         |                         |   |   |         |                         |   |   |         |   |
| ▶ Platform Key(PK)  | 0                    | 0    | No Keys    |            |                    |   |   |         |                     |   |   |         |                         |   |   |         |                        |   |   |         |                         |   |   |         |                         |   |   |         |   |
| ▶ Key Exchange Keys   | 0                    | 0    | No Keys    |            |                    |   |   |         |                     |   |   |         |                         |   |   |         |                        |   |   |         |                         |   |   |         |                         |   |   |         |   |
| ▶ Authorized Signatures   | 0                    | 0    | No Keys    |            |                    |   |   |         |                     |   |   |         |                         |   |   |         |                        |   |   |         |                         |   |   |         |                         |   |   |         |   |
| ▶ Forbidden Signatures  | 0                    | 0    | No Keys    |            |                    |   |   |         |                     |   |   |         |                         |   |   |         |                        |   |   |         |                         |   |   |         |                         |   |   |         |   |
| ▶ Authorized TimeStamps   | 0                    | 0    | No Keys    |            |                    |   |   |         |                     |   |   |         |                         |   |   |         |                        |   |   |         |                         |   |   |         |                         |   |   |         |   |
| ▶ OsRecovery Signatures   | 0                    | 0    | No Keys    |            |                    |   |   |         |                     |   |   |         |                         |   |   |         |                        |   |   |         |                         |   |   |         |                         |   |   |         |   |

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|                |   |
|----------------|---|
| Field Name     | <b>Factory Key Provision</b>  |
| Default Value  | [Disabled]  |
| Possible Value | Enabled<br>Disabled   |
| Help           | Install factory default Secure Boot keys after the platform reset and while the System is in Setup mode |

|            |  |
|------------|--|
| Field Name | <b>Restore Factory Keys</b>  |
| Help       | Force System to User Mode. Install factory default Secure Boot key databases |

|            |   |
|------------|---|
| Field Name | <b>Reset to Setup Mode</b>                      |
| Help       | Delete all Secure Boot key databases from NVRAM |

|            |   |
|------------|---|
| Field Name | <b>Export Secure Boot variables</b>   |
| Help       | Copy NVRAM content of Secure Boot variables to files in a root folder on a file system device |

|            |  |
|------------|--|
| Field Name | <b>Enroll Efi Image</b>  |
| Help       | Allow the image to run in Secure Boot mode. Enroll SHA256 Hash certificate of a PE image into Authorized Signature Database (db) |

|            |   |
|------------|---|
| Field Name | <b>Remove 'UEFI CA' from DB</b>   |
| Help       | Device Guard ready system must not list 'Microsoft UEFI CA' Certificate in Authorized Signature database (db) |

|            |   |
|------------|---|
| Field Name | <b>Restore DB defaults</b>              |
| Help       | Restore DB variable to factory defaults |

|               |   |
|---------------|---|
| Field Name    | <b>Platform Key (PK)</b>  |
| Default Value | Size:0, Keys:0, Key source: No Keys   |
| Help          | Enroll Factory Defaults or load certificates from a file:<br>1.Public Key Certificate:<br>a)EFI_SIGNATURE_LIST<br>b)EFI_CERT_X509 (DER)<br>c)EFI_CERT_RSA2048 (bin)<br>d)EFI_CERT_SHAXXX<br>2.Authenticated UEFI Variable<br>3.EFI PE/COFF Image(SHA256)<br>Key Source:<br>Factory,External,Mixed |
| comment       | Press Enter when selected to go into the associated Sub-Menu "Key Management".  |

|               |   |
|---------------|---|
| Field Name    | <b>Key Exchange Keys</b>  |
| Default Value | Size:0, Keys:0, Key source: No Keys   |
| Help          | Enroll Factory Defaults or load certificates from a file:<br>1.Public Key Certificate:<br>a)EFI_SIGNATURE_LIST<br>b)EFI_CERT_X509 (DER)<br>c)EFI_CERT_RSA2048 (bin)<br>d)EFI_CERT_SHAXXX<br>2.Authenticated UEFI Variable<br>3.EFI PE/COFF Image(SHA256)<br>Key Source:<br>Factory,External,Mixed |
| comment       | Press Enter when selected to go into the associated Sub-Menu.   |

|               |   |
|---------------|---|
| Field Name    | <b>Authorized Signatures</b>  |
| Default Value | Size:0, Keys:0, Key source: No Keys   |
| Help          | Enroll Factory Defaults or load certificates from a file:<br>1.Public Key Certificate:<br>a)EFI_SIGNATURE_LIST<br>b)EFI_CERT_X509 (DER)<br>c)EFI_CERT_RSA2048 (bin)<br>d)EFI_CERT_SHAXXX<br>2.Authenticated UEFI Variable |

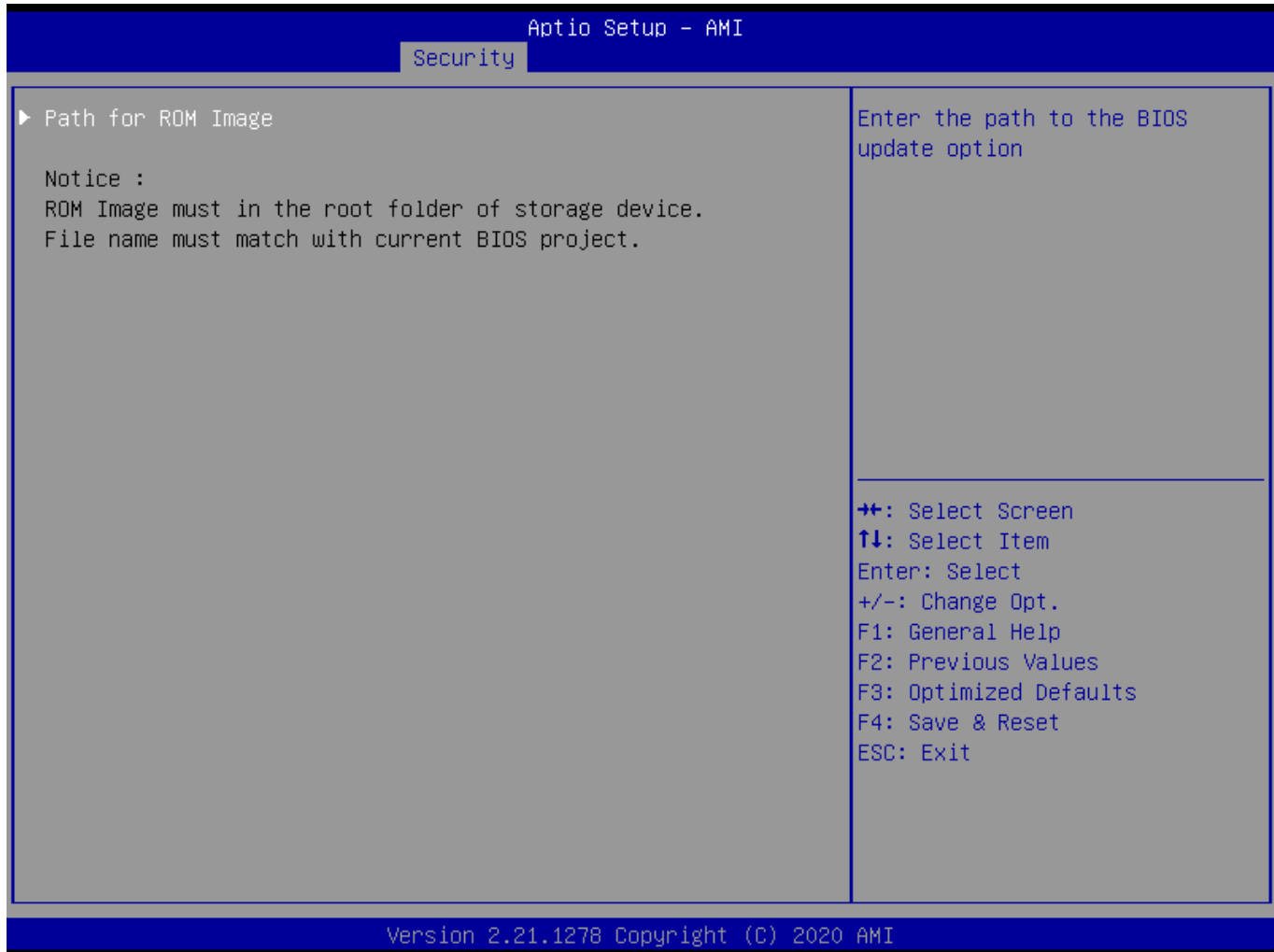
|         |  |
|---------|--|
|         | 3.EFI PE/COFF Image(SHA256)<br>Key Source:<br>Factory,External,Mixed |
| comment | Press Enter when selected to go into the associated Sub-Menu.        |

|               |   |
|---------------|---|
| Field Name    | <b>Forbidden Signatures</b>   |
| Default Value | Size:0, Keys:0, Key source: No Keys   |
| Help          | Enroll Factory Defaults or load certificates from a file:<br>1.Public Key Certificate:<br>a)EFI_SIGNATURE_LIST<br>b)EFI_CERT_X509 (DER)<br>c)EFI_CERT_RSA2048 (bin)<br>d)EFI_CERT_SHAXXX<br>2.Authenticated UEFI Variable<br>3.EFI PE/COFF Image(SHA256)<br>Key Source:<br>Factory,External,Mixed |
| comment       | Press Enter when selected to go into the associated Sub-Menu.   |

|               |   |
|---------------|---|
| Field Name    | <b>Authorized TimeStamps</b>  |
| Default Value | Size:0, Keys:0, Key source: No Keys   |
| Help          | Enroll Factory Defaults or load certificates from a file:<br>1.Public Key Certificate:<br>a)EFI_SIGNATURE_LIST<br>b)EFI_CERT_X509 (DER)<br>c)EFI_CERT_RSA2048 (bin)<br>d)EFI_CERT_SHAXXX<br>2.Authenticated UEFI Variable<br>3.EFI PE/COFF Image(SHA256)<br>Key Source:<br>Factory,External,Mixed |
| comment       | Press Enter when selected to go into the associated Sub-Menu.   |

|               |   |
|---------------|---|
| Field Name    | <b>OsRecovery Signatures</b>  |
| Default Value | Size:0, Keys:0, Key source: No Keys   |
| Help          | Enroll Factory Defaults or load certificates from a file:<br>1.Public Key Certificate:<br>a)EFI_SIGNATURE_LIST<br>b)EFI_CERT_X509 (DER)<br>c)EFI_CERT_RSA2048 (bin)<br>d)EFI_CERT_SHAXXX<br>2.Authenticated UEFI Variable<br>3.EFI PE/COFF Image(SHA256)<br>Key Source:<br>Factory,External,Mixed |
| comment       | Press Enter when selected to go into the associated Sub-Menu.   |

## BIOS Update



|            |  |
|------------|--|
| Field Name | <b>Path for ROM Image</b>                |
| Help       | Enter the path to the BIOS update option |

## 5. Boot Page

Aptio Setup - AMI

Main Advanced Event Logs Security **Boot** Save & Exit

---

Boot Configuration

Setup Prompt Timeout **1**

Bootup NumLock State [Off]

Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting.

FIXED BOOT ORDER Priorities

Boot Option #1 [USB Floppy]

Boot Option #2 [CD/DVD]

Boot Option #3 [USB CD/DVD]

Boot Option #4 [Hard Disk]

Boot Option #5 [USB Key]

Boot Option #6 [USB Hard Disk]

Boot Option #7 [NVME]

Boot Option #8 [Network]

▶ UEFI Hard Disk Drive BBS Priorities

▶ UEFI USB Key Drive BBS Priorities

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↔: Select Screen  
 ↑↓: Select Item  
 Enter: Select  
 +/-: Change Opt.  
 F1: General Help  
 F2: Previous Values  
 F3: Optimized Defaults  
 F4: Save & Reset  
 ESC: Exit

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|                |   |
|----------------|---|
| Field Name     | <b>Setup Prompt Timeout</b>   |
| Default Value  | 1   |
| Possible Value | 1~65535   |
| Help           | Number of seconds to wait for setup activation key. 65535(0xFFFF) means indefinite waiting. |

|                |                                   |
|----------------|-----------------------------------|
| Field Name     | <b>Bootup NumLock State</b>       |
| Default Value  | [Off]                             |
| Possible Value | On<br>Off                         |
| Help           | Select the keyboard NumLock state |



|                |  |
|----------------|--|
| Field Name     | <b>Boot Option #1</b>  |
| Default Value  | [USB Floppy]   |
| Possible Value | USB Floppy, CD/DVD, USB CD/DVD, Hard Disk , USB Key, USB Hard Disk , NVME, Network, Disabled |
| Help           | Sets the system boot order   |

|                |  |
|----------------|--|
| Field Name     | <b>Boot Option #2</b>  |
| Default Value  | [CD/DVD]   |
| Possible Value | USB Floppy, CD/DVD, USB CD/DVD, Hard Disk , USB Key, USB Hard Disk , NVME, Network, Disabled |
| Help           | Sets the system boot order   |

|                |  |
|----------------|--|
| Field Name     | <b>Boot Option #3</b>  |
| Default Value  | [USB CD/DVD]   |
| Possible Value | USB Floppy, CD/DVD, USB CD/DVD, Hard Disk , USB Key, USB Hard Disk , NVME, Network, Disabled |
| Help           | Sets the system boot order   |

|                |  |
|----------------|--|
| Field Name     | <b>Boot Option #4</b>  |
| Default Value  | [Hard Disk]  |
| Possible Value | USB Floppy, CD/DVD, USB CD/DVD, Hard Disk , USB Key, USB Hard Disk , NVME, Network, Disabled |
| Help           | Sets the system boot order   |

|                |  |
|----------------|--|
| Field Name     | <b>Boot Option #5</b>  |
| Default Value  | [USB Key]  |
| Possible Value | USB Floppy, CD/DVD, USB CD/DVD, Hard Disk , USB Key, USB Hard Disk , NVME, Network, Disabled |
| Help           | Sets the system boot order   |

|                |  |
|----------------|--|
| Field Name     | <b>Boot Option #6</b>  |
| Default Value  | [USB Hard Disk]  |
| Possible Value | USB Floppy, CD/DVD, USB CD/DVD, Hard Disk , USB Key, USB Hard Disk , NVME, Network, Disabled |
| Help           | Sets the system boot order   |

|                |  |
|----------------|--|
| Field Name     | <b>Boot Option #7</b>  |
| Default Value  | [NVME]   |
| Possible Value | USB Floppy, CD/DVD, USB CD/DVD, Hard Disk , USB Key, USB Hard Disk , NVME, Network, Disabled |
| Help           | Sets the system boot order   |

|                |  |
|----------------|--|
| Field Name     | <b>Boot Option #8</b>  |
| Default Value  | [Network]  |
| Possible Value | USB Floppy, CD/DVD, USB CD/DVD, Hard Disk , USB Key, USB Hard Disk , NVME, Network, Disabled |
| Help           | Sets the system boot order   |

|            |  |
|------------|--|
| Field Name | <b>UEFI USB Floppy Drive BBS Priorities</b>  |
| Help       | Specifies the Boot Device Priority sequence from available UEFI USB Floppy Drives. |
| Comment    | Press Enter when selected to go into the associated Sub-Menu.                      |

|            |   |
|------------|---|
| Field Name | <b>UEFI CDROM/DVD ROM Drive BBS Priorities</b>                                    |
| Help       | Specifies the Boot Device Priority sequence from available UEFI CDROM/DVD Drives. |
| Comment    | Press Enter when selected to go into the associated Sub-Menu.                     |

|            |   |
|------------|---|
| Field Name | <b>UEFI USB CDROM/DVD ROM Drive BBS Priorities</b>                                    |
| Help       | Specifies the Boot Device Priority sequence from available UEFI USB CDROM/DVD Drives. |
| Comment    | Press Enter when selected to go into the associated Sub-Menu.                         |

|            |   |
|------------|---|
| Field Name | <b>UEFI Hard Disk Drive BBS Priorities</b>  |
| Help       | Specifies the Boot Device Priority sequence from available UEFI Hard Disk Drives. |
| Comment    | Press Enter when selected to go into the associated Sub-Menu.                     |

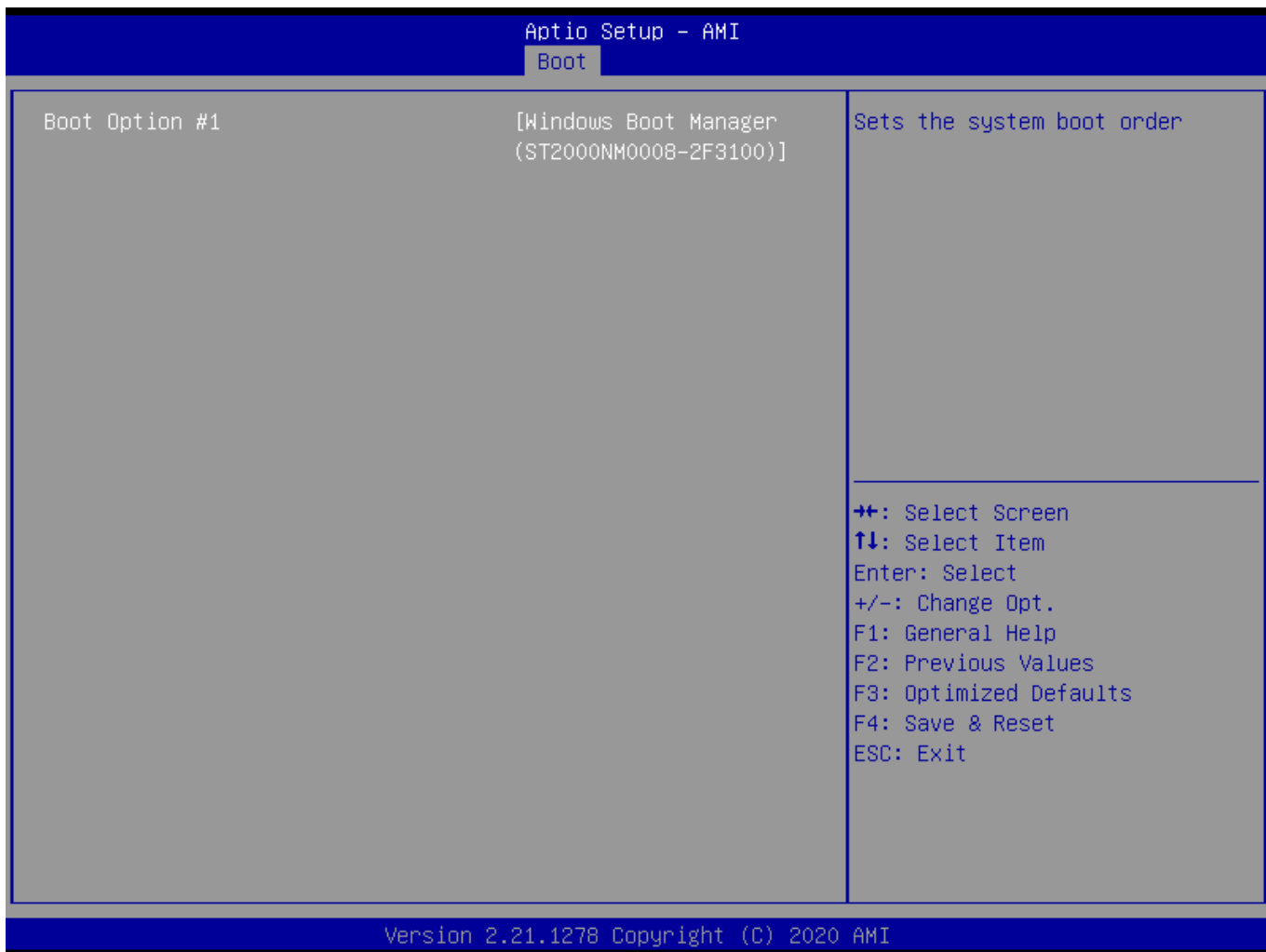
|            |   |
|------------|---|
| Field Name | <b>UEFI USB KEY Drive BBS Priorities</b>  |
| Help       | Specifies the Boot Device Priority sequence from available UEFI USB Key Drives. |
| Comment    | Press Enter when selected to go into the associated Sub-Menu.                   |

|            |   |
|------------|---|
| Field Name | <b>UEFI USB Hard Disk Drive BBS Priorities</b>  |
| Help       | Specifies the Boot Device Priority sequence from available UEFI USB Hard Disk Drives. |
| Comment    | Press Enter when selected to go into the associated Sub-Menu.                         |

|            |  |
|------------|--|
| Field Name | <b>UEFI NVME Drive BBS Priorities</b>  |
| Help       | Specifies the Boot Device Priority sequence from available UEFI NVME Drives. |
| Comment    | Press Enter when selected to go into the associated Sub-Menu.                |

|            |   |
|------------|---|
| Field Name | <b>UEFI NETWORK Drive BBS Priorities</b>  |
| Help       | Specifies the Boot Device Priority sequence from available UEFI NETWORK Drives. |
| Comment    | Press Enter when selected to go into the associated Sub-Menu.                   |

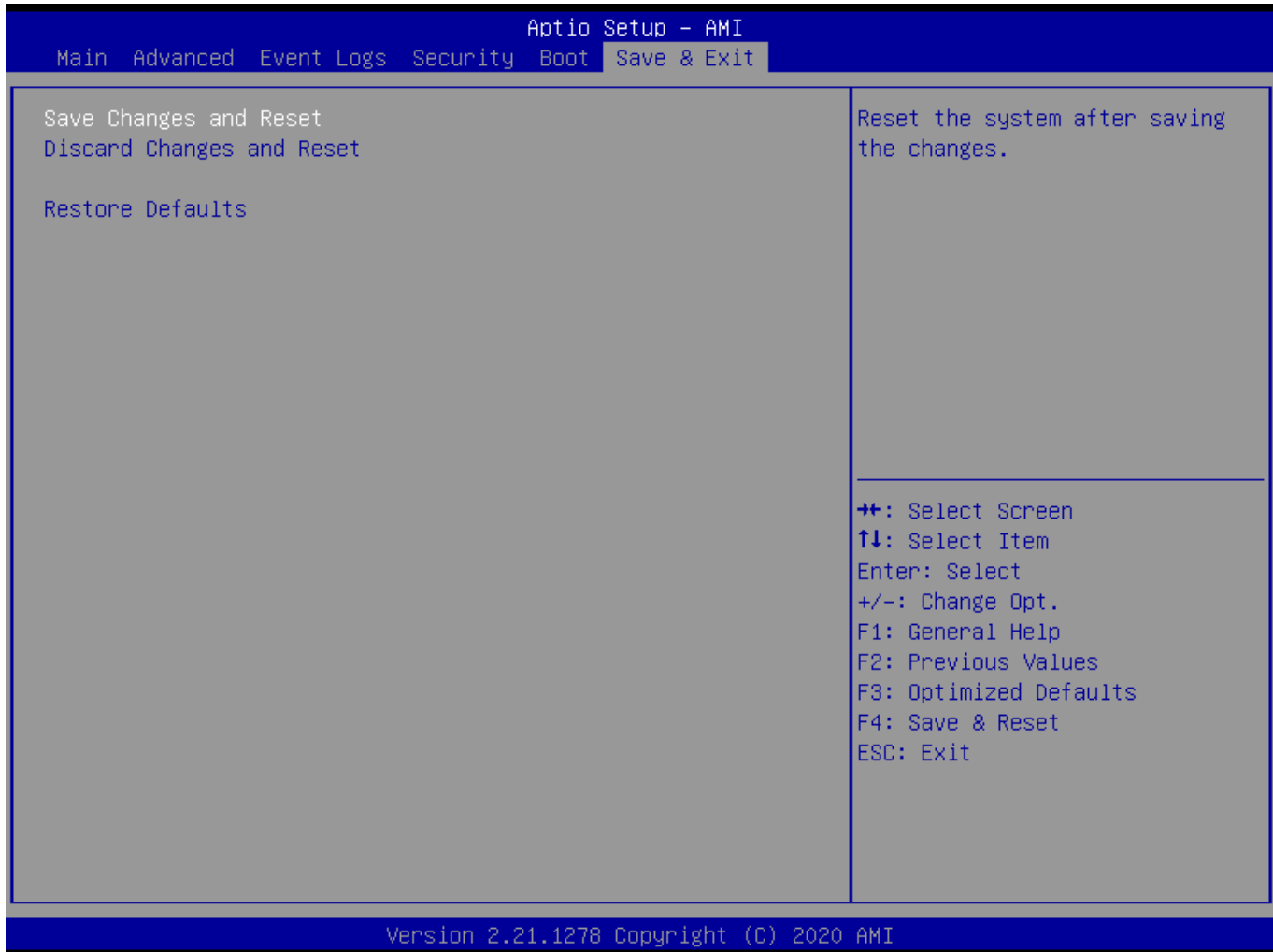
## (List Boot Device Type) Drive BBS Priorities



|                |  |
|----------------|--|
| Field Name     | <b>Boot Option #1</b>                    |
| Default Value  |  |
| Possible Value | Boot Device Name 1 of this type, Disable |
| Help           | Sets the system boot order               |

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## 6. Save & Exit Page



|            |  |
|------------|--|
| Field Name | <b>Save Changes and Reset</b>              |
| Help       | Reset the system after saving the changes. |

|            |  |
|------------|--|
| Field Name | <b>Discard Changes and Rest</b>                |
| Help       | Reset system setup without saving any changes. |

|            |  |
|------------|--|
| Field Name | <b>Restore Defaults</b>                                |
| Help       | Restore/Load Default values for all the setup options. |