

# Cervoz Industrial Embedded Module

M.2 2280 NVMe

Titan Series (3D NAND)

T420 Family

*Product Datasheet*





## Revision History

Date	Revision	Description
2019.03.15	1.0	First Released



---

## Table of Contents

<b>1. Product Overview.....</b>	<b>4</b>
1.1 Introduction.....	4
1.2 Feature.....	4
1.3 Product Appearance & Models.....	5
<b>2. Product Specifications.....</b>	<b>6</b>
2.1 General Specifications.....	6
2.2 Performance.....	6
2.3 Electronic Specifications.....	7
2.3.1 Block Diagram.....	7
2.3.2 Pin Assignment.....	7-9
2.4 Environmental Specifications.....	10
2.5 Mechanical Specifications.....	10
<b>3. Supported Command.....</b>	<b>11</b>
3.1 List of Command Sets.....	11
<b>4. Part No. Decoder.....</b>	<b>12</b>
4.1 Part No. Decoder.....	12~13

## 1. Product Overview

### 1.1 Introduction

Cervoz Industrial M.2 2280 Embedded Module T420 family is a Solid State Flash Disk product that is in compliance with the M.2 and PCIe Gen3x2 standards. M.2 2280 T420 family fits in any M.2 2280(B+M or M) sockets in a PC or motherboard; it can be used for both booting and storage purposes.

T420 family uses SSD grade quality 3D NAND flash memory from the industry leading manufacturer Toshiba. Cervoz's firmware builds in a powerful ECC algorithm call Low-Density Parity Check (LDPC) decoding to improve data reliability. This product includes various capacities to choose from.

T420 family offers outstanding performance and reliability; the product family is a perfect solution for multi-tasking capabilities and general industrial applications.

### 1.2 Feature

- Compliant with PCIe Gen3x2 interface (backward compatible to PCIe Gen 2 and Gen 1)
- Compliance:
  - NVMe 1.3
  - PCI Express Base 3.1
- 3D NAND flash memory
- Capacity: 128GB ~ 512GB
- With DDR3 DRAM Buffer
- End-to-End data protection
- SLC write cache technology
- Thermal throttling
- Operating as boot disk
- Product includes Standard Temperature range
- Static and dynamic wear leveling
- Bad block management
- S.M.A.R.T. & TRIM command
- Power Saving Modes:
  - Support APST
  - Support ASPM
  - Support L1.2
- Fully compatible with Windows 8.1, Windows 10, Mac OS, and Linux\*
- RoHS compliant

\*Microsoft recommends when use the M.2 NVMe SSD the driver for windows 8.1 and above is required. Mac and Linux system users need to make sure whether there is a driver support M.2 NVMe SSD or not.

### 1.3 Product Appearance & Models

Cervoz Industrial M.2 2280 Module T420



T420 Family Standard Temp. (0°C ~ 70°C) Model No.	Capacity
CIE-M8T420TLF128GS	128GB
CIE-M8T420TLF256GS	256GB
CIE-M8T420TMF512GS	512GB

**Please Note:**

Since certain storage capacity has to be reserved for firmware and controller management purposes; the physical capacity of the SATA flash module will be approximately 92.96% of the indicated capacity. If you need to install an image that has the exact (or close to) the indicated size of the flash module, please choose your flash module with a greater capacity.

## 2. Product Specifications

### 2.1 General Specifications

<b>Form Factor</b>	M.2 PCIe 2280
<b>Interface</b>	PCIe Gen3x2 (backward compatible to PCIe Gen 2 and Gen 1)
<b>Connector</b>	M.2 2280 (B+M)
<b>NAND Flash Type</b>	3D NAND
<b>Capacity</b>	128GB/256GB/512GB
<b>Sequential Read</b>	up to 1680MB/s
<b>Sequential Write</b>	up to 1065MB/s
<b>DDR3 DRAM Buffer</b>	Included
<b>ECC Scheme</b>	Applies the LDPC (Low Density Parity Check) of ECC algorithm
<b>MTBF</b>	2,000,000 hours
<b>TeraByte Written (TBW)</b>	128GB : 125 256GB : 250 512GB : 500
<b>Supply Voltage</b>	+3.3V DC +/-5%
<b>Power Consumption</b>	Active mode: < 3455mW Idle mode: < 460mW
<b>Dimension (LxWxH)</b>	80.00*22.00*3.90mm

### 2.2 Performance

The performance was measured with below PC configuration:

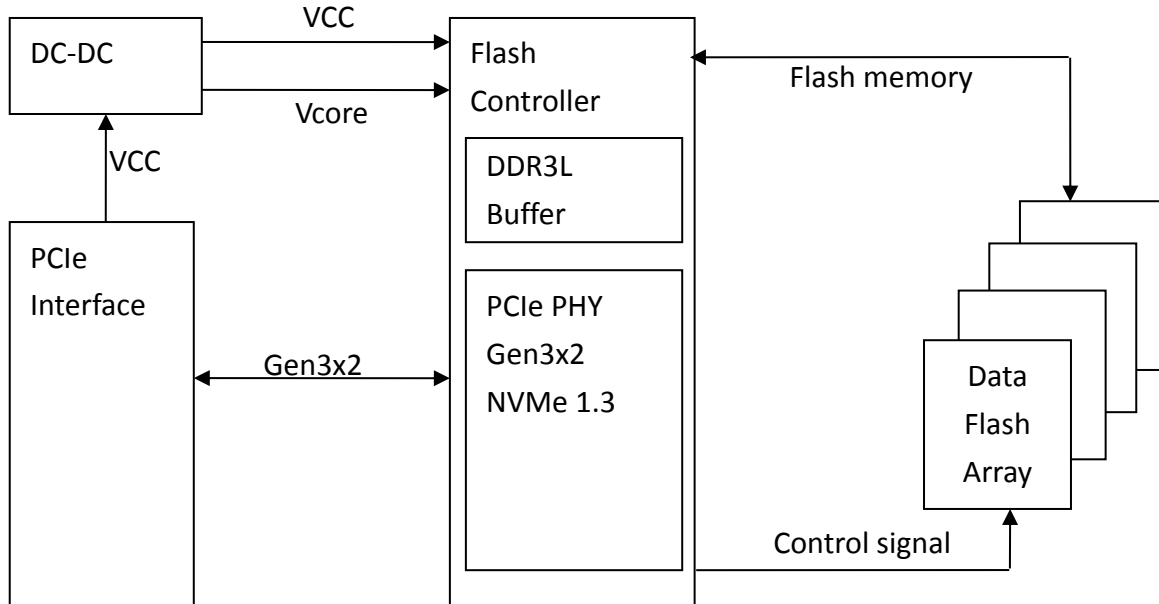
- Platform: GIGABYTE Z270M-DH3
- RAM: Cervoz CIR-S4DUS2404G (DDR4 4G 2400MHz)
- Operation Systems: Win10 64bit
- Testing Utility: Crystal Disk Mark v5.1.0 X64
- PCIe Gen3 performance

<b>Capacity</b>	128GB	256GB	512GB
<b>Sequential Read (Q32T1)</b>	1510MB/s	1675MB/s	1680MB/s
<b>Sequential Write (Q32T1)</b>	500MB/s	970MB/s	1065MB/s
<b>4KB Random Read (Q32T1)</b>	355MB/s	550MB/s	545MB/s
<b>4KB Random Write (Q32T1)</b>	495MB/s	515MB/s	525MB/s

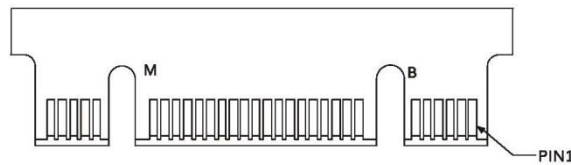
Actual performance might differ based on different using conditions and environment.

## 2.3 Electronic Specifications

### 2.3.1 Block Diagram



### 2.3.2 Pin Assignment



Pin #	Pin Description	Signal Name	Pin #	Pin Description	Signal Name
1	Ground	GND	10	Status indicators via LED devices	LED1#(O)
2	3.3V power in	+3.3V	11	Not Used	NC
3	Ground	GND	12	Module Key	Key
4	3.3V power in	+3.3V	13	Module Key	Key
5	Not Used	NC	14	Module Key	Key
6	Not Used	NC	15	Module Key	Key
7	Not Used	NC	16	Module Key	Key
8	Not Used	NC	17	Module Key	Key
9	Not Used	NC	18	Module Key	Key

Pin #	Pin Description	Signal Name	Pin #	Pin Description	Signal Name
19	Module Key	Key	36	Not Used	NC
20	Not Used	NC	37	PCIe RX Differential signal defined by the PCI Express M.2 spec	PERp1
21	Ground	GND	38	Not Used	NC
22	Not Used	NC	39	Ground	GND
23	Not Used	NC	40	SMBus Clock; Open Drain with pull-up on platform	SMB_CLK (I/O)(0/1.8V)
24	Not Used	NC	41	PCIe TX Differential signal defined by the PCI Express M.2 spec	PETn0
25	Not Used	NC	42	SMBus Clock; Open Drain with pull-up on platform	SMB_CLK (I/O)(0/1.8V)
26	Not Used	NC	43	PCIe TX Differential signal defined by the PCI Express M.2 spec	PETp0
27	Ground	GND	44	Alert notification to master; Open Drain with pull-up on platform; Active low.	ALERT#(O)(0/1.8V)
28	Not Used	NC	45	Ground	GND
29	PCIe TX Differential signal defined by the PCI Express M.2 spec	PETn1	46	Not Used	NC
30	Not Used	NC	47	PCIe RX Differential signal defined by the PCI Express M.2 spec	PERn0
31	PCIe TX Differential signal defined by the PCI Express M.2 spec	PETp1	48	Not Used	NC
32	Not Used	NC	49	PCIe RX Differential signal defined by the PCI Express M.2 spec	PERp0
33	Ground	GND	50	PE-Reset is a functional reset to the card as defined by the PCIe Mini CEM specification.	PERST#(I)(0/3.3V)
34	Not Used	NC	51	Ground	GND
35	PCIe RX Differential signal defined by the PCI Express M.2 spec	PERn1	52	Clock Request is a reference clock request signal as defined by the PCIe Mini CEM specification; Also used by L1 PM Substates.	CLKREQ#(I/O)(0/3.3V)





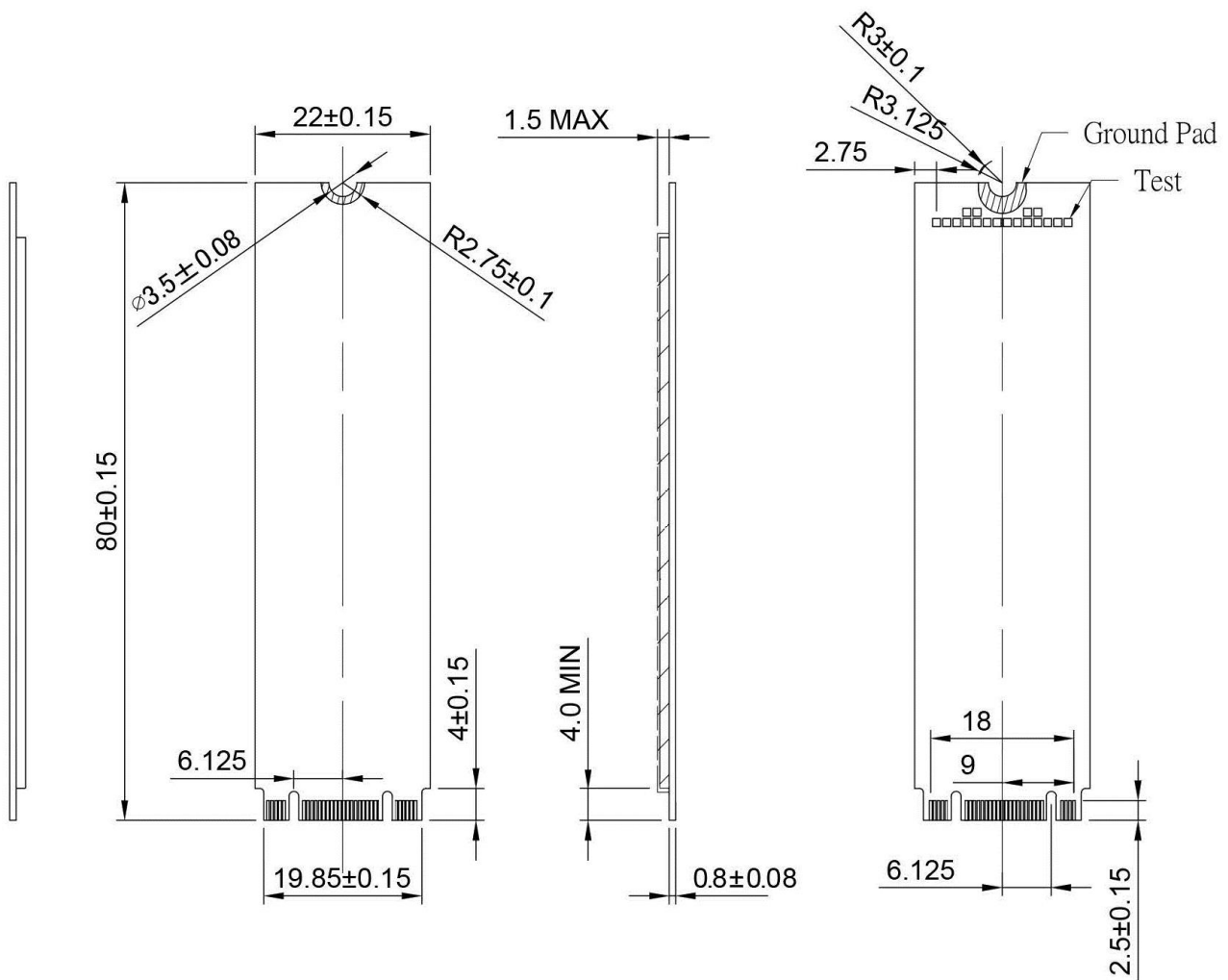
Pin #	Pin Description	Signal Name	Pin #	Pin Description	Signal Name
53	PCIe Reference Clock signals (100 MHz) defined by the PCI Express M.2 spec.	REFCLKn	65	Module Key	Key
54	PCIe PME Wake. Open Drain with pull up on platform; Active Low.	PEWAKE#(I/O)(0/3.3V)	66	Module Key	Key
55	PCIe Reference Clock signals (100 MHz) defined by the PCI Express M.2 spec.	REFCLKp	67	Not Used	NC
56	Manufacturing Data line. Used for SSD manufacturing only. Not used in normal operation. Pins should be left N/C in platform Socket.	Reserved for MFG DATA	68	32.768 kHz clock supply input that is provided by the platform chipset to reduce power and cost for the module.	SUSCLK(32K Hz) (I)(0/3.3V)
57	Ground	GND	69	Not Used	NC
58	Manufacturing Data line. Used for SSD manufacturing only. Not used in normal operation. Pins should be left N/C in platform Socket.	Reserved for MFG CLOCK	70	3.3V power in	+3.3V
59	Module Key	Key	71	Ground	GND
60	Module Key	Key	72	3.3V power in	+3.3V
61	Module Key	Key	73	Ground	GND
62	Module Key	Key	74	3.3V power in	+3.3V
63	Module Key	Key	75	Not Used	GND
64	Module Key	Key			

## 2.4 Environmental Specifications

Type		Value
Temperature	Standard Temperature Operating:	0°C~70°C
	Standard Temperature Storage:	-40°C~85°C
Humidity	Operating & Storage	10~95%, Non-Condensing
Vibration	Non-Operating	20G, 10Hz~2000Hz
Shock	Non-Operating	1500G, 0.5ms

## 2.5 Mechanical Specifications

Type	Value
Form Factor	M.2 2280 (B+M)
Length	80.00mm +/-0.15mm
Width	22.00mm +/-0.15mm
Thickness	2.30mm +/-0.08mm



### 3. Supported Command

#### 3.1 List of Command Sets

Admin Commands	
Code	Description
00h	Delete I/O Submission Queue
01h	Create I/O Submission Queue
02h	Get Log Page
04h	Delete I/O Completion Queue
05h	Create I/O Completion Queue
06h	Identify
08h	Abort
09h	Set Features
0Ah	Get Features
0Ch	Asynchronous Event Request
10h	Firmware Activate
11h	Firmware Image Download
Admin Commands – NVM Command Set Specific	
Code	Description
80h	Format NVM
81h	Security Send
82h	Security Receive
NVM Commands	
Code	Description
00h	Flush
01h	Write
02h	Read
04h	Write Uncorrectable
08h	Write Zeroes
09h	Dataset Management

## 4. Part No. Decoder

### 4.1 Part No. Decoder

1	-	2	3	4	5	6	7	8	9
Product Line	-	Form Factor	Product Series	Cervoz Family Code (Bus / Internal Control)	Flash Brand	Flash Capacity	Flash Mode	Module Capacity	Operating Temp.
XXX	-	XX	X	XXX	X	X	X	XXXX	X

### 1. Product Line

CIS	Cervoz Industrial SSD
CIM	Cervoz Industrial Memory Card
CIE	Cervoz Industrial Embedded Module

### 2. Form Factor

2S	2.5" SATA
2P	2.5" PATA
CF	CompactFlash
CA	CFast
MS	mSATA
HM	Half Size mSATA
HS	Half Slim
M4	M.2 2242
M6	M.2 2260
M8	M.2 2280
0V	PATA Disk 40pin Vertical
4V	PATA Disk 44pin Vertical
4L	PATA Disk 44pin Horizontal Left
7T	SATA Disk 7pin Vertical Tall
7S	SATA Disk 7pin Vertical Short
7L	SATA Disk 7pin Horizontal Left
7R	SATA Disk 7pin Horizontal Right

### 3. Product Series

S	Supreme Series (SLC)
R	Reliance Series (RO-MLC)
M	Momentum Series (MLC)
T	Titan Series (TLC)

### 4. Cervoz Family Code

Bus and Internal Control for Cervoz Product Families

### 5. Flash Brand

M	Micron
T	Toshiba



### 6. Flash Capacity

A	256Mb
B	512Mb
C	1Gb
D	2Gb
E	4Gb
F	8Gb
G	16Gb
H	32Gb
I	64Gb
J	128Gb
K	256Gb
L	512Gb
M	1Tb
N	2Tb
O	4Tb

### 7. Flash Mode

Internal Control for Flash Mode

### 8. Module Capacity

128M	128MB
256M	256MB
512M	512MB
001G	1GB
002G	2GB
004G	4GB
008G	8GB
016G	16GB
032G	32GB
064G	64GB
128G	128GB
256G	256GB
512G	512GB
001T	1TB

### 9. Operating Temperature

S	Standard Grade (0~ +70°C)
W	Wide Temperature Grade (-40 ~ +85°C)