

## CIR-W3SULII1602G (5FH6C) - (88)

DDR3 WIDE TEMP. SO-DIMM LOW VOLTAGE 1600MHz 2GB

Description	Specifications	
<p>The CIR-W3SULII1602G is 256M words X 64 bits, 1 rank. Unbuffered Small Outline Dual In-Line Memory Module (SO-DIMM). DDR3L SDRAMs in Fine Ball Grid Array (FBGA) packages on a 204pin glass-epoxy substrate. Provide a high performance 8 byte interface in 67.60mm width form factor of industry standard. It is suitable for easy interchange and addition.</p>	Density	2GB
	Pin Count	204pin
	Type	Unbuffered
	Dimensions	67.6mm x 30.0mm
	ECC	Non-ECC
	Component Config	256M x 8 bit
	Data Rate	1600 MHz
	CAS Latency	11
	Voltage	1.35V / 1.5V
	PCB Layers	8
	Operating Temp.(TCASE)	-40°C~+95°C
	Module Ranks	Single Rank

### Features

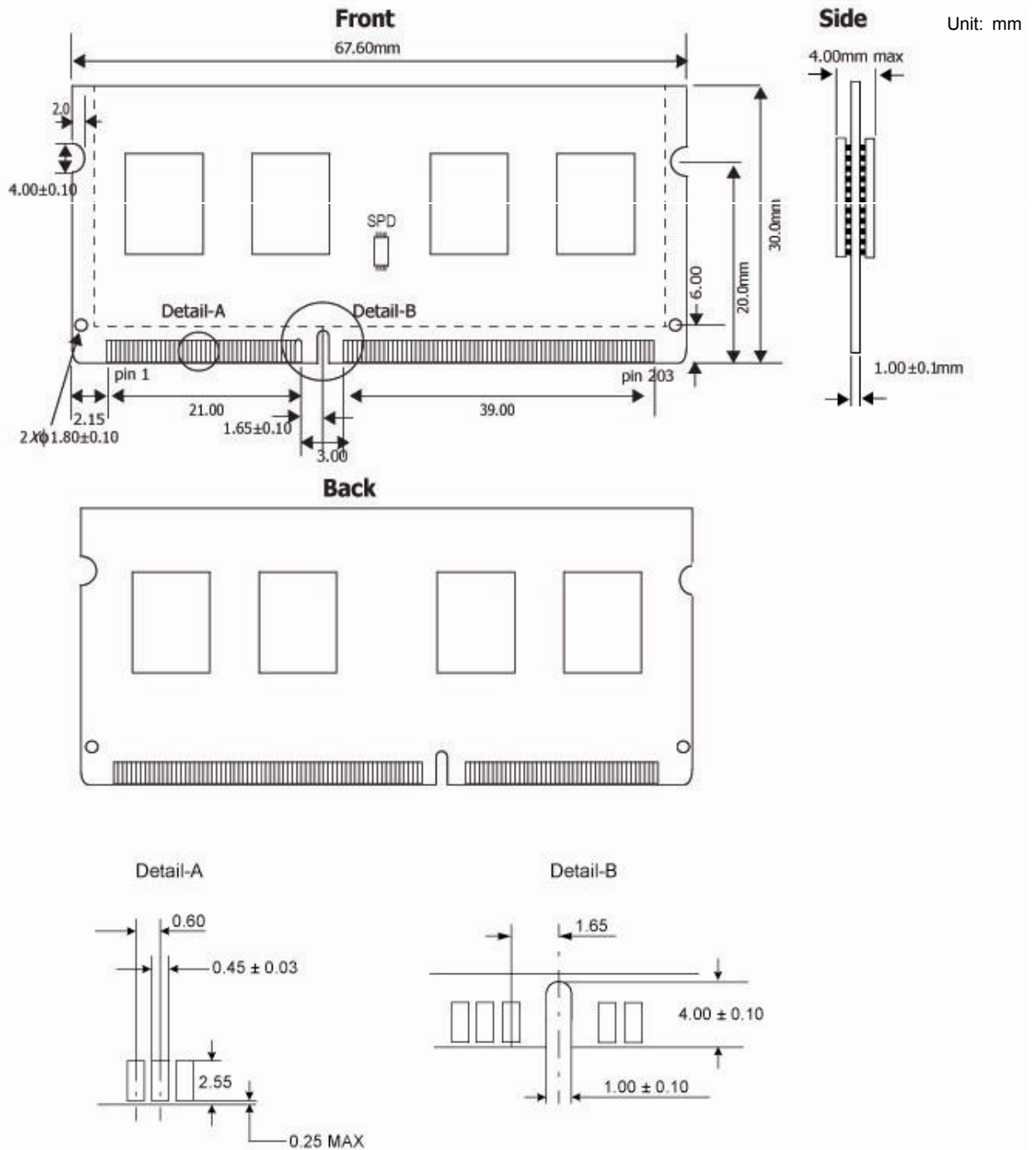
- Data rate: 1600MHz
- RoHS compliant products.
- 204pin, Small outline dual in-line memory module (SO-DIMM)
- VDD= 1.35V (1.28V~1.45V) & VDDQ=1.5V(1.425V~1.575V)
- Interface: SSTL\_15
- Programmable CAS Latency (CL): 6,7,8,9,10,11 support
- Fully differential clock inputs (CK, /CK) operation
- Differential Data Strobe (DQS, /DQS)
- Serial presence detect with EEPROM
- 8 independent internal bank
- 8K refresh cycles /64ms
- On Die Termination supported
- Asynchronous RESET pin supported
- ZQ calibration supported
- POSTED CAS additive latency (AL)
- Bi-directional Differential Data Strobe
- Burst Length: 4, 8
- 8 bit pre-fetch



**Speed Grade**

Frequency Grade	Data Transfer Rate	CAS Latency Support						CL-tRCD-tRP
		CL6	CL7	CL8	CL9	CL10	CL11	
DDR3-1600	PC3-12800	800	1066	1066	1333	1333	1600	11-11-11

**Package Dimensions**



Tolerances : ± 0.15mm unless otherwise specified