CIR-W3DUIK1608G

DDR3 WIDE TEMP. DIMM 1600MHz 8GB

Description

The CIR-W3DUIK1608G is 1024M words X 64 bits, 2 ranks. Unbuffered Dual In-Line Memory Module (DIMM). DDR3 SDRAMs in Fine Ball Grid Array(FBGA) packages on a 240pin glass-epoxy substrate. Provide a high performance 8 byte interface in 133.35mm width form factor of industry standard. It is suitable for easy interchange and addition.

Specifications

Density	8GB
Pin Count	240pin
Type	Unbuffered
Dimensions	133.35mm x 30.0mm
ECC	Non-ECC
Component Config	512M x 8 bit
Data Rate	1600 Mbps
CAS Latency	11
Voltage:	1.5V
PCB Layers	6
Operating Temp.(TCASE)	-40°C~+95°C
Module Ranks	Dual Rank

Features

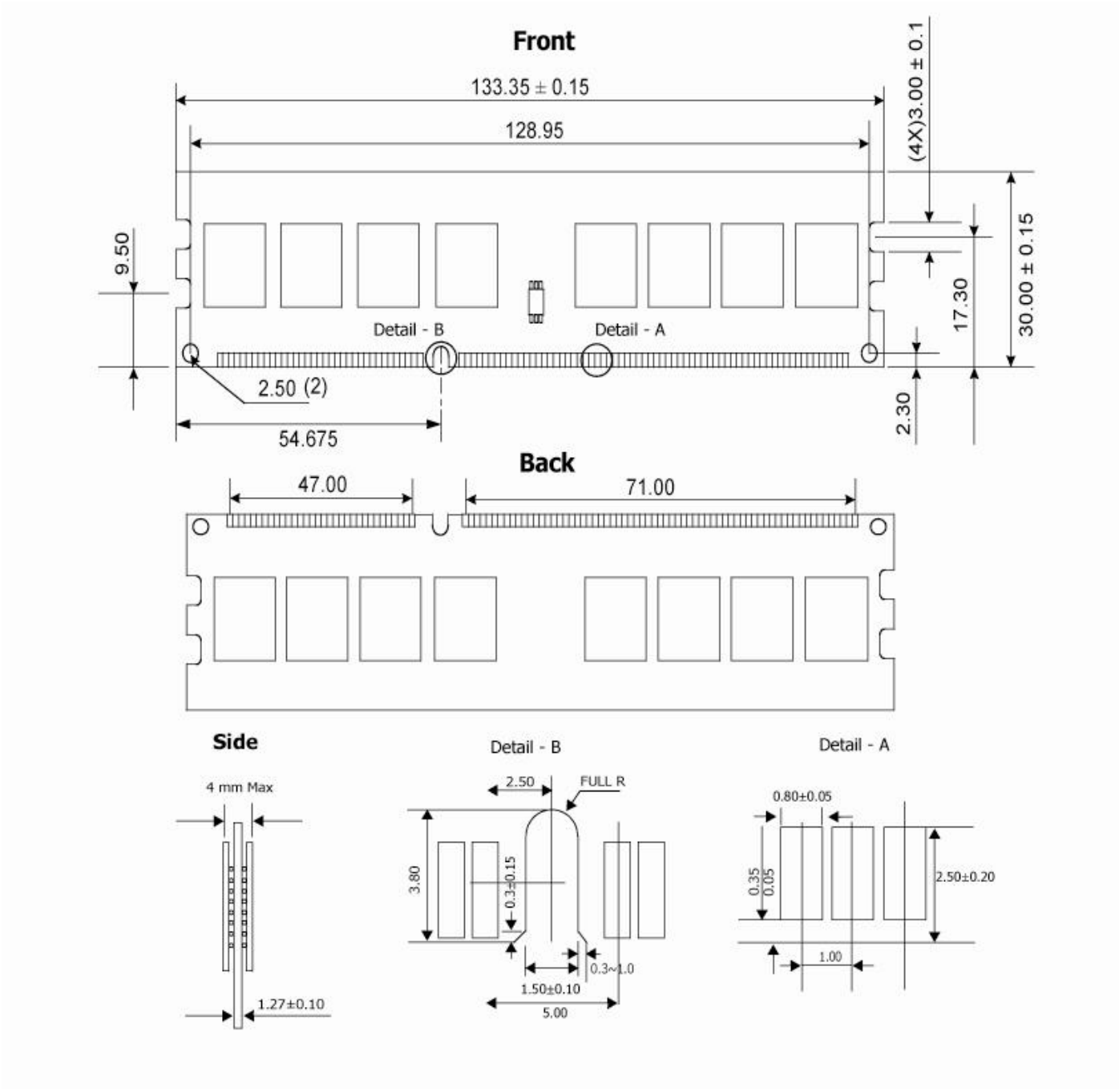
- Data rate: 1600Mbps
- RoHS compliant products.
- 240pin, dual in-line memory module(DIMM)
- VDD = 1.5V (1.425V to 1.575V)
- Interface: SSTL_15
- Programmable CAS Latency(CL): 6,7,8,9,10,11 support
- Fully differential clock inputs (CK, /CK) operation
- Differential Data Strobe (DQS, /DQS)
- Serial presence detect with EEPROM
- 8 independent internal bank
- 8K refresh cycles /64ms
- On Die Termination supported
- Asynchronous RESET pin supported
- ZQ calibration supported
- POSTED CAS additive latency (AL)
- Bi-directional Differential Data Strobe
- Burst Length: 4, 8
- 8 bit pre-fetch
- TCASE of 0°C to 95°C (Components)
  -- 64ms, 8,192 cycle refresh at 0°C to 85°C
  -- 32ms at 85°C to 95°C

Speed Grade

Frequency Grade	Data Transfer Rate	CAS Latency Support						CL-tRCD-tRP
		CL6	CL7	CL8	CL9	CL10	CL11	
DDR3-1600	PC3-12800	800	1066	1066	1333	1333	1600	11-11-11

Package Dimensions

Unit: mm



Tolerances : ± 0.15mm unless otherwise specified