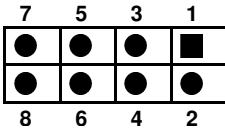


PXE-19S2 –R10

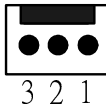
19-Slot PICMG 1.3 PCIe to PCI Bridge Backplane User Manual Ver. 1.0

1. USB1, USB2: External 4 Port USB Connectors From the Mainboard



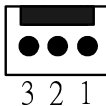
PIN	DESCRIPTION	PIN	DESCRIPTION
1	USBVCC	2	GND
3	DATA-	4	DATA+
5	DATA+	6	DATA-
7	GND	8	USBVCC

2. FAN1~FAN5: Fan Connector



PIN	DESCRIPTION
1	GND
2	+12V
3	NC

3. ATXCTL1: Backplane to Mainboard ATX Power Control Connector



PIN	DESCRIPTION
1	GND
2	PSON#
3	5VSB

4. PWRBTN: External Power On Button



PIN	DESCRIPTION
1	PWRBT#
2	GND

5. RESET: Reset Button Connector



PIN	DESCRIPTION
1	GND
2	SHB_RST#

6. PWRGD: External Power Good Connector



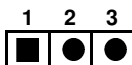
PIN	DESCRIPTION
1	GND
2	PWRGD

7. SM-BUS: External System Management BUS Connector



PIN	DESCRIPTION
1	SMDATA
2	SMBCLK

8. CN1: PCIE X16 SDVO Control Signal Pin



PIN	DESCRIPTION
1	EXP_EN
2	SDVO_CLK
3	SDVO_DAT

9. J1, J2, J3: Write Protect Setup

J1、J2 and J3 are the EEPROM write protect pin of the PCI Express to PCI Bridge will allow EEPROM normal read/write operations when J1、J2 or J3 held high. When the Jump is brought low and the EEPROM WPEN bit is “1”, all write operations to the status register are inhibited.



J1、J2、J3	DESCRIPTION
ON (Short)	Write Inhibited
OFF (Open)	Normal Read/Write (default)

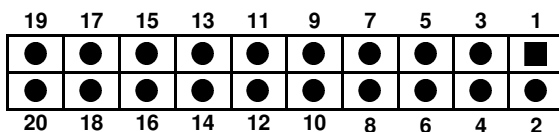
10. J4、J5、J6 : PCI Slot Freq. Setting



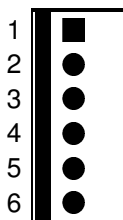
J4、J5、J6	DESCRIPTION
ON (Short)	33MHz
OFF (Open)	Auto (default)

11. IOPWR: The I/O Power Connector indicators for controlling your CPU board

PIN	DESCRIPTION	PIN	DESCRIPTION
1	GND	2	-12V
3	+12V	4	GND
5	IPMB_DA	6	5VSB
7	GND	8	GND
9	IPMB_CL	10	5VSB
11	+5V	12	PWRGD
13	SMDATA	14	SHB_RST#
15	5VSB	16	GND
17	SMBCLK	18	PSOEN#
19	+3.3V	20	PWRBT#

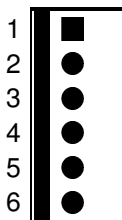


12. PWR1, PWR2: AT +5V Power Connector



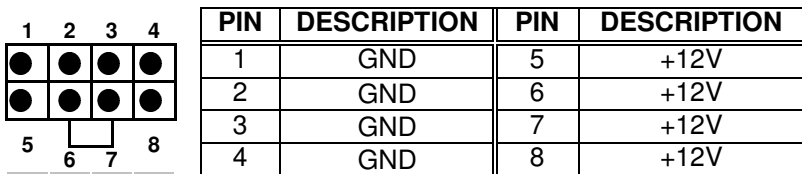
PIN	DESCRIPTION
1	+5V
2	+5V
3	+5V
4	GND
5	GND
6	GND

13. PWR3, PWR4: AT +3.3V Power Connector



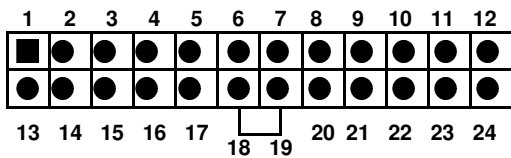
PIN	DESCRIPTION
1	+3.3V
2	+3.3V
3	+3.3V
4	GND
5	GND
6	GND

14. 12V1 : +12V POWER Connector



15. PWR1 : ATX Power Connector

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	+3.3V	13	+3.3V
2	+3.3V	14	-12V
3	GND	15	GND
4	+5V	16	PSON#
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	PWRGD	20	NC
9	+5VSB	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	+3.3V	24	GND



16. PCI routing table:

PICMG1.3 Local PCI Routing

SIGNAL	REQ#	GNT#	IDSEL	INTA#	INTB#	INTC#	INTD#
PIN No.	B18	A17	A26	A6	B7	A7	B8
PCI1	REQ0#	GNT0#	AD31	B	C	D	A
PCI2	REQ1#	GNT1#	AD30	C	D	A	B
PCI3	REQ2#	GNT2#	AD29	D	A	B	C
PCI4	REQ3#	GNT3#	AD28	A	B	C	D

PCI Express to PCI Group A - C Routing

SIGNAL	REQ#	GNT#	IDSEL	INTA#	INTB#	INTC#	INTD#
PIN No.	B18	A17	A26	A6	B7	A7	B8
PCI_A1 & PCI_B1 & PCI_C1	REQ0#	GNT0#	AD16	A	B	C	D
PCI_A2 & PCI_B2 & PCI_C2	REQ1#	GNT1#	AD17	B	C	D	A
PCI_A3 & PCI_B3 & PCI_C3	REQ2#	GNT2#	AD18	C	D	A	B
PCI_A4 & PCI_B4 & PCI_C4	REQ3#	GNT3#	AD19	D	A	B	C

17. Notice:

- a. Recommend to use 6mm width spacer to install
- b. Torsion must less than 6lb-inch
- c. PCIEX1_1 (PCIeX4 Slot) only support PCIeX1 signal

